

EXIT FOR BOOT
RUN CMOS SETUP
RUN XCMOS SETUP

2024/8/3 07:57

CMOS SETUP (C) Copyright 1985-1989, American Megatrends Inc.,

Date (mm/date/year): Tue, Jan 01 1980
Time (hour/min/sec): 00 : 05 : 43
Floppy drive A: : Not Installed
Floppy drive B: : Not Installed

Base memory size : 640 KB
Ext. memory size : 0 KB
Numeric processor : Not Installed

Hard disk C: type : Not Installed
Hard disk D: type : Not Installed
Primary display : Not Installed
Keyboard : Not Installed

Cyln Head WPCom LZone Sec Size

Scratch RAM option : 1

Sun	Mon	Tue	Wed	Thu	Fri	Sat
30	31	1	2	3	4	5
6	7	8	9	10	11	12
13	14	15	16	17	18	19
20	21	22	23	24	25	26
27	28	29	30	31	1	2
3	4	5	6	7	8	9

Month : Jan, Feb, ..., Dec
Date : 01, 02, 03, ..., 31
Year : 1901, 1902, ..., 2099

ESC = Exit, ↓ → ↑ ← = Select, PgUp/PgDn = Modify

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EXTENDED CMOS SETUP PROGRAM Ver - 1.52 ,(C)1988, American Megatrends Inc.

C&T 386 CHIPSET SETUP PROGRAM
MAIN MENU

EASY C&T 386 CHIPSET REGISTER SETUP
206,301 REGISTERS SET UP
307 REGISTER SET UP
ENABLE/DISABLE VIDEO AND MAIN BIOS SHADOW
WRITE CMOS REGISTERS AND EXIT
DO NOT WRITE CMOS REGISTERS AND EXIT

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WARNING--IMPROPER USE OF SETUP MAY CAUSE THE SYSTEM TO FAIL NORMAL OPERATIONS!

IF THE SYSTEM FAILS, PRESS AND HOLD THE <INS> KEY AND TURN THE MACHINE OFF AND THEN ON!

RELEASE THE <INS> KEY AFTER MEMORY TEST STARTS!

H I T <ESC> T O S T O P N O W!

H I T <ENTER> T O C O N T I N U E!

Memory Configuration			
Block	No. of banks	DRAM Type	Waitstate
0	1 BANK	256K	2 WAIT STATE
1	NONE		2 WAIT STATE
2	NONE		2 WAIT STATE
3	NONE		2 WAIT STATE

Clock Source		
Processor Clock	Bus Clock	DMA Clock
PROCESSOR OSCILLATOR	ATCLK	SCLK/2

Shadow Ram\Pipeline			
BIOS Shadow	Video Shadow	C4000H,16K	Pipeline Mode
F0000H,64K	C0000H,16K	C4000H,16K	DISABLED
DISABLED	DISABLED	DISABLED	DISABLED

TWO WAIT STATE
THREE WAIT STATE
FOUR WAIT STATE

MOVE BAR-<PgUp/PgDn>
CHANGE WINDOWS -↑↓-
EXIT-<ESC>

BITS 7 - 0	
82C206 01H ->	11 00 00 0 0
82C301 04H ->	RRR 0 0 0 RR
05H ->	00 00 01 01
06H ->	01 01 01 11

Go to Prev/Next Register -↑↓
Go to Prev/Next Entry - →←
Scroll Bit value - PgUp/PgDn
Return to MAIN MENU - <ESC>

CLOCK AND WAIT STATE CONTROL

XIOR/XIOW WAIT STATES
00=1 I/O WAIT STATE
01=2 I/O WAIT STATE
10=3 I/O WAIT STATE
11=4 I/O WAIT STATE

BITS 7 - 0

```

82C307  08H -> RRR  0 0 0 1 1
        09H -> 0 0 0 0 0 0 0 1
        0AH -> 0 0 0 0 0 0 0 0
        0BH -> 0 0 0 0 0 0 0 0
        0CH -> 0 0 0 0 0 0 0 0
        0DH -> 1 1 1 1 1 1 1 1
        0EH -> 1 1 1 1 1 1 1 1
        0FH -> 1 1 1 1 1 1 1 1
        10H -> 01  0000  00
        11H -> 00  R 00  00 R
        12H -> 00  0000  00
        13H -> 00  R 00  RRR
        14H -> 00  0000  00
        15H -> 00  R 00  RRR
        16H -> 00  0000  00
        17H -> 00  R 00  RRR
        20H -> 1 0 RR  0  RRR
        28H -> 0      RRRRRR
        2AH -> RRRRRR  1

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Go to Prev/Next Register -↑↓
 Go to Prev/Next Entry - ←→
 Scroll Bit value - PgUp/PgDn
 Return to MAIN MENU - <ESC>

IDENTIFICATION

MIDDLE BOOT WRITE PROTECT
 0=R/W RAM AT 16128K
 1=R/O RAM AT 16128K

SETUP SHADOW RAM FOR 212

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MAIN  BIOS SHADOW AT F0000H,64K -> 0
VIDEO BIOS SHADOW AT C0000H,16K -> 0
VIDEO BIOS SHADOW AT C4000H,16K -> 0

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Go to Prev/Next Register -↑↓
 Go to Prev/Next Entry - ←→
 Scroll Bit value - PgUp/PgDn
 Return to MAIN MENU - <ESC>

MAIN BIOS SHADOW AT F0000H,64K

1=SHADOW ENABLE
 0=SHADOW DISABLE