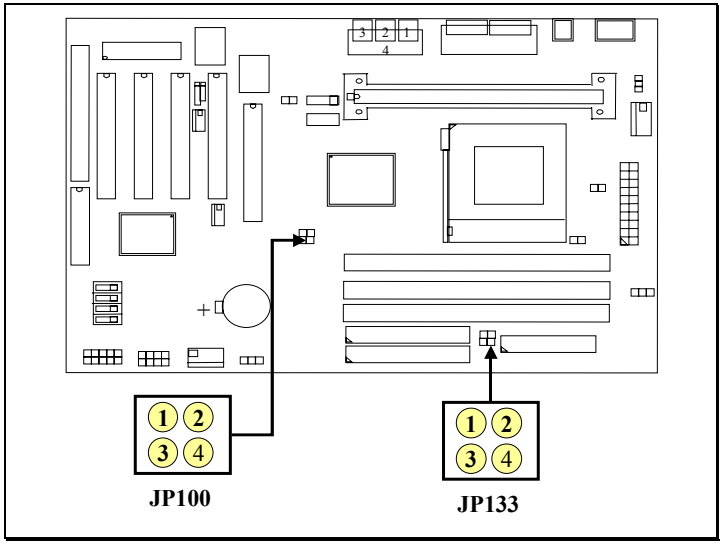
IMPORTANT UPDATE INFORMATION

JP100 / JP133: Over Clock Selection

This mainboard is designed by using VIA 693A chipset and it supports up to 133MHz FSB clock. The jumper setting of JP100/133 mentioned in the user's manual page 3-19 was modified as following:



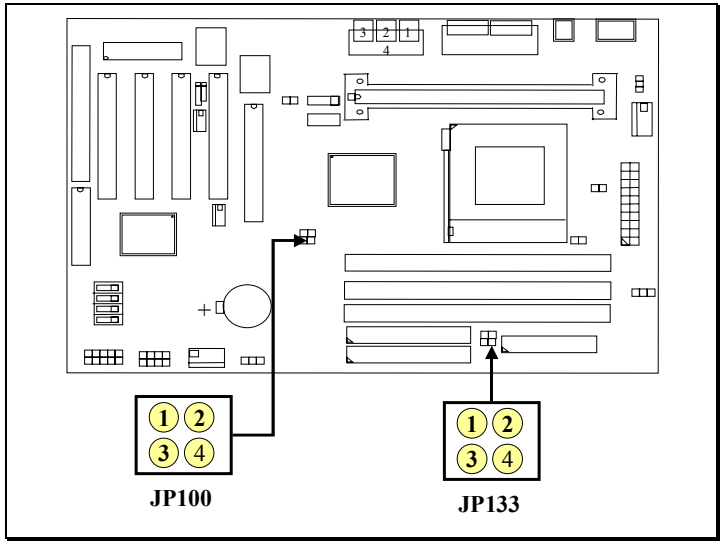
FSB CLOCK	JP 100		JP133	
	Pin1 - Pin2	Pin3 - Pin4	Pin1 - Pin2	Pin3 - Pin4
66MHz	Open	Short	Open	Short
100MHz	Open	Open	Open	Short
133MHz	Open	Open	Open	Open

DOC NO. UM-DVBX-L1

IMPORTANT UPDATE INFORMATION

JP100 / JP133: Over Clock Selection

This mainboard is designed by using VIA 693A chipset and it supports up to 133MHz FSB clock. The jumper setting of JP100/133 mentioned in the user's manual page 3-19 was modified as following:



FSB CLOCK	JP 100		JP133	
	Pin1 - Pin2	Pin3 - Pin4	Pin1 - Pin2	Pin3 - Pin4
66MHz	Open	Short	Open	Short
100MHz	Open	Open	Open	Short
133MHz	Open	Open	Open	Open

DOC NO. UM-DVBX-L1

1. PT-DVBX2 □□□□□□□
2. PT-DVAX2□□UM-DVBX-E1□□□□□□□

3. □□□□UM-DVBX-E2, □□□□□□□□□