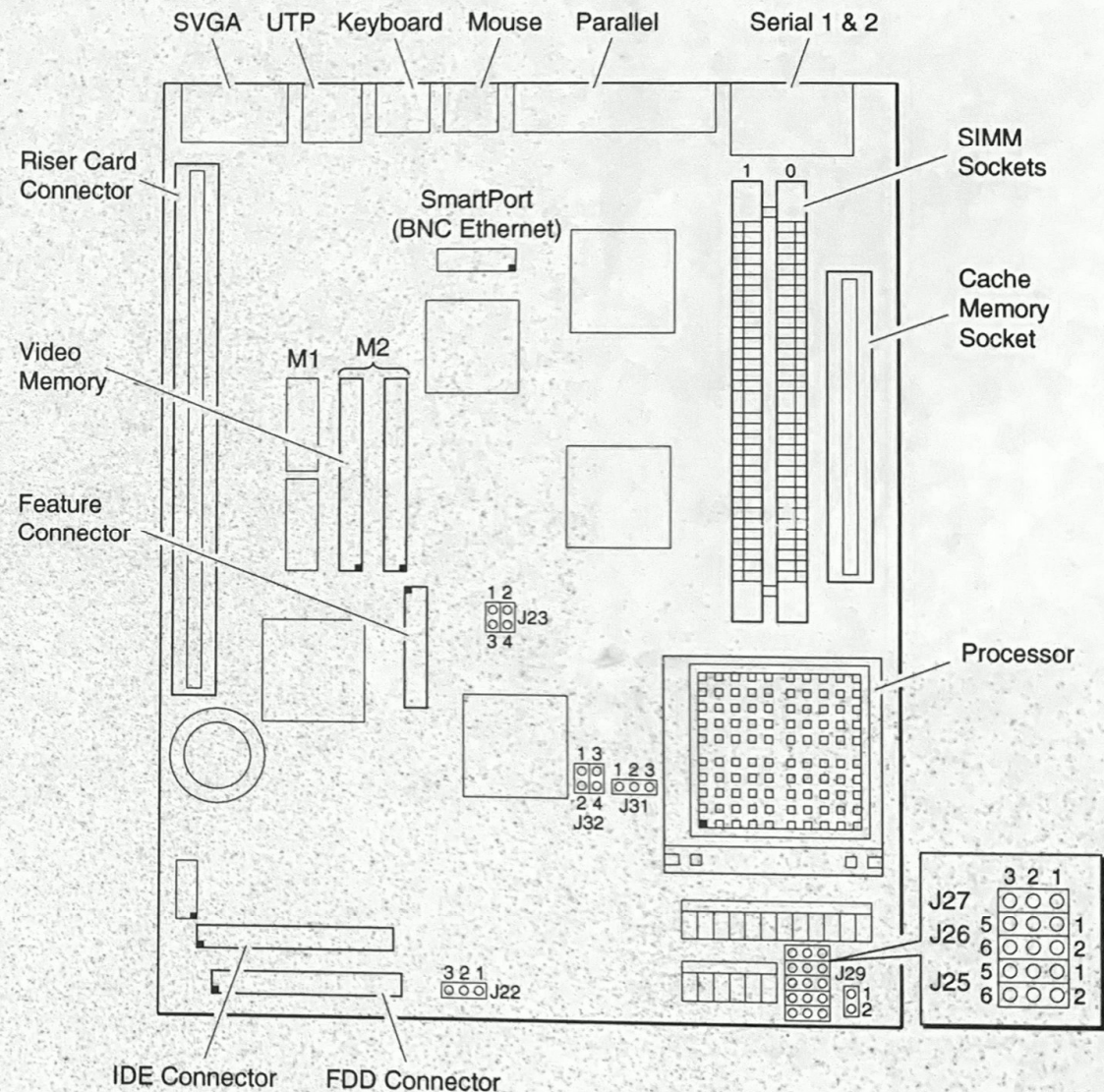


CWP System Board Configuration

Function	Jumper: Pins	Setting
Reference Only 33 MHz PGA CPU Internal Cache Write Through	J22:1-2 On, J23:1-3 On, J25:1-3 On, J25:2-4 On, J26:1-3 On, J26:2-4 On, J27:1-2 On, J29:1-2 Off, J31:1-2 On, J32:1-2 Off, J32:3-4 Off	☐ ☐ ☐ ☐ ☐ ☐
PRC450-DX2 50 MHz PGA CPU Internal Cache Write Through	J22:1-2 On, J23:2-4 On, J25:1-3 On, J25:2-4 On, J26:1-3 On, J26:2-4 On, J27:1-2 On, J29:1-2 Off, J31:1-2 On, J32:1-2 Off, J32:3-4 Off	☐ ☐ ☐ ☐ ☐ ☐
PRC466-DX2 66 MHz PGA CPU ^B Internal Cache Write Through	J22:1-2 On, J23:1-3 On, J25:1-3 On, J25:2-4 On, J26:1-3 On, J26:2-4 On, J27:1-2 On, J29:1-2 Off, J31:1-2 On, J32:1-2 Off, J32:3-4 Off	☐ ☐ ☐ ☐ ☐ ☐
PRC4100-DX4 100 MHz PGA CPU Internal Cache Write Through	J22:1-2 On, J23:1-3 On, J25:3-5 On, J25:4-6 On, J26:3-5 On, J26:4-6 On, J27:2-3 On, J29:1-2 Off, J31:1-2 On, J32:1-2 Off, J32:3-4 Off	☐ ☐ ☐ ☐ ☐ ☐

^B = Base



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