San Li SL-586VT3

Rev. 1.0 Made in Taiwan.

JBAT1	
1-2	Retain CMOS data (default)
2-3	Clear CMOS data

JROM1	
1-2	5V Flash ROM
2-3	12V Flash ROM

JPCI1	
1-2	Normal PCI card (default)
2-3	ISA reset PCI add-on cards

JD	
1-2	3.3V SDRAM
2-3	5V SDRAM

JBS	
open	For non-Cyrix CPU's
closed	For Cyrix CPU's: Set the "linear burst" option in the BIOS under "Chipset features" and close this jumper.

Clock	JCK1	JCK2	JCK3
50 MHz	2-3	2-3	2-3
55 MHz	2-3	2-3	1-2
60 MHz	1-2	2-3	2-3
66 MHz	2-3	1-2	2-3
75 MHz	1-2	2-3	1-2

JCK4	
1-2	synchronous PCI bus (clock / 2)
2-3	asynchronous PCI bus (33MHz) - especially for 75MHz clock

CPU	JV1	JV2
Single supply: Vcore = V_IO and selected by JPW1	off	1-2, 3-4
Dual supply: V_iO = 3.3V, Vcore selected by JPW1	1-2, 3-4	off

Vcore	JPW1
3.3V	1-2
3.5V	3-4
2.8V	5-6
3.2V	7-8
2.2V	1-2, 7-8

Multiplier	JBF1	JBF2	JBF3
1.5	open	open	open
2	**close**	open	open
2.5	**close**	**close**	open
3	open	**close**	open
3.5	open	open	open
4	**close**	open	**close**
4.5	**close**	**close**	**close**

USB 1.0 connectors CN3 and CN4:

Pin	USB- Signal
1	+5V
2	Data(-)
3	Data(+)
4	Ground

A standard 2 port USB bracket can be used (pin 9 and 10 haven't a socket-pin then).

Additional hints:

128MB single side and 256MB double side are the highest memory capacity which I tested successfully.

User of a Cyrix CPU should read the comment at jumper JBS. I tested successfully a Cyrix MII 366GP (2.5x 100MHz) with 66MHz bus clock and a factor 3.5 (233MHz core clock) with the latest bios version VER:C.2 12/02/97.

An AMD K6-2 processor starts but can't boot. My fastest AMD-CPU on this board: AMD K6/300.