

# R582E / R582V Jumper Setting

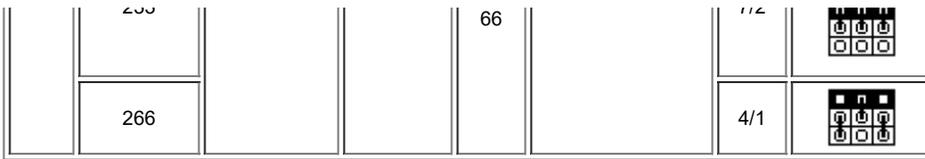
AMD k6-2/3 400MHz = 100MHz x 4 , k6-2/3 450MHz = 100MHz x 4.5

AMD k6-2/3 500MHz = 100MHz x 5 ( CPU voltage mark request on CPU surface).

## R-582E/V CPU Jumper Setting Quick Reference

CPU type		CPU Power Voltage			System Clock		CPU Speed	
		I/O	Core	VID	MHz	JP1,JP2,JP3,JP4	Ratio	JP8,JP9,JP10
Intel	P55C(MMX) -166	3.4v	2.8v		66		5/2	
	P55C(MMX) -200						3/1	
	P55C(MMX) -233						7/2	
Cyrix & IBM	6x86 -PR166	3.5v			66		2/1	
	6x86 -PR200				75			
IBM	6x86L -PR166	3.4v	2.9v		66		2/1	
	6x86L -PR200				75			
Cyrix & IBM 6x86 MX	PR200	3.4v	2.9v		66		5/2	
	PR233				83		2/1	
	PR233				75		5/2	
	PR233				66		3/1	
	PR233				100		2/1	
	PR266				83		5/2	
	PR300				75		3/1	
	PR300				66		7/2	
Cyrix M II & IBM 6x86 MX	PR233	3.4v	2.9v		83		2/1	
	PR233				75		5/2	
	PR233				66		3/1	
	PR233				100		2/1	

								
	PR266				83		5/2	
	PR300				75		3/1	
	PR300				66		7/2	
	PR333				100		5/2	
	PR333				83		3/1	
	PR333				75		7/2	
	PR333				66		4/1	
	PR350				100		3/1	
Cyril M II & IBM 6x86 MX	PR350	3.4v	2.9v		83		7/2	
	PR350				75		4/1	
AMD 5k86	PR133	3.4v			66		2/1	
	PR166						5/2	
	PR200						3/1	
AMD K6	166	3.4v	2.9v		66		5/2	
	200						3/1	
	233						7/2	
	266	3.4v	3.2v		66		4/1	
AMD K6-2	250	3.4v	2.2v		100		5/2	
	266				66		4/1	
	300				100		3/1	
	333				95		7/2	
	350				100		7/2	
IDT C6	200	3.4v					3/1	
	233						7/2	



**CPU Speed Configuration:**

Freq ratio	JP10,JP9,JP8	System freq (MHz)	PCI	AGP	JP4,JP3,JP2,JP1
1.5/3.5		66	33	66	
2.0		68	34	68	
2.5		75	37	75	
3.0		83	27	55	
4.0		95	31.5	63	
4.5		100	33	66	
5.0		112	37	74	
5.5					

CPU Speed=(Frequency ratio) x (System Frequency).

PCI has a maximum bandwidth of 33MB---one half of the 66MHz

System Frequency. The 75MHz System Frequency are not supported by the current PCI Rev. 2.1 Specification.

**CPU Voltage Configuration:**

CPU Power Voltage	VID	CPU Power Voltage	VID
2.1V	1-2 short	2.9V	1-2 & 7-8 short
2.2V	3-4 short	3.0V	3-4 & 7-8 short
2.3V	1-2 & 3-4 short	3.1V	1-2 & 3-4 & 7-8 short
2.4V	5-6 short	3.2V	5-6 & 7-8 short
2.5V	1-2 & 5-6 short	3.3V	1-2 & 5-6 & 7-8 short
2.6V	3-4 & 5-6 short	3.4V	3-4 & 5-6 & 7-8 short
2.7V	1-2 & 3-4 & 5-6 short	3.5V	1-2 & 3-4

			& 5-6 & 7-8 short
2.8V	7-8 short		

## SDRAM CLOCK

JP7	JP6	SDRAM
2-3	2-3	SDRAM CLK=CPU CLK (default)
1-2	1-2	SDRAM CLK=AGP CLK

## CMOS Clear Jumper : JP12

Function	JP12 Setting
(a) Normal	1-2 short
(b) Clear CMOS	2-3 short