

R534 Jumper Setting

R-534E CPU Jumper Setting Quick Reference

CPU type		CPU Power Voltage				System Clock		CPU Speed	
		I/O	Core	JP13	JP26	MHz	JP10,JP11,JP12	Ratio	JP18,JP19
Intel	P54C-75	3.4		1  5	3  4	50		3/2	
	P54C-90					60		3/2	
	P54C-120						2/1		
	P54C-150	3.5		1  5	1  2			5/2	
	P54C-180						3/1		
	P54C-100	3.4		1  5	3  4	66		3/2	
	P54C-133							2/1	
	P54C-166							5/2	
	P54C-200	3.5		1  5	1  2			3/1	
	P55C-200					3.4	2.8	1  5	5  6
Cyrix 6x86	6x86-P120	3.5		1  5	1  2	50		2/1	
	@100MHz								
	6x86-P133					55			
	@110MHz								
	6x86-P150					60			
	@120MHz								
	6x86-P166	66							
	@133MHz								
	6x86-P200	75							
@150MHz									
6x86L-P166.	3.4	2.8	1  5	5  6	66				
@133MHz									
6x86L-P200.					75				
@150MHz									
AMD 5K86	P75	3.4		1  5	3  4	50		3/2	
	P90					60			
	P100					66			

CPU Voltage Selector :

1. For Single Power CPU : Intel P54C, Cyrix 6x86, IBM 6x86, AMD 5k86.

Step1 : JP13 : 1-2 Close, 3-4 Close, 5-6 Close.

Step2 : JP28 : 1-2 Close.

Step3 : JP27

VOLT 1.	JP27
3.3V	1-2 : Close. 3-4 : Close.
3.4V	1-2 : Close. 3-4 : Open.
3.5V	1-2 : Open. 3-4 : Open

2. For Dual Power CPU : Intel P55C(MMX), Cyrix 6x86L, IBM 6x86L.

Step1 : JP13 : 1-2 Open, 3-4 Open, 5-6 Open.

Step2 : JP28 : 2-3 Close.

Step3 : JP27, JP26 :

VOLT 1.	JP27(I/O)	VOLT 2.	JP26(Core)
3.3V	1-2:Close. 3-4:Close.	2.4V	1-2:Close. 3-4:Close.
3.45V	1-2:Close. 3-4:Open.	2.7V	1-2:Close. 3-4:Open.
3.5V	1-2:Open. 3-4:Open.	3.8V	1-2:Open. 3-4:Open.

CMOS Clear Jumper : JP25.

Function	JP25
Normal Operation (default)	1-2 Close
Clear CMOS data	2-3 Close

Flash EPROM Selector : JP23, JP24.

Program Mode	JP23	JP24
None Flash Mode	1-2	1-2
+5V Flash ROM	1-2	2-3
+12V Flash ROM	2-3	2-3

Cache Size & SRAM Locations

Cache Size	Tag RAM	On Board SRAM Chip	256K Cache Module
(a) 256KB	32K8 x 1	32K32 x 2 pcs	None
(b) 512KB	32K8 x 1	32K32 x 2 pcs	1 pcs
(c) 512KB	32K8 x 1	64K32 x 2 pcs	None

Cache RAM Access Mode Selector : JP5.

JP5 :

Close --- For Cyrix 6x86, IBM 6x86 Linear Mode.(When you select Linear Mode, you also must setting the Linear Mode in the BIOS SETUP Item.)

Open --- For CPU Toggle Mode.(Intel P54C & P55C CPU only support Toggle mode.)

Memory Modules Voltage Selector : JP7, JP8.

72-pin SIMM or 168-pin DIMM Memory Selector.

Memory Voltage	JP7	JP8	Memory Type
(a)3.3 V	2-3 Close	2-3 Close	168-pin SDRAM DIMM
	5-6 Close	5-6 Close	
(b)5V (default)	1-2 Close	1-2 Close	72-pin SIMM or
	4-5 Close	4-5 Close	168-pin DIMM DRAM

R534 Using 72-pin SIMMs and 168-pin DIMMs DRAM Table :

Total DRAM Size	BANK .A		BANK .B		DIMM	
	SIM 1	SIM 2	SIM 3	SIM 4	M1	M 2
8 MB	4 MB	4 MB	-	-	-	-
16 MB	4 MB	4 MB	4 MB	4 MB	-	-
24 MB	4 MB	4 MB	8 MB	8 MB	-	-
40 MB	4 MB	4 MB	16 MB	16 MB	-	-
72 MB	4 MB	4 MB	32 MB	32 MB	-	-
16 MB	8 MB	8 MB	-	-	-	-
24 MB	8 MB	8 MB	4 MB	4 MB	-	-
32 MB	8 MB	8 MB	8 MB	8 MB	-	-
48 MB	8 MB	8 MB	16 MB	16 MB	-	-
80 MB	8 MB	8 MB	32 MB	32 MB	-	-
32 MB	16 MB	16 MB	-	-	-	-
40 MB	16 MB	16 MB	4 MB	4 MB	-	-
48 MB	16 MB	16 MB	8 MB	8 MB	-	-
64 MB	16 MB	16 MB	16 MB	16 MB	-	-
96 MB	16 MB	16 MB	32 MB	32 MB	-	-
64 MB	32 MB	32 MB	-	-	-	-
72 MB	32 MB	32 MB	4 MB	4 MB	-	-
80 MB	32 MB	32 MB	8 MB	8 MB	-	-
96 MB	32 MB	32 MB	16 MB	16 MB	-	-

128 MB	32 MB	32 MB	32 MB	32 MB	-	-
16 MB	-	-	-	-	16 MB	-
32 MB	-	-	-	-	16 MB	16 MB
48 MB	-	-	-	-	16 MB	32 MB
32 MB	-	-	-	-	32 MB	-
64 MB	-	-	-	-	32 MB	32 MB
24 MB	4 MB	4 MB	-	-	16 MB	-
32 MB	8 MB	8 MB	-	-	16 MB	-
48 MB	16 MB	16 MB	-	-	16 MB	-
80 MB	32 MB	32 MB	-	-	16 MB	-
40 MB	4 MB	4 MB	-	-	32 MB	-
48 MB	8 MB	8 MB	-	-	32 MB	-
64 MB	16 MB	16 MB	-	-	32 MB	-
96 MB	32 MB	32 MB	-	-	32 MB	-

Note :

1. The 168-pin DIMMs DRAM are +5V(Voltage), Please refer to 5V DIMM.

2. “-“=Empty.