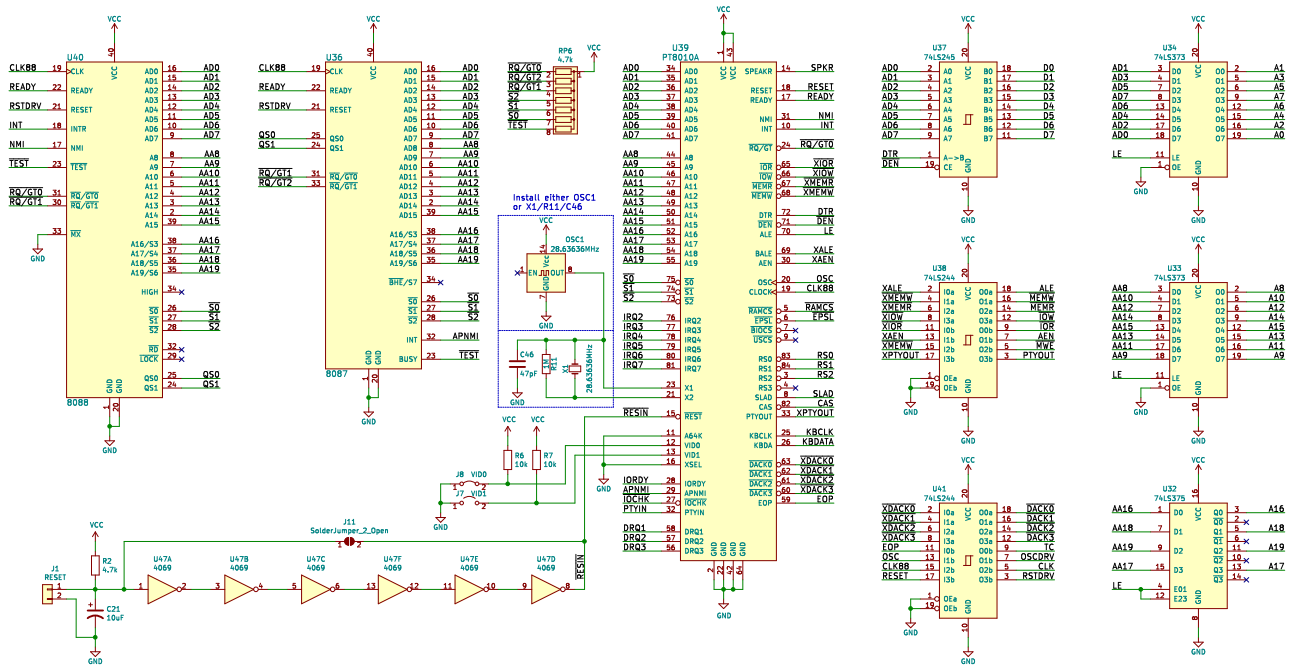
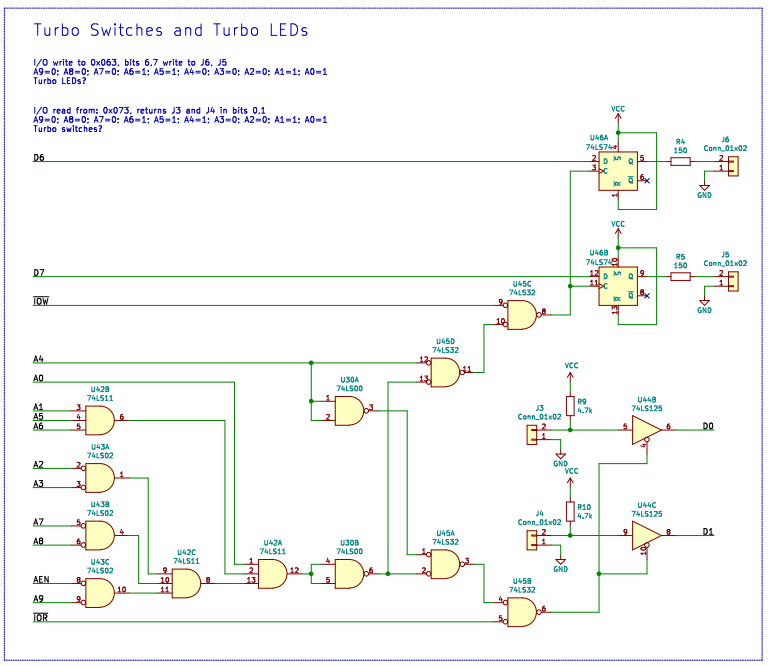
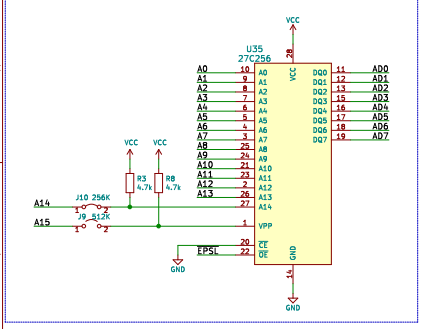
Processor, Coprocessor, Chipset, Buffers and Address Latches



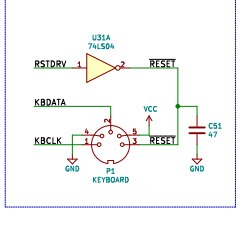
Turbo Switches and Turbo LEDs



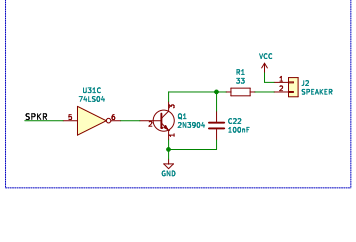
BIOS ROM



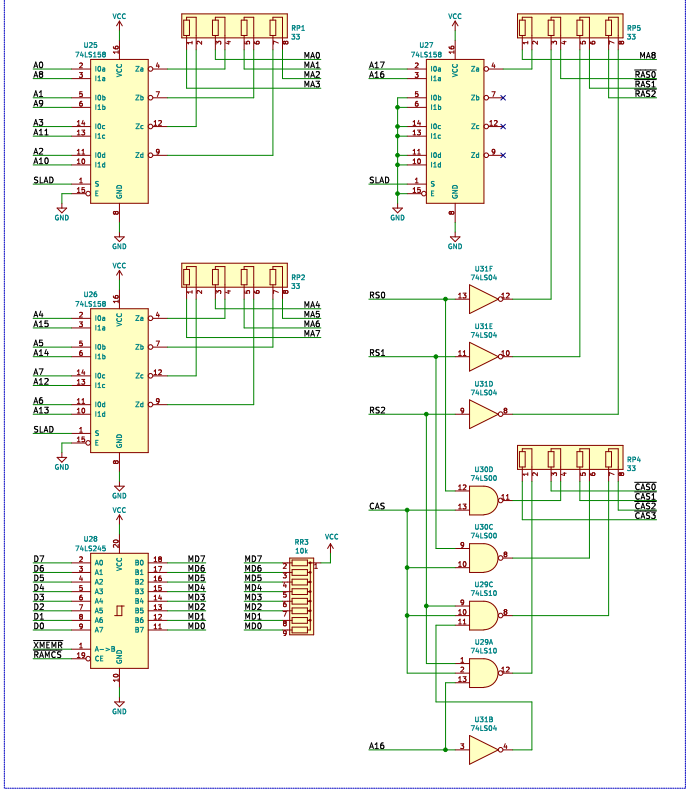
Keyboard



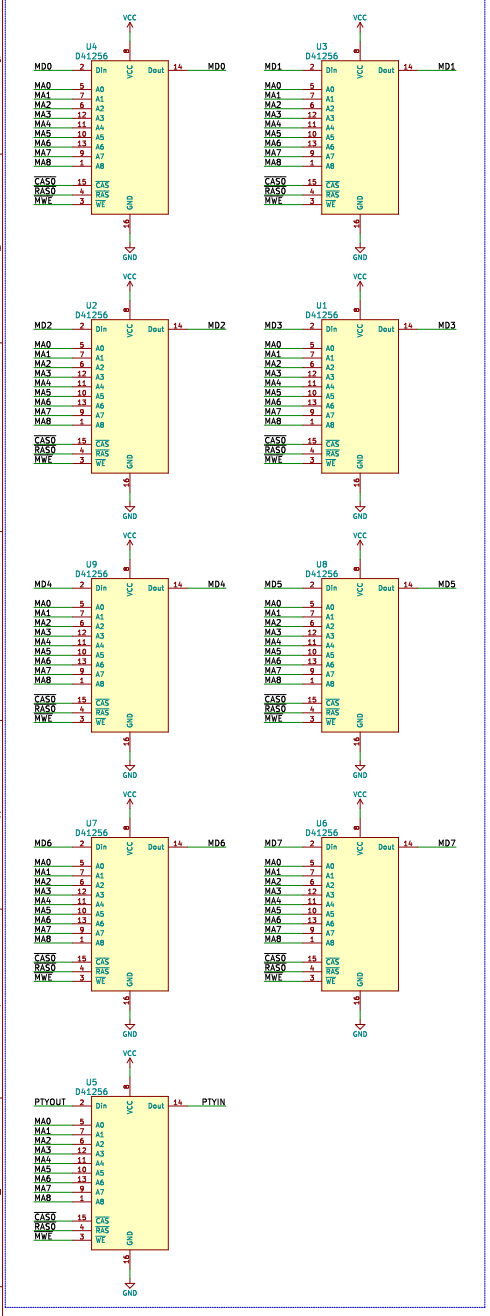
Speaker



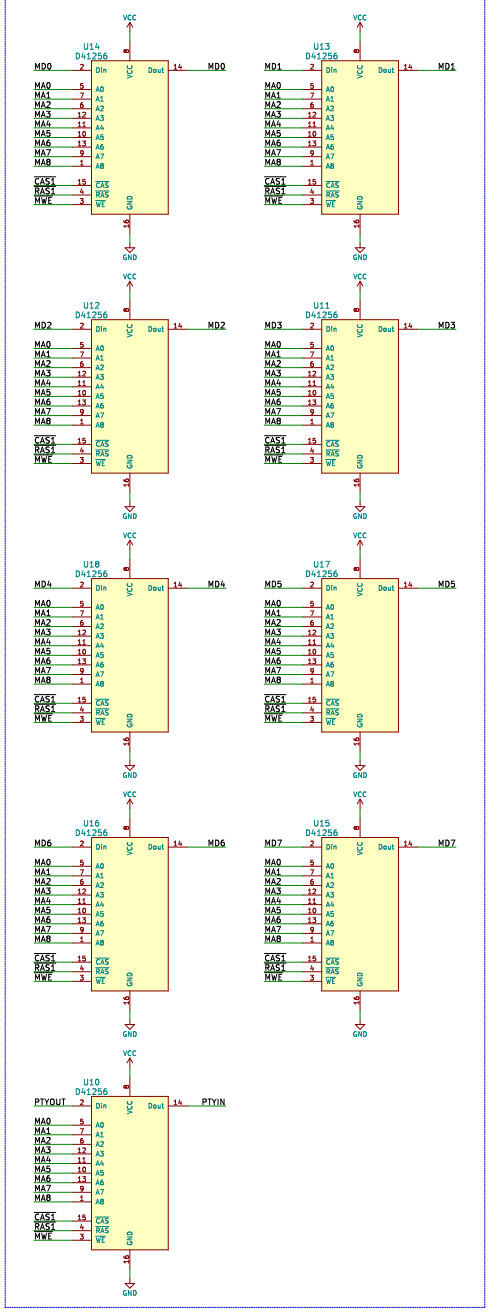
DRAM Multiplexers and Buffers



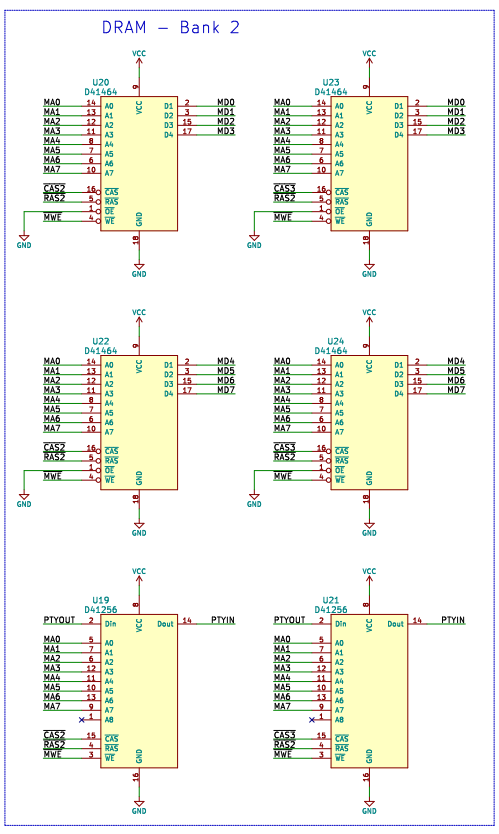
DRAM – Bank 0



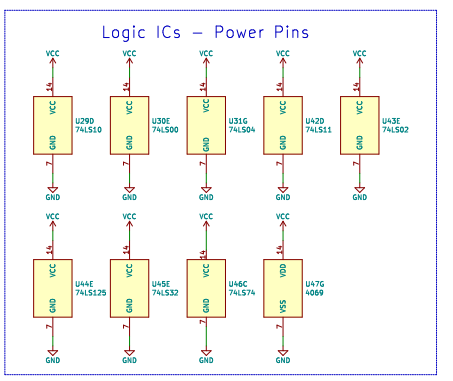
DRAM – Bank 1



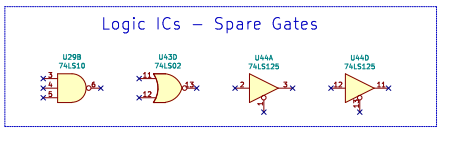
DRAM – Bank 2



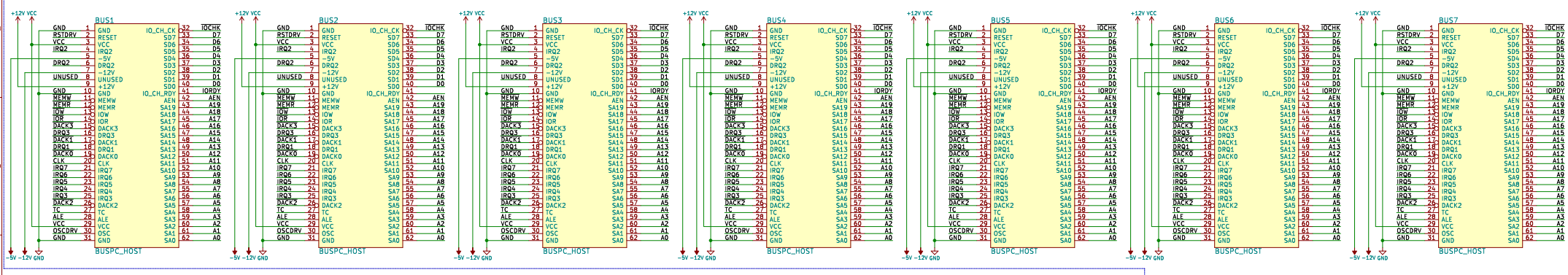
Logic ICs – Power Pins



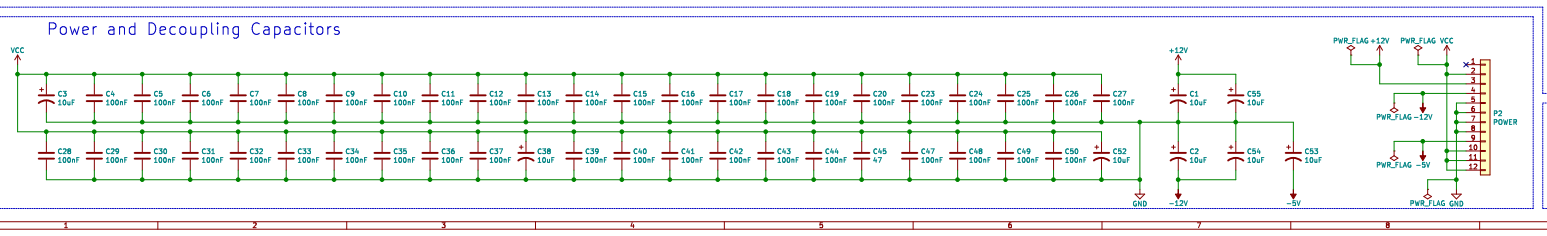
Logic ICs – Spare Gates

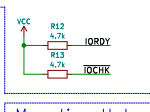


ISA Slots



Power and Decoupling Capacitors





Mounting Holes

H1 M4 H2 M4
H3 M4 H4 M4
H5 M4 H6 M4

Proton Turbo XT is an open source hardware project
Documentation and design files: https://github.com/sklselev/Proton_Turbo_XT
Licensed under CERN-OHL-S: https://cern-ohl.web.cern.ch

Reverse-engineered by Sergey Kiselev

Sheet: /
File: Proton_Turbo_XT.sch

Title: Proton PT8010A Based Turbo XT Motherboard

Size: A1	Date: 2021-11-09	Rev: 1.0
KiCad E.D.A. kicad (5.1.8)-1		Id: 1/1