

DTK Computers Edition 3.00

Technical Information Sheet for PKM0038S E2-A  
S=shorted, 0=open

CPU Type Selection

486SX

JP8	2-3	JP12	OPEN	JP21	2-3	JP30	OPEN
JP9	OPEN	JP13	OPEN	JP22	1-2	JP31	OPEN
JP10	2-3	JP14	OPEN	JP24	OPEN		
JP11	OPEN	JP15	OPEN	JP25	OPEN		

486DX

JP8	1-2	JP12	OPEN	JP21	2-3	JP30	OPEN
JP9	CLOSED	JP13	OPEN	JP22	1-2	JP31	OPEN
JP10	2-3	JP14	OPEN	JP24	OPEN		
JP11	2-3	JP15	OPEN	JP25	OPEN		

P24T

JP8	1-2	JP12	3-4	JP21	1-2	JP30	OPEN
JP9	CLOSED	JP13	OPEN	JP22	1-2	JP31	OPEN
JP10	1-2	JP14	2-3	JP24	1-2		
JP11	1-2	JP15	1-2	JP25	1-2		

SLE486SX

JP8	2-3	JP12	3-4	JP21	2-3	JP30	OPEN
JP9	OPEN	JP13	1-2	JP22	1-2	JP31	OPEN
JP10	2-3	JP14	2-3	JP24	OPEN		
JP11	OPEN	JP15	4-5	JP25	OPEN		

SLE486DX2/DX

JP8	1-2	JP12	3-4	JP21	2-3	JP30	OPEN
JP9	CLOSED	JP13	1-2	JP22	1-2	JP31	OPEN
JP10	2-3	JP14	2-3	JP24	OPEN		
JP11	2-3	JP15	4-5	JP25	OPEN		

CYRIX M6

JP8	2-3	JP12	2-3,4-5	JP21	1-2	JP30	OPEN
JP9	CLOSED	JP13	OPEN	JP22	2-3	JP31	OPEN
JP10	2-3	JP14	1-2,3-4	JP24	2-3		
JP11	OPEN	JP15	2-3	JP25	2-3		

CYRIX M7

JP8	1-2	JP12	2-3	JP21	1-2	JP30	OPEN
JP9	CLOSED	JP13	OPEN	JP22	2-3	JP31	OPEN
JP10	2-3	JP14	1-2,3-4	JP24	2-3		
JP11	2-3	JP15	2-3	JP25	2-3		

P24D

JP8	1-2	JP12	1-2,3-4	JP21	2-3	JP30	CLOSED
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JP9 CLOSED JP13 2-3 JP22 1-2 JP31 CLOSED  
 JP10 1-2 JP14 2-3 JP24 OPEN  
 JP11 2-3N JP15 4-5 JP25 1-2

JP18 (For CX486S2 ONLY) JP18 = CLOSED

Connectors

J3:Turbo Switch Connector J4:Turbo LED  
 4-5 Normal (AT) Speed <1> + Anode  
 3-4 Turbo Mode (default) <2> - Cathode

CN2:Power Supply Connector  
 <1> Power Good <7> Ground  
 <2> +5VDC <8> Ground  
 <3> +12VDC <9> -5VDC  
 <4> -12VDC <10> +5VDC  
 <5> Ground <11> +5VDC  
 <6> Ground <12> +5VDC

JP20:CPU Clock Delay JP16:CMOS RAM Battery Setting  
 CLKIN & CPUCLK same phase 1-2 Normal operation 2-3  
 CLKIN delay for CPUCLK 2-3 Discharge CMOS 1-2

JP17:Monitor Setting JP26:Green PC Break Switch  
 closed EGA Break Function C  
 Open Mono, VGA/EGA Normal (default) 0

VL-BUS Identify Pins VL-BUS CLOCK SPEED  
 VL-BUS Wait State  
 0 WS 1-2 <= 33MHz 1-2  
 1 WS 2-3 > 33MHz 2-3

Cache Size Selection

Clock Frequency

Cache	JP1	JP2	JP3	JP4	MHz	20	25	33	40	50
128 KB	1-2	1-2	1-2,3-4	1-2	JP5	0	0	C	0	C
256 KB	2-3	2-3	2-3,4-5	1-2	JP6	0	C	C	C	0
512 KB	1-2	2-3	1-2,3-4	2-3	JP7	0	0	C	C	0
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Memory Installation

On-board memory is located in three banks 0, 1, and 2. Bank 0 consists of 4 30 pin SIMM sockets which must be used first. These 4 SIMMs must be the same capacity. Banks 1 and 2 are 72 pin SIMMs SIMM speed required for best performance is 70 ns.

BANK0	BANK1	BANK2	TOTAL	BANK0	BANK1	BANK2	TOTAL
256K	NONE	NONE	1MB	1M	4M	4M	36MB
256K	256K	NONE	2MB	1M	8M	NONE	36MB

256K	256K	512K	4MB	1M	1M	8M	40MB
1M	NONE	NONE	4MB	4M	8M	NONE	48MB
256K	1M	NONE	5MB	16M	NONE	NONE	64MB
256K	256K	1M	6MB	4M	4M	8M	64MB
1M	1M	NONE	8MB	256K	16M	NONE	65MB
1M	1M	1M	12MB	1M	16M	NONE	68MB
4M	NONE	NONE	16MB	1M	8M	8M	68MB
256K	4M	NONE	17MB	1M	1M	16M	72MB
256K	256K	4M	18MB	4M	16M	NONE	80MB
1M	4M	NONE	20MB	4M	8M	8M	80MB
1M	1M	4M	24MB	4M	4M	16M	96MB
4M	4M	NONE	32MB	16M	16M	NONE	128MB
256K	NONE	NONE	MB	1M	NONE	NONE	MB