

# Hardware JUMPER LIST

## Installation For PCI500

### P54C CPU SPEED

CPU	BUS	JP14	JP8	JP9	JP11
P75, K5-75	50	2-3, 5-6	3-4, 5-6	OFF	OFF
P90, K5-90	60	2-3, 5-6	5-6	OFF	OFF
P100, K5-100	66	2-3, 5-6	3-4	OFF	OFF
CX-120+	50	2-3, 5-6	3-4, 5-6	ON	OFF
CX-133+	55	2-3, 5-6	OPEN	ON	OFF
P120, K5-120, CX-150+	60	2-3, 5-6	5-6	ON	OFF
P133, K5-133, CX-166+	66	2-3, 5-6	3-4	ON	OFF
CX-200+	75	2-3, 5-6	1-2	ON	OFF
P150, K5-150	60	2-3, 5-6	5-6	ON	ON
P166, K5-166	66	2-3, 5-6	3-4	ON	ON

### P55C MMX CPU SPEED (Q5 MUST INSTALL LT1083)

CPU	BUS	JP14	JP8	JP9	JP11
MMX/K6/CXL-200	66	1-2, 4-5	3-4	OFF	ON

JP1: CPU VOLTAGE.

OPEN: 3.3V (DEFAULT)

SHORT: 3.45V

JP2: VOLTAGE REGULATOR TYPE

1- 2: LT1084

2- 3: LP2951 (DEFAULT)

JP5: CACHE SIZE:

1-2SHORT: 256K

2-3SHORT: 512K (DEFAULT)

JP15: RTC CLEAR

3- 3: NORMAL OPERATION

3- 4: CLEAR CMOS RAM (MUST ON POWER-OFF OPERATION)