



MANUAIS DE MOTHERBOARDS JBOND

PCI500C-K



- Socket 7
- Intel 82430TX chipset.
- SMC FDC37C672 Power Multi I/O chip.
- Dimensions: 8.7x10.6 inches 2/3 Baby AT form.
- Award PnP PCI flash BIOS.
- 512K bytes L2 SRAM cache.
- Two 168-pin DIMM sockets.
- Four 72-pin SIMM sockets.
- Two Enchance IDE sockets (up to four IDE devices) support fast ATA-2, ATAPI, and Ultra DMA/33 functions.
- One Floppy socket supports two floppy drivers with 360K, 720K, 1.22M, 1.44M, and 2.88M bytes.
- Four PCI slots. (PCI spec. V2.1)
- Four ISA slots. (1 PCI/ISA shared slot)
- PS/2 keyboard and PS/2 mouse connectors on board.
- Two Serial Port sockets.
- One Parallel Port socket supports SPP, EPP, and ECP.
- Two USB Port connectors on board.
- One FIR (Fast IrDA) Port connector on board (transfer rate up to 4MB/s).
- (Optional) Adaptec AIC-7880 Ultra Wide SCSI controller or AIC-7860 Ultra SCSI controller.

1. CPU Jumper Settings



Intel	JP1	JP1	JP1	JP2	JP2	JP2	JP3	JP3	JP3	JP3
w/o MMX technology	PIN 1	PIN 2	PIN 3	PIN 1	PIN 2	PIN 3	PIN 1	PIN 2	PIN 3	PIN 4
90MHz	NC	NC	NC	ON	NC	NC	NC	ON	ON	ON
100MHz	NC	ON	ON	ON						
120MHz	NC	ON	NC	ON	NC	NC	NC	ON	ON	ON
133MHz	NC	ON	NC	NC	NC	NC	NC	ON	ON	ON
150MHz	ON	ON	NC	ON	NC	NC	NC	ON	ON	ON
166MHz	ON	ON	NC	NC	NC	NC	NC	ON	ON	ON
180MHz	ON	NC	NC	ON	NC	NC	NC	ON	ON	ON
200MHz	ON	NC	NC	NC	NC	NC	NC	ON	ON	ON
Intel	JP1	JP1	JP1	JP2	JP2	JP2	JP3	JP3	JP3	JP3
w/ MMX technology	PIN 1	PIN 2	PIN 3	PIN 1	PIN 2	PIN 3	PIN 1	PIN 2	PIN 3	PIN 4
150MHz	ON	ON	NC	ON	NC	NC	NC	NC	NC	ON
166MHz	ON	ON	NC	ON						
180MHz	ON	NC	NC	ON	NC	NC	NC	NC	NC	ON
200MHz	ON	NC	ON							
233MHz	NC	ON								
AMD	JP1	JP1	JP1	JP2	JP2	JP2	JP3	JP3	JP3	JP3
K5	PIN 1	PIN 2	PIN 3	PIN 1	PIN 2	PIN 3	PIN 1	PIN 2	PIN 3	PIN 4
K5-PR90	NC	NC	NC	ON	NC	NC	ON	ON	ON	ON
K5-PR100	NC	NC	NC	NC	NC	NC	ON	ON	ON	ON
K5-PR120	NC	ON	NC	ON	NC	NC	ON	ON	ON	ON
K5-PR133	NC	ON	NC	NC	NC	NC	ON	ON	ON	ON
K5-PR166	ON	ON	NC	NC	NC	NC	ON	ON	ON	ON

AMD	JP1	JP1	JP1	JP2	JP2	JP2	JP3	JP3	JP3	JP3
K6	PIN 1	PIN 2	PIN 3	PIN 1	PIN 2	PIN 3	PIN 1	PIN 2	PIN 3	PIN 4
K6-PR166 (2.9v)	ON	ON	NC	NC	NC	NC	ON	NC	NC	ON
K6-PR200 (2.9v)	ON	NC	NC	NC	NC	NC	ON	NC	NC	ON
K6-PR200 (2.2v)	ON	NC	NC	NC	NC	NC	NC	ON	NC	NC
K6-PR233 (3.2v)	NC	ON	ON							
K6-PR233 (2.2v)	NC	ON	NC	NC						
K6-PR266 (2.2v)	NC	ON	ON	NC	NC	NC	NC	ON	NC	NC
K6-PR300 (2.2v)	ON	ON	ON	NC	NC	NC	NC	ON	NC	NC
AMD	JP1	JP1	JP1	JP2	JP2	JP2	JP3	JP3	JP3	JP3
K6-2 (3D)	PIN 1	PIN 2	PIN 3	PIN 1	PIN 2	PIN 3	PIN 1	PIN 2	PIN 3	PIN 4
K6-PR266 (2.2v)	NC	ON	ON	NC	NC	NC	NC	ON	NC	NC
Cyrix / IBM	JP1	JP1	JP1	JP2	JP2	JP2	JP3	JP3	JP3	JP3
6x86	PIN 1	PIN 2	PIN 3	PIN 1	PIN 2	PIN 3	PIN 1	PIN 2	PIN 3	PIN 4
PR133+GP(55x2)	NC	ON	NC	ON	ON	NC	ON	ON	ON	ON
PR150+GP(60x2)	NC	ON	NC	ON	NC	NC	ON	ON	ON	ON
PR166+GP(66x2)	NC	ON	NC	NC	NC	NC	ON	ON	ON	ON
PR200+GP(75x2)	NC	ON	NC	NC	ON	NC	ON	ON	ON	ON
Cyrix / IBM	JP1	JP1	JP1	JP1	JP2	JP2	JP2	JP3	JP3	JP3
6x86L	PIN 1	PIN 2	PIN 3	PIN 4	PIN 1	PIN 2	PIN 3	PIN 1	PIN 2	PIN 3
PR166+GP(66x2)	NC	ON	NC	ON						
PR200+GP(75x2)	NC	ON	NC	NC	ON	NC	NC	NC	NC	ON
Cyrix / IBM	JP1	JP1	JP1	JP2	JP2	JP2	JP3	JP3	JP3	JP3
6x86 MX	PIN 1	PIN 2	PIN 3	PIN 1	PIN 2	PIN 3	PIN 1	PIN 2	PIN 3	PIN 4
MX-PR166GP(66x2)	NC	ON	NC	ON						
MX-PR166GP(60X2.5)	ON	ON	NC	ON	NC	NC	NC	NC	NC	ON
MX-PR200GP(75x2)	NC	ON	NC	NC	ON	NC	NC	NC	NC	ON
MX-PR200GP(66x2.5)	ON	ON	NC	ON						
MX-PR233GP(75x2.5)	ON	ON	NC	NC	ON	NC	NC	NC	NC	ON
MX-PR233GP(66x3)	ON	NC	ON							
MX-PR266 (83x2.5)	ON	ON	NC	ON	ON	ON	NC	NC	NC	ON
Cyrix / IBM	JP1	JP1	JP1	JP2	JP2	JP2	JP3	JP3	JP3	JP3
MII	PIN 1	PIN 2	PIN 3	PIN 1	PIN 2	PIN 3	PIN 1	PIN 2	PIN 3	PIN 4
MII 233(75x2.5)	ON	ON	NC	NC	ON	NC	ON	NC	NC	ON
MII 233(66x3)	ON	NC	NC	NC	NC	NC	ON	NC	NC	ON
MII 266(83x2.5)	ON	ON	NC	ON	ON	ON	ON	NC	NC	ON
MII 300(75x3)	ON	NC	NC	NC	ON	NC	ON	NC	NC	ON
MII 300(66x3.5)	NC	NC	NC	NC	NC	NC	ON	NC	NC	ON

Note:

- ON - jumper block short
- NC- jumper block open

2. Clear CMOS Data Jumper Settings

- None

3. Support DIMM Module List

- Each DIMM socket supports 8 to 128M bytes DIMM module.
- Vcc provides 3.3v and 5.0v
- Support 4-clock SDRAM and EDO DIMM modules.

4. Support SIMM Module List

- Each SIMM socket supports 2 to 64M bytes SIMM module.
- Vcc provides 5.0v
- Support SRAM, EDO, and FPG SIMM modules.

5. Support Year 2000 Compliance

- BIOS version 1.00 or later supports Year 2000 compliance.

6. CPU Overheating Protection (CPU Defender)

- Intel Pentium 200/233, AMD K6, Cyrix 6x86/MX and Cyrix MII select "High."
- Other CPUs select "Low."

	High	Low	Disable
JP18	ON	NC	Don't care
JP19	NC	NC	ON

When this function is enabled, the system will sound an alarm if the CPU overheats and will automatically reduce the CPU frequency. When the CPU temperature returns to normal, the alarm will stop and the CPU frequency will return to normal. If your system still sounds the overheating alarm, please check whether your CPU cooling fan needs to be improved. Frequent overheating will affect the stability of the system and reduce the system's operating life.

7. ATX Power Supply Optional

PC97 compliance supports software Windows 95 shut down automatically. (ATX power only). AT power connector to ATX power adapter (optional). Support suspend mode for CPU auto slow down.

- ATX power adapter jumper cable connects to JP17.
- Jumper B1 switches to ATX.

8. Support LS-120 Zip Driver Boot Function

9. Support SCSI/CD-ROM function

10. Support INT 13 Expansion Mode (control above 8.4 GB IDE Hard Disk)