



MANUAIS DE MOTHERBOARDS JBOND

PCI500C-D



- **Socket 7.**
- **SIS 85C5501/85C5502/85C5503 chipset.**
- Winbond W83787F Multi I/O chip.
- Dimensions: 8.7x13 inches 2/3 Baby AT form.
- Award PnP PCI flash BIOS.
- 256K bytes L2 SRAM cache.
- Four 72-pin SIMM sockets.
- Two Enchance IDE sockets (up to four IDE devices) support fast ATA-2 and ATAPI functions.
- One Floppy socket supports two floppy drivers with 360K, 720K, 1.22M, and 1.44M bytes.
- Four PCI slots. (PCI spec. V2.0)
- Four ISA slots. (1 PCI/ISA shared slot)
- Two Serial Port sockets.
- One Parallel Port socket supports SPP, EPP, and ECP.
- Two USB Port connectors on board.
- **(Optional) NCR 53C810PT SCSI controller.**

1. CPU Jumper Settings

- **(Optional) NCR 53C810 SCSI controller.**

1. CPU Jumper Settings

FOR <MX8318(U35) model>

	JP11	JP12	JP13	JP6
75MHz	NC	NC	ON	NC
90MHz	NC	ON	NC	NC
100MHz	ON	NC	ON	NC
120MHz	NC	ON	NC	ON
133MHz	ON	NC	ON	ON

FOR <MX8325(U36) model>

	JP11	JP12	JP13	JP6
75MHz	ON	ON	NC	NC
90MHz	ON	NC	ON	NC
100MHz	ON	ON	ON	NC
120MHz	ON	NC	ON	ON
133MHz	ON	ON	ON	ON

Note:

- ON - jumper block short
- NC- jumper block open

2. Clear CMOS Data Jumper Settings

Operating Mode	Battery	Operation	JP24	JP15
SIS5503	Lithium CR2032	Normal	2-3	
SIS5503	Lithium CR2032	Clear CMOS	3-4	
SIS5503	Ext-Battery	Normal	Server as Connector	
SIS5503	Ext-Battery	Clear CMOS	Disconnect	
RTC DS12887A		Normal		Open
RTC DS12887A		Clear CMOS		Short While Power-OFF
RTC bq3287A		Normal		Open
RTC bq3287A		Clear CMOS		Short While Power-ON

3. External Cache Memory Jumper Settings

Cache Size	Data SRAM	Tag SRAM	JP8	JP9	Cacheable DRAM
256K	Eight 32Kx8	8Kx8	1-2	1-2	32MB
512K	Eight 64Kx8	32Kx8	2-3	1-2	64MB
1M	Eight 128Kx8	64Kx8	2-3	2-3	128MB

4. Support SIMM Module List

- Each SIMM socket supports 1M to 32M bytes SIMM module.
- Vcc provides 5.0v
- Support SRAM, EDO, and FPG SIMM modules.

5. Power-saving Mode Control Jumper

Power-saving Controled	JP14
STP. CLK	1-2
SMI	2-3

6. CPU Internal Cache Mode Selection Jumper

Cache Mode	JP14
Write-back	2-3
Disable	1-2

7. CPU Voltage Selection Jumper

CPU Voltage	JP14
3.3V to 3.45V	NC
3.45V to 3.6V (Intel VRE Spec. Pentium P54C CPU)	ON

8. On-board SCSI Jumper Settings

On-board SCSI		JP21	JP18	JP19	JP22
Enable	IRQ9	NC	2-3	2-3	1-2
Enable	IRQ11	NC	2-3	2-3	2-3
Disable		ON	1-2	1-2	NC

9. On-board SCSI Terminator Power Source Jumper Settings

Operating Mode	JP23
System (Default)	ON
Peripheral	NC

10. Serial Port Jumpers

UART A	JP39	
COM 1 3F8h (Default)	1-2	
Disable	2-3	
UART B	JP33	JP40
COM 2 3E8h (Default)	2-3	1-2
COM 3 2F8h	1-2	1-2
COM 4 2E8h	1-2	2-3
Disable	2-3	2-3

11. Printer Port Enable Jumper

Printer Port	JP35
LPT 1 (Default)	1-2
Disable	2-3

12. Printer Mode Settings Jumper

Printer Port Mode	JP32	JP36
Printer (Default)	2-3	2-3
EPP Mode	2-3	1-2
ECP/EPP	1-2	2-3
EXT, Two FDD	1-2	1-2

13. Printer IRQ Selection Jumper

Printer Port IRQ	JP34
IRQ 7 (Default)	1-2

IRQ 5

2-3

14. Printer Port Data Transfer Direction

Printer Port Data	JP42
Output Only	ON
Bi-direction (Default)	NC

15. EPP DMA Channel Selection Jumper

EPP DMA	JP37	JP38
Channel 1 (Default)	1-2	1-2
Channel 3	2-3	2-3

16. On-board IDE Enable Jumper

On-board IDE	JP2
Enable (Default)	1-2
Disable	2-3

17. On-board FDC Enable Jumper

On-board FDC	JP31
Enable (Default)	1-2
Disable	2-3

18. On-board Game Port Enable Jumper

On-board Game Port	JP30
Enable (Default)	ON
Disable	NC