

PCI5000

Dip Switch Settings



Table 1 - SW1

1	Onboard VGA on/off
2	Bus frequency (see table 3)
3	Bus frequency (see table 3)
4	Reserved (should be set to off)
5	Core/bus frequency ratio (see table 2)
6	Core/bus frequency ratio (see table 2)
7	Reserved (should be set to off)
8	SDM-II FLASH disk support on/off

Table 2 - CPU type & speed (SW1)

CPU internal frequency (MHz)	Bus frequency (MHz)	SW1.2	SW1.3	SW1.5	SW1.6
Intel Pentium processor (P54C).					
100	66	OFF	ON	OFF	OFF
120	60	ON	OFF	ON	OFF
133	66	OFF	ON	ON	OFF
150	60	ON	OFF	ON	ON
166	66	OFF	ON	ON	ON
180	60	ON	OFF	OFF	ON
200	66	OFF	ON	OFF	ON
Intel Pentium processor with MMX (P55C) technology.					
166*)	66	OFF	ON	ON	ON
200*)	66	OFF	ON	OFF	ON
233*)	66	OFF	ON	OFF	OFF

*) Remember to setup dipswitch SW2 - onboard programmable power supply setting - when using MMX processors (see table 4).

Table 3 - Bus frequency setting (SW1).

SW 1.2	SW 1.3	Freq (MHz)
ON	OFF	60
OFF	ON	66

Table 4 - Dip switch settings (SW2, CPU voltage).

V _{CPU}	SW2.1	SW2.2	SW2.3	SW2.4	
0,no CPU	OFF	OFF	OFF	OFF	
2.7 V	ON	ON	ON	OFF	
2.8 V	OFF	OFF	OFF	ON	P55C (3.3V enable (JPWR3))
2.9 V	ON	OFF	OFF	ON	
3.0 V	OFF	ON	OFF	ON	
3.1 V	ON	ON	OFF	ON	

3.2 V	OFF	OFF	ON	ON	
3.3 V	ON	OFF	ON	ON	P54C (3.3V disable (JPWR3))

[Top Of Page](#)

[Main Page](#)

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