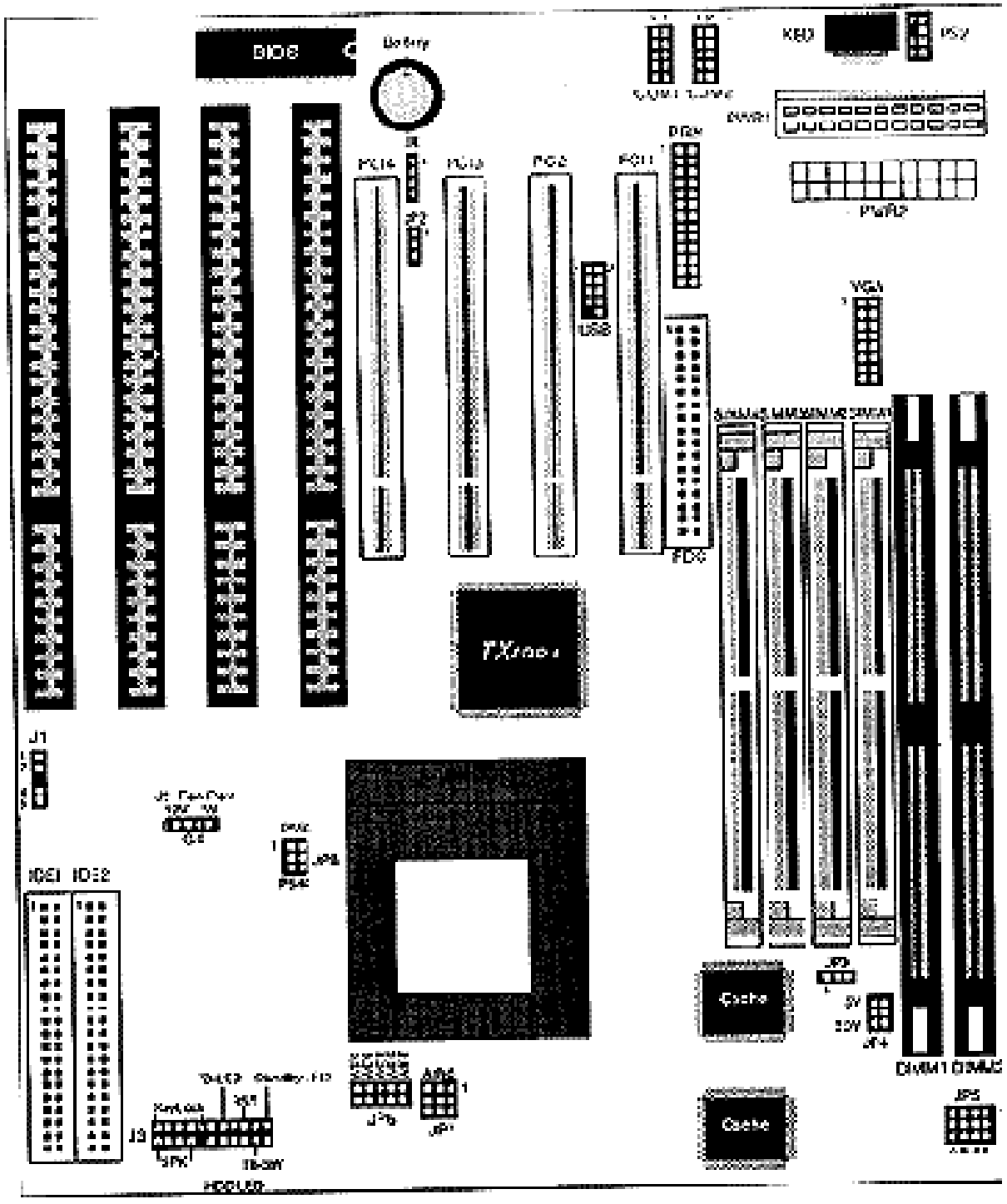
# *M567*

{ Data from M567 manual ver1.1 }




{ Data from M567 manual }

---

## PWR1-Power Supply Connectors

| Pin | Description | Pin | Description |
|---|---|---|---|

| | | | |
|---|---|---|---|
| 1 | Power Good | 7 | Ground |
| 2 | + 5V DC | 8 | Ground |
| 3 | + 12V DC | 9 | - 5V DC |
| 4 | - 12V DC | 10 | + 5V DC |
| 5 | Ground | 11 | + 5 V DC |
| 6 | Ground | 12 | + 5V DC |

---

## PWR2 - ATX Style Power Connector

| Pin | Description | Pin | Description |
|---|---|---|---|
| 1 | 3.3 V | 11 | 3.3 V |
| 2 | 3.3 V | 12 | -12 V |
| 3 | Ground | 13 | Ground |
| 4 | +5V | 14 | PS-ON |
| 5 | Ground | 15 | Ground |
| 6 | +5V | 16 | Ground |
| 7 | Ground | 17 | Ground |
| 8 | Power OK | 18 | -5V |
| 9 | 5VSB | 19 | +5V |
| 10 | +12V | 20 | +5V |

---

## VGA - VGA Connector

| Pin | Description | Pin | Description |
|---|---|---|---|
| 1 | R | 9 | Vcc |
| 2 | G | 10 | Ground |
| 3 | B | 11 | NC |
| 4 | NC | 12 | DDC DATA |
| 5 | Ground | 13 | H SYNC |
| 6 | Ground | 14 | V SYNC |
| 7 | Ground | 15 | DDC CLK |
| 8 | Ground | | |

---

## Keyboard Connector

| Pin | Description |
|---|---|
| 1 | Keyboard Clock |
| 2 | Keyboard Data |
| 3 | N.C. |
| 4 | Ground |
| 5 | + 5VDC |

---

## USB - 2 Sets of Universal Serial

## Bus Connectors

| Pin | Description |
|---|---|
| 1,2 | +5 VDC |
| 3,4 | Data - |
| 5,6 | Data + |
| 7,8 | Ground |
| 9,10 | Ground |

---

## PS2 - PS/2 Mouse Connector

| Pin | Description |
|---|---|
| 1 | Mouse CLK |
| 2 | Ground |
| 3 | N.C. |
| 4 | Mouse Data |
| 5 | N.C. |
| 6 | N.C. |
| 7 | N.C. |
| 8 | +5VDC |

---

## IR - Infrared

## Connector

| Pin | Description |
|---|---|
| 1 | IR In |
| 2 | Ground |
| 3 | IR Out |
| 4 | +5VDC |

---

## J3(KEY LOCK) - Keylock & Power LED Connector

| Pin | Description |
|---|---|
| 1 | LED Output |
| 2 | N.C. |
| 3 | Ground |
| 4 | Keylock |
| 5 | Ground |

---

## J3 (TB-LED) - Turbo LED Connector

| Pin | Description |
|---|---|
| | |

| | |
|---|---|
| + | Anode |
| - | Ground |

---

## J3 (RST) - Reset Switch Connector

| Setting | Description |
|---|---|
| Open | Normal Mode |
| Close | Reset System |

---

## J3 (STANDBY LED) - Standby LED Connector

| Pin | Description |
|---|---|
| + | Anode |
| - | Ground |

---

## J3 (SPK) - Speaker Connector

| Pin | Description |
|---|---|

| | |
|---|---|
| 1 | DATA Out |
| 2 | N.C. |
| 3 | Ground |
| 4 | + 5V |

---

## J3 (HDD-LED) - Hard Disk LED Connector

| Pin | Description |
|---|---|
| + | 5V |
| - | Active Low |

---

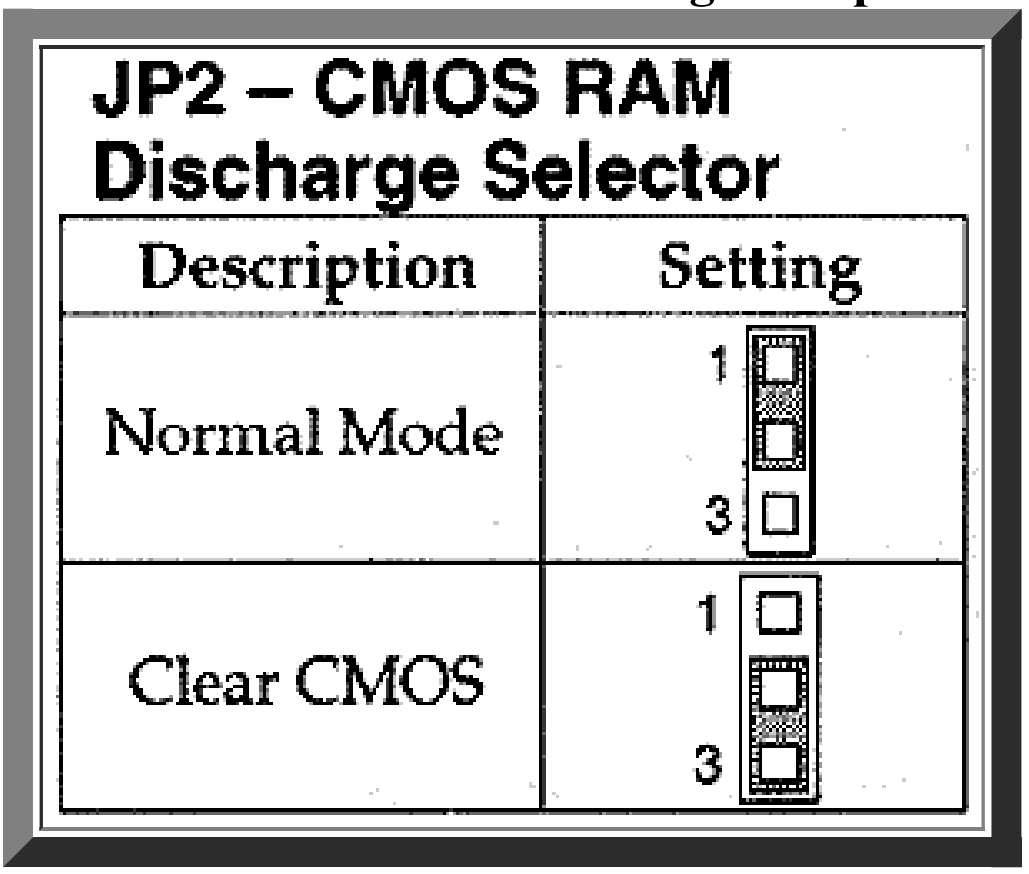
## J2 (FAN-POW) - Fan Power

| Pin | Description |
|---|---|
| 1 | +12V |
| 2 | Ground |
| 3 | Ground |
| 4 | +5V |

---

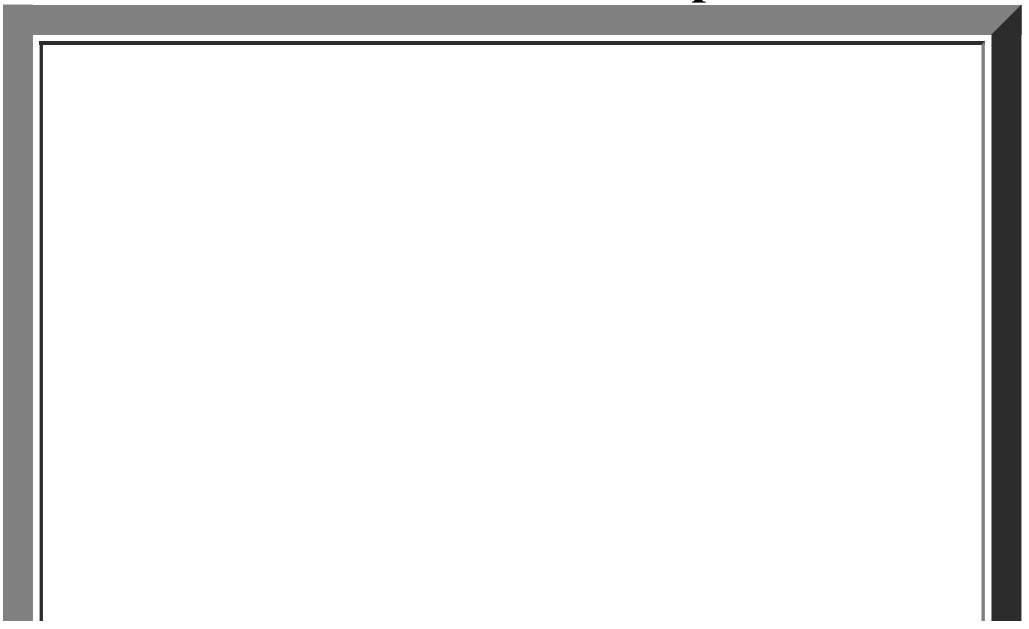
# M567 Mainboard

## JP2-CMOS RAM Discharge Jumper

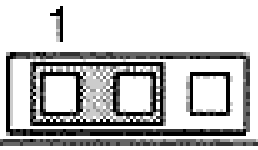
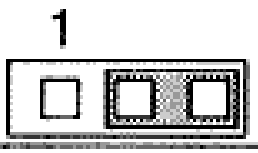



---

## JP3-Internal VGA Option



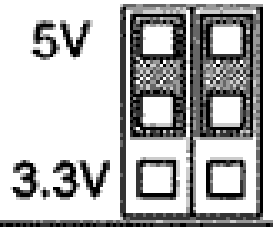
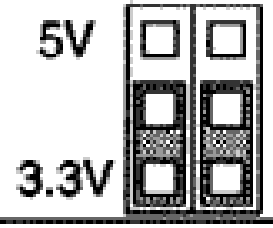
## JP3 – Internal VGA Option

| Description | Setting |
|---|---|
| Disable |   |
| Enable |   |

---

**JP4-DIMM Moduile Voltage Selector**

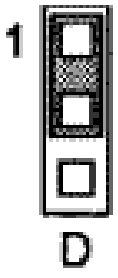
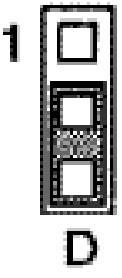
## JP4 – DIMM Voltage Selectors*

| Voltage Selectors | Setting |
|---|---|
| 5V |   |
| 3.3V |   |

---

## JP5(D)-PCI Clock Jumper

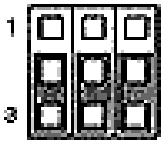
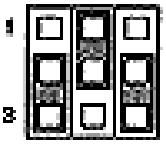
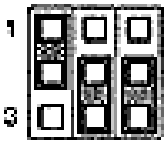
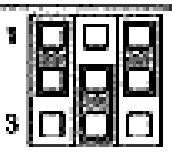
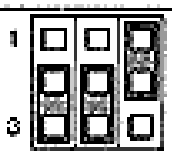
**JP5(D) – PCI Clock Jumper****

| Description | Setting |
|---|---|
| CPU CLK/2 |   |
| 33 MHz |   |

---

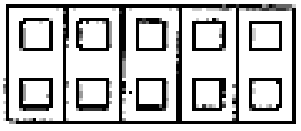
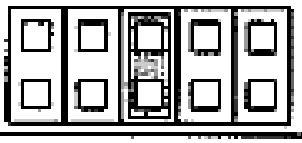
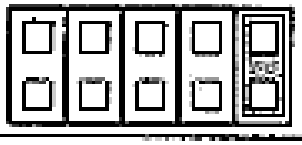
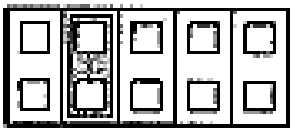
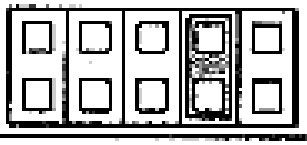
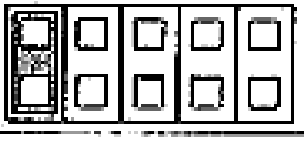
## JP5 (A,B,C)- CPU External Clock Selectors

## JP5 (A, B, C) – CPU External Clock Selectors

| External Clock | JP5 (A, B, C) Settings | External Clock | JP5 (A, B, C) Settings |
|---|---|---|---|
| 50MHz |   | 66MHz |   |
| 55MHz |   | 75MHz |   |
| 60MHz |   | | |

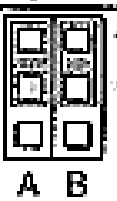
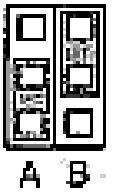
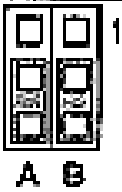
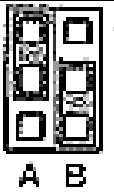
## JP6 - CPU Core Voltage Selectors

### JP6 – CPU Core Voltage Selectors***

| Core Vcc | Setting | Core Vcc | Setting |
|---|---|---|---|
| 2.5V |  | 3.2V |  |
| 2.8V |  | 3.3V |  |
| 2.9V |  | 3.5V |  |

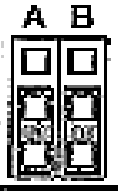
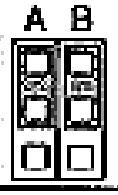
## JP7(A,B) CPU Internal Clock Speed Jumpers

**JP7 (A, B) – CPU Internal Clock Speed Selectors**

| IDT | Intel | Cyrix | AMD | JP7 |
|---|---|---|---|---|
| Reserved | 1.5X/ 3.5X | Reserved | K5 1.5X/ K6 3.5X |   |
| Reserved | 2.0X | 2.0X | Reserved |   |
| Reserved | 2.5X | M2 2.5X | 2.5X |   |
| C6 3.0X | 3.0X | M2 3.0X | K6 3.0X |   |

## JP8 - CPU Type Selector

## JP8 – CPU Type Selector

| CPU | Setting | Example |
|---|---|---|
| P54C (Single Voltage) |   | Intel P54C, IDT C6 |
| P55C (Dual Voltage) |   | Intel MMX™, AMD K6, IBM/Cyrix 6x86L/6x86MX(M2) |

* *JP4 must be set to 3.3V position for all synchronous DRAM.*
** *JP5(D) must be set to 2-3 position only when the system is running at 75MHz.*
*** *JP6 must be set to 3.3V position for Intel P54C (Single Voltage) CPUs.*

---

***PCCHIPS Manufacturing Limited***
*Unit 8, 1/Floor, Fook Hong Industrial Building,*
*19 Sheung Yuet Road, Kowloon Bay, Hong Kong.*
*Tel:(852) 2795 3890 Fax:(852) 2795 3179*

---

*Modified: Oct 17 97 by Kevin Chan*

---