

Amstrad



PC7486SLC

PERSONAL COLOUR COMPUTER

SERVICE MANUAL

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Technical Specifications

Processor	TI486SLC
Clock Speed	33MHz
Memory	2-4Mb parity checked on board RAM
Cache RAM	64kb
Memory Expansion	16Mb via 4 SIMM Sockets
HDD	130 / 214Mb fitted as standard access time 16mS
FDD	3.5" 1.4Mb
Expansion Slots	2x16 bit, 2/3 length ISA slots
Graphics	Super VGA, VGA, EGA, CGA, MDA & Hercules compatible
Video Memory	512kb/1024kb
Max Resolution	1024 x 768 pixel
On Screen Colours	Up to 256 max. out of total palette of over 250,000
External Interface	Bi-directional Parallel port, Twin RS232C serial ports, PS/2 type Kbd. socket, PS/2 type mouse socket, external SVGA monitor port, external SVGA Monitor Power Socket
Power Supply	65W 110/220-240V Auto-Ranging
System Unit Dimensions	325mm (W) x 78mm (H) x 281mm (D)
Keyboard	102 key high quality PS/2 type
Mouse	Two button PS/2 type
SVGA Monitors	SVGA 14" Hi-Res Colour Monitor 0.39 Dot Pitch SVGA 14" Ultra Hi-Res Colour Monitor 0.28 Dot Pitch Low Radiation

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SAFETY TEST

1. **Flash Test:** Test at 1.5kV RMS/3 sec between the live and neutral poles of the mains lead and all accessible metal parts points on the exterior of the set.
2. **Insulation Resistance Test:** Test at Test at 1.5kV RMS/3 sec between the live and neutral poles of the mains lead and all accessible metal parts points on the exterior of the set to show a resistance greater than 4M ohm.
3. **Earth Continuity Test:** The resistance of the mains lead shall not exceed 0.5 ohms.

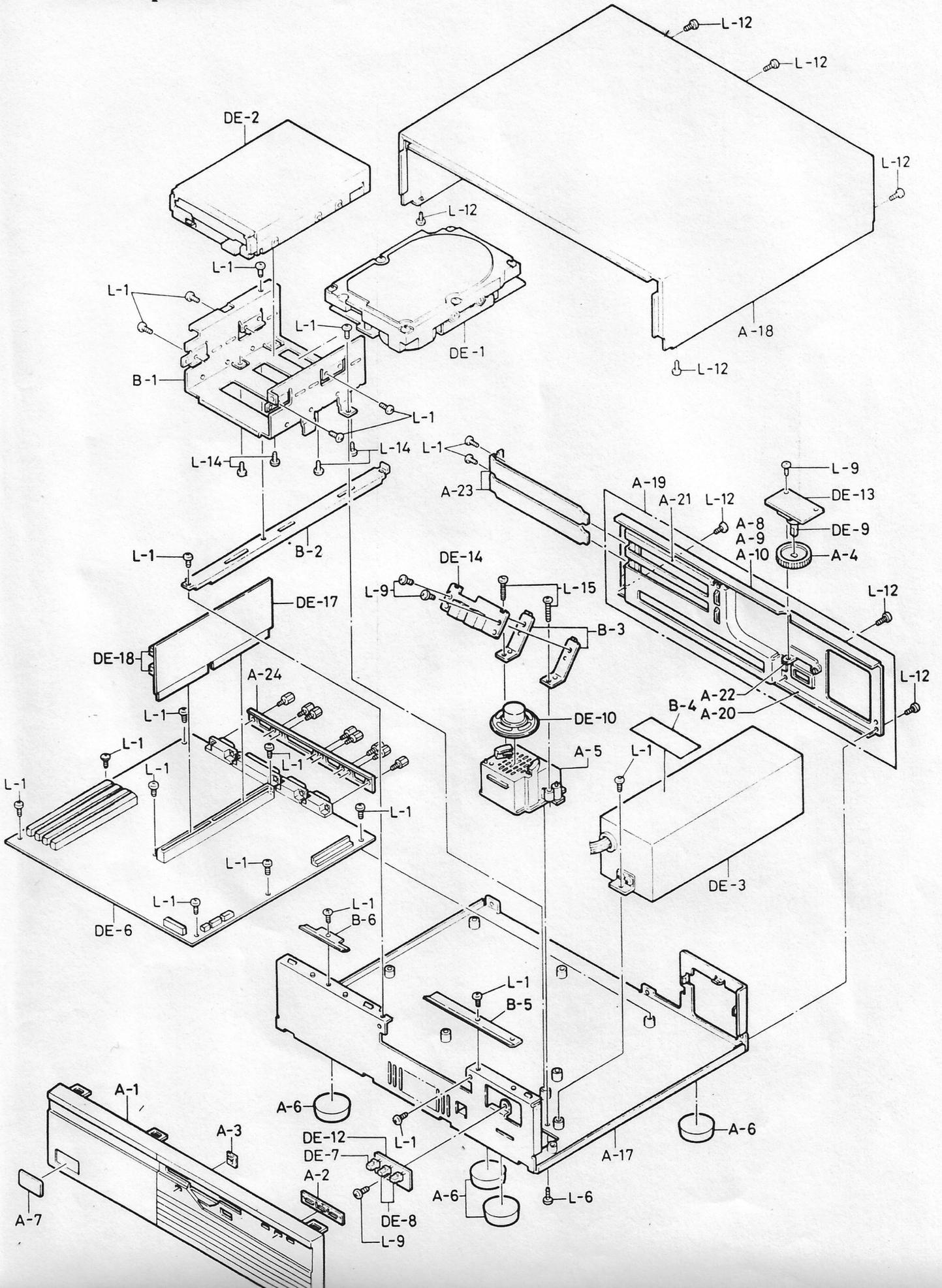
Please Note: When any work is completed on this unit, correct safety test must be carried out to ensure continued electrical safety.

Please Note: All parts shown with Δ are safety items and must be replaced with similar items having similar safety specifications.

In keeping with our policy of continually improving our service and the technical quality of our products we reserve the right to change component types, manufacturers, source of supply of supply or technical specifications at any time

Amstrad Plc.
BRENTWOOD HOUSE, 169 KINGS ROAD, BRENTWOOD, ESSEX CM14 4EF

Cabinet Exploded View



PC7486SLC Cabinet Parts List

Ref.No.	Description	Pt. No.
A-1	Front Panel Assy	274215
A-2	Lens LED	
A-3		
A-4	Knob Volume Control	273892
A-5	Keyboard Mouse Cover	
A-6	Foot	274216
A-7	Inaly PC7486SLC-33	
A-17	Bottom Cabinet	
A-18	Top Cabinet	273893
A-19	Back Cabinet Assy	274217
A-23	Expansion Slot Cover	273914
ICs		
U2	IC TH6887A Interface	274218
U3,5,6	IC 1489 / SN75189 Interface	272287
U4,7	IC 1488 / SN75188 Interface	272286
U8	IC 83C757AF 100P Win Chip	274219
U9	IC 74F244 SOL 20P TTL	
U10	IC WB486SLC ROM	274220
U11	IC CL-GD5420-75QC-B AVGA Chip	274221
U12,40	IC 74F00 SO 14P TTL	
U13-16	IC 256K*4-7 Video DRAM	42103
U17-20	IC V53C104FP70 Video DRAM	42103
U21	IC 74HC132 SO 14P TTL	
U22,23,25,30 38,41,42	IC 74LS245 SOL TTL	272264
U24	IC 7406 SO 14P TTL	273930
U26	IC W83C312F Win QFP Chip	274222
U27	IC 80486SLC 33M QFP 100P CPU	274223
U29,33,34,39	IC 74F373 SOL 20P TTL	
U31	IC 8042 Interface 40P DIP	274224
U32	IC W83C311WF Win 160P Chip	274225
U35-37	IC W24257BK-20 Cache RAM	42065
SIMM1,2	IC 1M*8-7 RAM Module	42066
Transistors		
Q3	IC TL431	273812
Q4	Tr 2N3904	273165
Q5	Tr MPSA13	274226
Diodes		
D3-10	D 1N4148 SDM LL34	

Elrctrical Parts List

Ref.No.	Description	Pt. No.
Miscellaneous		
DE-1	HDD Seagate 130Mb UK formatted	274207
DE-1	HDD Seagate 214Mb UK	274208
DE-1	HDD seagate 130Mb	42023
DE_1	HDD seagate214Mb	42024
DE-2	FDD 3.5" 1.4Mb MP420-1/F17W	273723
DE-3	Power Supply Module	273888
DE-6	Main PCB Assy.	274200
DE-9	VRot 500 Ω AL001	273922
DE-10	Speaker	273923
DE-12	LED PCB Assy	
DE-13	Volume PCB Assy	
DE-14	Jack PCB Assy	274131
DE-17	Expansion PCB Assy	
RN1,2	33 Ω 8P/4R 1/8w	
RN3-5	22 Ω 8P/4R 1/8w	
OSC1	Crystal 66MHz	274227
Y2	X'tal 24MHz	273183
Y3	X'tal 14.318MHz	273162
J2,3	Connector 9P D Type M 90 $^{\circ}$	
J4	Connector 25P D Type FM 90 $^{\circ}$	
J5	Connector 15P D Type FM 90 $^{\circ}$	
F1	PICO Fuse 125V 3A	274228
	FDD Cable	273925
	HDD Cable	273926
	Mains Lead UK	270053
	Mains lead Euro	271067
	Mouse MSPS2	272710
	Complete Keyboard Assy. UK/HL	273895
	Complete Keyboard Assy. Au	273901
	Complete Keyboard Assy. Fr	273899
	Complete Keyboard Assy. It	273898
	Complete Keyboard Assy. Ge	273897
	User Instructions PC User Guide	274229
	7486SLC Technical Ref Manual	274230
	Software Installation Instructions	274231
	Cord DIN Connector KBD7	274199

Description	Ref. No.	Pt.No.
	Resistors	
0 ohm	R8,99	
1 ohm	R53,54	
22 ohm	R56,58,59,102-107,118,119	
27 ohm	R96	
33 ohm	R44,45,51,52,109	
47 ohm	R18-25	
51 ohm	R65,75	
75 ohm	R33,36-38,50	
220 ohm	R17	
270 ohm	R34	
330 ohm	R28,29,82,100,101	
680 ohm	R60,61	
910 ohm	R63	
1k ohm	R9,26,27,30-32,35,46-48,73,84	
3.3k ohm	R69-72,110-117	
4.7k ohm	R1-6,14-16,66,67,76-79,83,90	
5.1k ohm	R13	
6.8k ohm	R40-43	
10kohm	R49,68,74,80,81,85,87-89,91-93,98	
20k ohm	R62,86	
47k ohm	R97	
100k ohm	R64	
	Capacitors	
	Ceramic	
10pf	C5,6	
22pF	C49,66,69	
47pF	C25-30,48,53-56	
51pF	C64	
68pF	C63	
100pF	C15,50,51	
220pF	C32,33,57,58,60,61	
270pF	C46	
1000pF	C45	
0.0022uF	C7-14	
0.0047uF	C2	
	Metal Film	
0.1uF	BC1-41,BBC2,C1,16-18,24,31,34 36-39,42,43,59,62,67	

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Power On Self Test Fault Condition Description (using IBM compatible POST Diagnostics Card)

01	Processor register test about to start, and NMI to be disabled
02	NMI is Disabled. Power on delay starting.
03	Power on delay complete. Any initialisation before keyboard BAT is in progress.
04	Any initialisation before keyboard BAT is complete. Reading keyboard SYS bit, to check soft reset/power-on.
05	Soft reset/power-on determined. Going to enable ROM. i.e. disable shadow RAM/Cache if any.
06	ROM is enabled. Calculating ROM BIOS checksum, and waiting for KB controller input-buffer to be free.
07	ROM BIOS checksum passed, KB controller I/B free. Going to issue the BAT command to keyboard controller.
08	BAT command to keyboard controller is issued. Going to verify BAT command.
09	Keyboard controller BAT result verified. Keyboard command byte to be written next.
0A	Keyboard command byte code is issued. Going to write command byte data.
0B	Keyboard controller command byte is written. Going to issue Pin-23,24 blocking/unblocking command.
0C	Pin-23,24 of keyboard controller is blocked/unblocked. NOP command of keyboard controller to be issued next.
0D	NOP command processing is done. CMOS shutdown register test to be done next.
0E	CMOS shutdown register R/W test passed. Going to calculate CMOS checksum, and update DIAG byte.
0F	CMOS checksum calculation is done, DIAG byte written. CMOS init. to begin (If "INIT CMOS IN EVERY BOOT IS SET").
10	CMOS initialisation done (if any). CMOS status register about to init for Date and Time
11	CMOS Status register initialised. Going to disable DMA and Interrupt controllers.
12	DMA controller #1, #2, interrupt controller #1, #2 disabled. About to disable video display and init port-B.
13	Video display is disabled and port-B is initialised. Chipset init/auto memory detection about to begin.
14	Chipset initialisation/auto memory detection over. 8254 timer test about to start.
15	CH-2 timer test halfway. 8254 CH-2 timer test to be completed.
16	CH-2 timer test over. 8254 CH-1 timer test to be completed.
17	CH-1 timer test over. 8254 CH-0 timer test to be completed.
18	CH-0 timer test over. About to start memory refresh.
19	Memory refresh started. Memory refresh test to be done next.
1A	Memory refresh line is toggling. Going to check 15mS ON/OFF time.
1B	Memory refresh 30mS test complete. Base 64k memory test about to start.
20	Base 64k memory test started. Address line test to be done next.

21	Address line test passed. Going to do toggle parity.
22	Toggle parity over. Going to sequential data R/W test.
23	Base 64k sequential data R/W test passed. Any setup before interrupt vector init about to start.
24	Setup required before vector initialisation complete. Interrupt vector initialisation about to begin.
25	Interrupt vector initialisation done. Going to read I/O port of 8042 for turbo switch (if any).
26	I/O port of 8042 is read. Going to initialise global data for turbo switch.
27	Global data initialisation is over. Any initialisation after interrupt vector to be done next.
28	Initialisation after interrupt vector is complete. Going for monochrome setting.
29	Monochrome mode setting is done. Going for Colour mode setting.
2A	Colour mode setting is done. About to go for toggle parity before optional ROM test.
2B	Toggle parity over. About to give control for any setup required before optional video ROM check.
2C	Processing before video ROM control is done. About to look for optional video ROM and give control.
2D	Optional video ROM control is done. About to give control to do any processing after video ROM returns control.
2E	Return from processing after the video ROM control. If EGA/VGA not found then do display memory R/W test.
2F	EGA/VGA not found. Display memory R/W test about to begin.
30	Display memory R/W test passed. About to look for retrace checking.
31	Display memory R/W test or retrace checking failed. About to do alternate Display memory R/W test.
32	Alternate Display memory R/W test passed. About to look for the alternate display retrace checking.
33	Video display checking over. Verification of display type with switch setting and actual card to begin
34	Verification of display adapter done. Display mode to be set next.
35	Display mode set complete. BIOS ROM data area about to be checked.
36	BIOS ROM data area check over. Going to set cursor for power on message.
37	Cursor setting for power on message id complete. Going to display the power on message.
38	power on message complete. Going to read new cursor position.
39	New cursor position read and saved Going to display the reference string.
3A	Reference string display is over. Going to display the Hit <ESC> message.
3B	Hit <ESC> message displayed. Virtual mode memory test about to start.
40	preparation for virtual mode test started. Going to verify from video memory.
41	Returned after verifying from display memory. Going to prepare the descriptor tables.

42	Descriptor tables prepared. Going to enter in virtual mode for memory test.
43	Entered in the virtual mode. Going to enable interrupt for diagnostics mode.
44	Interrupts enabled (if diagnostics switch is on). Going to initialise data to check memory wrap around at 0:0.
45	Data initialised. Going to check for memory wrap around at 0:0 and finding the total system memory size.
46	Memory wrap around test done. Memory size calculation over. About to go for writing patterns to test memory.
47	Pattern to be tested written in extended memory. Going to write patterns in base 640k memory.
48	patterns written in base memory. Going to findout amount of memory below 1M memory.
49	Amount of memory below 1M found and verified. Going to findout amount of memory above 1M memory.
4A	amount of memory above 1M found and verified. Going for BIOS ROM data area check.
4B	BIOS ROM data area check over. Going to check <ESC> and to clear memory below 1M for soft reset.
4C	Memory below 1M cleared. (SOFT RESET) Going to clear memory above 1M.
4D	Memory above 1M cleared. (SOFT RESET) Going to save the memory size.
4E	Memory test started. (NO SOFT RESET) About to display the first 64k memory test.
4F	Memory size display started. This will be updated during memory test. Going for sequential and random memory test.
50	Memory test below 1M completed Going to adjust memory size for relocation/shadow.
51	Memory size adjusted due to relocation/shadow. Memory test above 1M to follow.
52	Memory test above 1M completed. Going to prepare to go to real mode.
53	CPU registers are saved including memory size. Going to enter in real mode.
54	Shutdown successfully, CPU in real mode. Going to restore registers saved during preparation for shutdown.
55	Register restored. Going to disable gate A20 address line.
56	A20 address lines are disabled successfully. BIOS ROM data are about to be checked.
57	BIOS ROM data check halfway. BIOS ROM data area check to be complete.
58	BIOS ROM data area check over Going to clear Hit <ESC> message.
59	Hit <ESC> message cleared. <WAIT...> message displayed. About to start DMA and interrupt controller test.
60	DMA page register test passed. About to verify from display memory.
61	Display memory verification over. About to go for DMA #1 base register test.
62	DMA #1 base register test passed. About to go for DMA #2 base register test.
63	DMA #2 base register test passed. About to go for BIOS ROM data check.

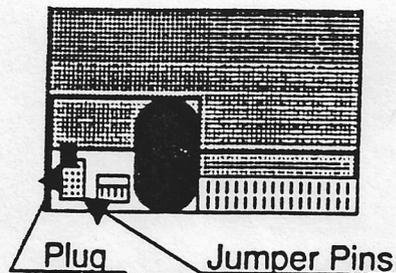
64	BIOS ROM data area check halfway. BIOS ROM data area check to be completed.
65	BIOS ROM data area check over. About to program DMA unit 1 and 2.
66	DMA unit 1 and 2 programming over. About to initialise 8259 interrupt controller
67	8258 initialisation over. About to start keyboard test.
80	Keyboard test started. Clearing output buffer, checking for stuck key, About to issue keyboard reset command.
81	keyboard reset error/stuck key found. About to issue keyboard controller interface test command.
82	Keyboard controller interface test over. About to write command byte and init circular buffer.
83	Command byte written, Global data init done. About to check for lock-key.
84	Lock-key checking over. About to check for memory size mismatch with cmos.
85	Memory size check done. About to display soft error and check for password or bypass setup.
86	Password checked. About to do programming before setup.
87	Programming before setup complete. Going to coms setup program.
88	Returning from coms setup program and screen is cleared. About to do programming after setup.
89	Programming after setup complete. Going to display power on screen message.
8A	First screen message displayed. About to display <WAIT...> message.
8B	<WAIT...> message displayed. About to do Main and Video BIOS shadow.
8C	Main and Video BIOS shadow successful. Setup options programming after coms setup about to start.
8D	Setup options are programmed, mouse check and init to be done next.
8E	Mouse check and initialisation complete. Going to hard disk and floppy reset.
8F	Floppy check returns that floppy is to be initialised. Floppy setup to follow.
90	Floppy setup is over. Test for hard disk presence to be done.
91	Hard disk presence test over. Hard disk setup to follow.
92	Hard disk setup completed. About to go for BIOS ROM data area check.
93	BIOS ROM data area check halfway. BIOS ROM data area check to be completed.
94	BIOS ROM data area check over. Going to set base and extended memory size.
95	Memory size adjusted due to mouse support, hdisk type 47. Going to verify from display memory.
96	Returned after verifying from display memory. Going to do any init before C800 optional ROM control.
97	Any init before C800 optional ROM control is over. Optional ROM check and control will be done next.

98	Optional ROM control is done. About to give control to do any required processing after optional ROM returns control.
99	Any initialisation required after optional ROM test over. Going to setup timer data area and printer base address,
9A	Return after setting timer and printer base address, Going to set the RS-232 base address.
9B	Returned after RS-232 base address. Going to do any initialisation before co-processor test.
9C	Required initialisation before co-processor is over. Going to initialise the co-processor next.
9D	Co-processor initialised. Going to do any initialisation after co-processor test.
9E	Initialisation after co-processor test is complete. Going to check extd keyboard, keyboard ID and num-lock.
9F	Extd keyboard check is done, ID flag set, num-lock on/off, Keyboard ID command to be issued.
A0	Keyboard ID command issued. Keyboard ID flag to be reset.
A1	Keyboard ID flag reset. Cache memory test to follow.
A2	Cache memory test over. Going to display any soft errors.
A3	Soft error display complete. Going to set the keyboard typematic rate.
A4	Keyboard typematic rare set. Going to program memory wait states.
A5	Memory wait states programming over. Screen to be cleared next.
A6	Screen cleared. Going to enable parity and NMI.
A7	NMI and parity enabled. Going to do any initialisation required before giving control to optional ROM at E000.
A8	Initialisation before E000 ROM control over. E000 ROM to get control next.
A9	Returned from E000 ROM control. Going to do initialisation required after E000 optional ROM control.
AA	Initialisation after E000 optional ROM control is over. Going to display the system configuration.
00	System configuration is displayed. Going to give control to INT 19h boot loader.

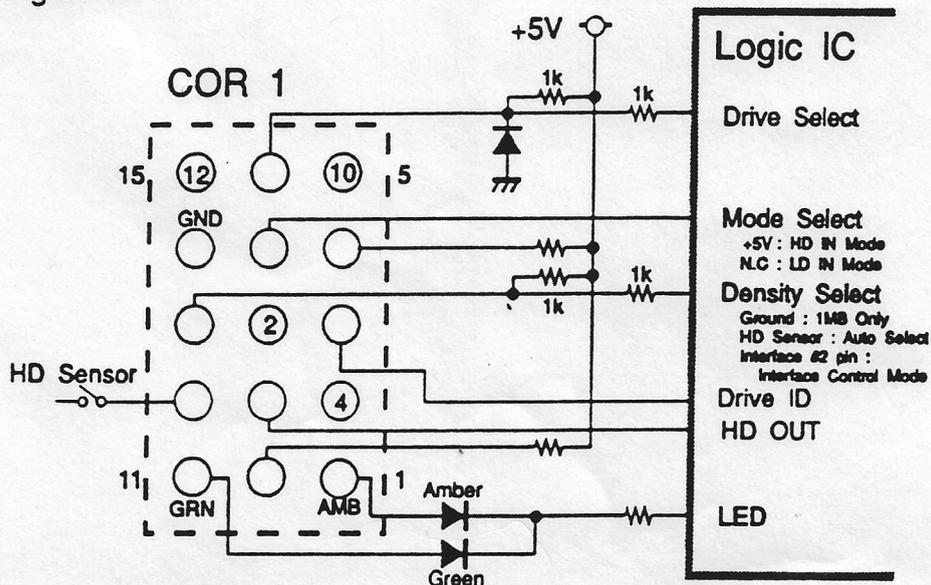
Main PCB Jumper Configuration

FUNCTION		JUMPER SETTING
Cache size	16kb	JP23 pin 2,3 closed
	64kb	JP23 pin 1,2 closed
Parity checking	enable	JP22 pin 1,2 closed
	disable	JP22 pin 2,3 closed
CMOS clear	clear CMOS	JP14 closed
	normal operation	JP15 open
Printer port	printer uni-directional	JP12 open
	printer bi-directional	JP12 closed
Video wait states	0 wait state	JP15 closed
	1 wait state	JP15 open
VGA adapter	disable on board VGA	JP16 pin 1,2 closed
	enable on board VGA	JP16 pin 2,3 closed
VGA mode	ISA bus	JP18 pin 1,2 closed
		JP19 pin 1,2 closed
	Local bus	JP18 pin 2,3 closed
		JP19 pin 2,3 closed

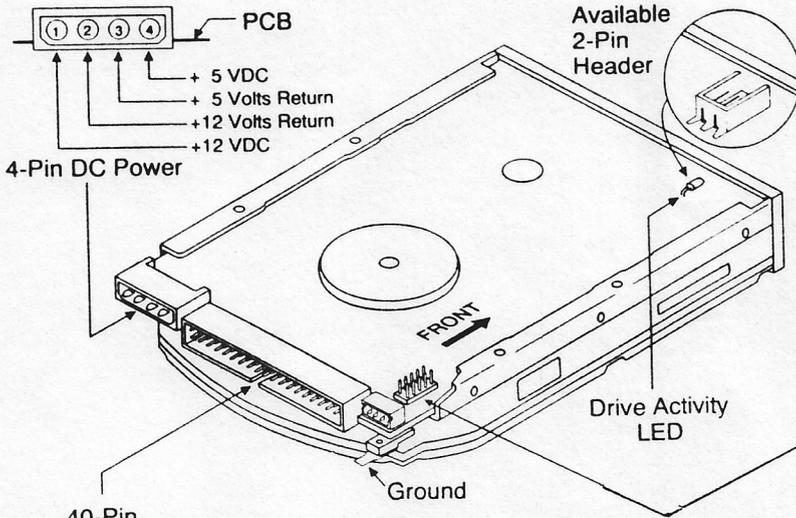
2MB 3.5" FDD



The interface circuits around the short plugs are shown in the drawing below :

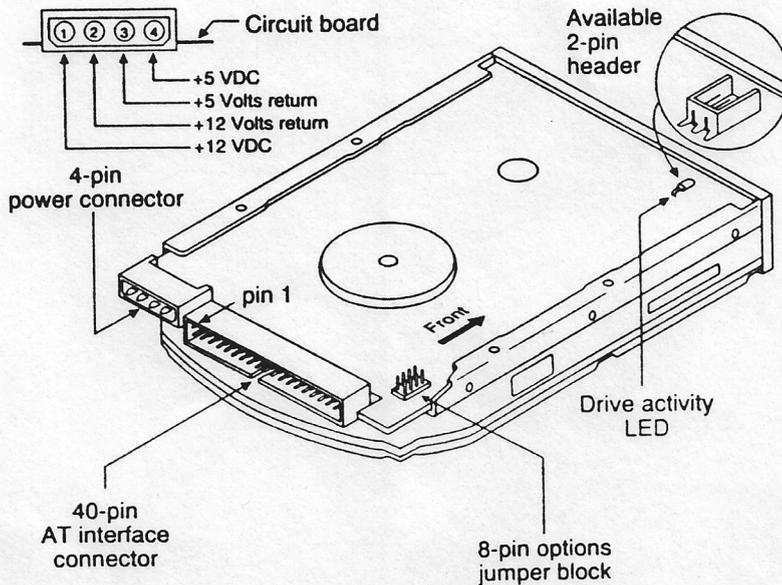
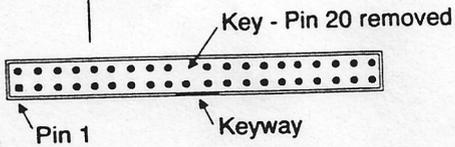


Drive Jumpers And Connectors



10-Pin
User Configuration Block

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8-pin options
jumper block

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