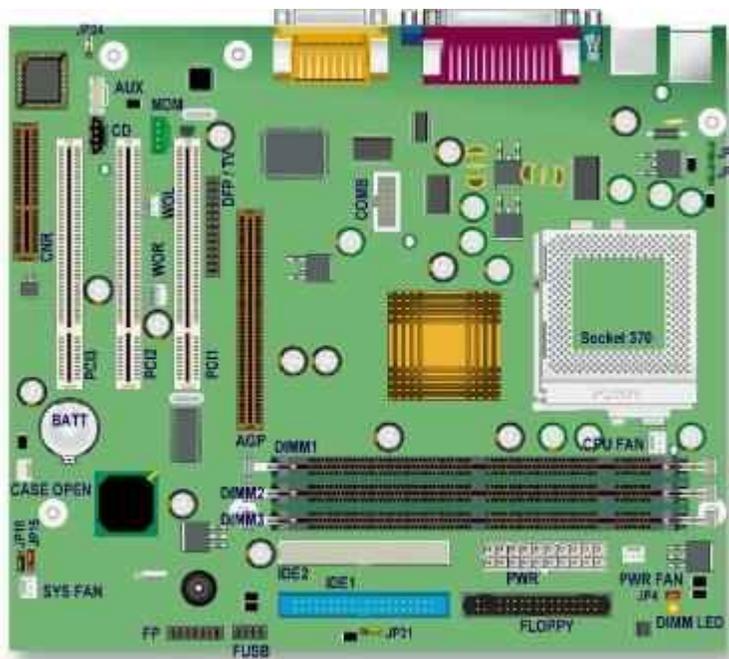


Packard Bell Mercure M/Board



Specification

- **Audio** : SigmaTel STAC9721 Codec
- **BIOS** : AMI
- **Chipset** : Intel 815 chipset consisting of the 82815 Graphics and AGP Memory Controller Hub (GMCH) and the 82801AB I/O Controller Hub (ICH2).
- **CPU Support** :Socket 370
 - Supports Intel Celeron FC-PGA (Flip-Chip Pin Grid Array) processor 533/66 to 700/66 MHz and faster.
 - Supports Intel PIII Coppermine FC-PGA (Flip-Chip Pin Grid Array) - 600/100 to 850/100 MHz and faster, 600/133 to 900/133 MHz and faster
 - Warning: Not all the above processors have been qualified. Check their current status with the Hardware Engineering dpt.
 - 66/100/133 MHz Host bus speed processors are supported.
- **Form Factor** : ATX 24.4 x 21.0 cm Form Factor
- **RAM** :
 - Supports 3 memory banks using two 168-pin unbuffered DIMM.
 - Supports up to 3 double sided DIMMS at a clock speed of 100 MHz.
 - Supports up to 2 double sided or 3 single sided DIMMS at a clock speed of 133 MHz.
 - Supports a maximum memory size of 512 MB (128-bit technology) or 256 MB (64-bit technology) for SDRAM.
 - Supports 3.3 V SDRAM DIMMs with gold-plated contacts.
 - ECC memory checking is not supported
- **Video** : Integrated in chipset

Jumpers

Jumper	Configuration
JP18 - Clear CMOS Jumper (CMOS)	2-3: Normal operation (default) 1-2: Clear current BIOS default settings
JP15 - Safe Mode/Recovery/Normal jumper	1-2: Normal (default) 2-3: Safe Mode (Restores safe values / loads the BIOS setup on boot) Open: Recovery (used only to flash a corrupted BIOS)
JP4 - Suspend To RAM (STR)	Open: Disable STR mode Close: Enable STR mode
JP3 - PS/2 Keyboard Power On	1-2: Enabled (default) 2-3: Disabled
JP1 - Rear USB Device Wake Up Selection	1-2: Enabled (default) 2-3: Disabled
JP24 - CNR Selection	1-2: CNR Secondary (Default) 2-3: CNR Primary AC'97 (onboard CODEC) disabled
JP31 - Front USB Keyboard Wake up	1-2: Enabled (default) 2-3: Disabled