Packard Bell 386 33 M/Board
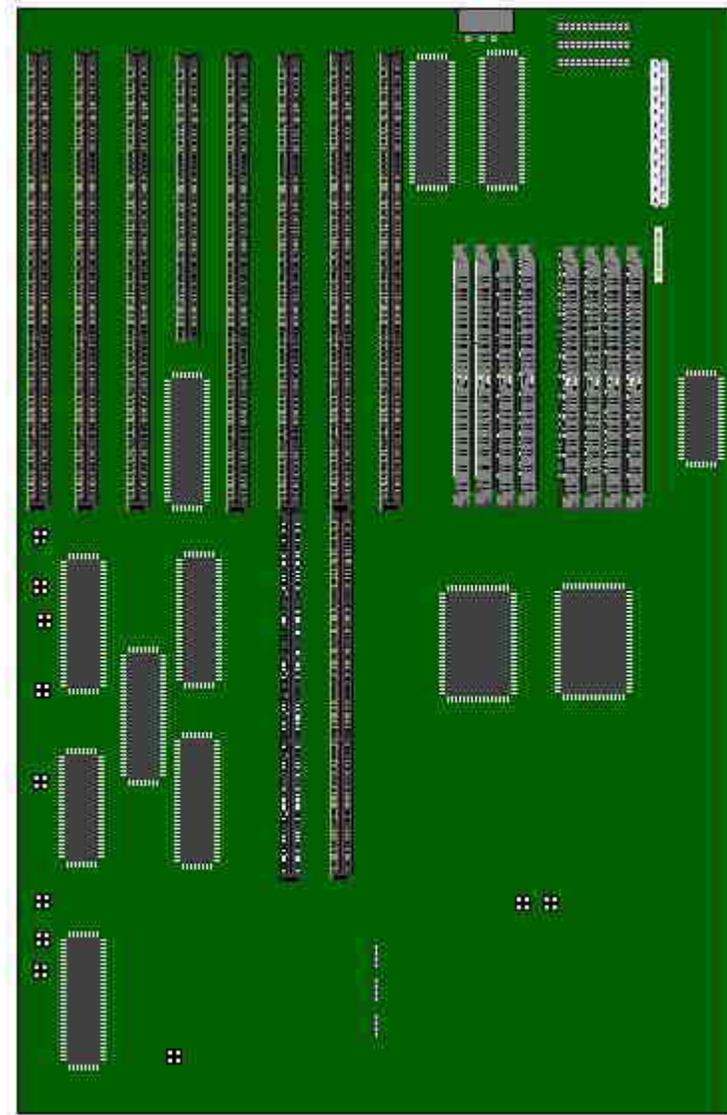

Specification

386-25 and 386-33 are basically the same. Jumper settings and motherboard layout may vary.

- **Battery :** DS1287 soldered on Motherboard
- **BIOS :** 2 X 32K Phoenix BIOS Phoenix 1.10.20C1 (Rev. D), Phoenix 1.10.19C2 (Rev. F). To access setup, Ctrl+Alt+Ins
- **Bus Architecture :** ISA based system bus
- **Cache :** 64K, 35 ns
- **CPU :** 80386-33 MHz. Keyboard Switchable Ctrl-Alt-1 = 8 MHz, Ctrl-Alt-2 = 33 MHz. Use number keys on numeric pad.
- **Expansion Slots :**
  - 2 x 32-bit ISA
  - 5 x 16-bit ISA slots
  - 1 x 8-bit ISA slot

- **Interfaces :**
    - 1 DB-25 Parallel Port
    - 2 DB-9 Serial ports
    - 1 Keyboard Port
    - 1 Ext. VGA Conn 15 pin
- **RAM :** 4MB standard Maximum on Motherboard 8 MB
- **Speaker :** Piezoelectric, soldered on motherboard
- **Video :** Oak, Trident, PBVGA Multi, or SVGA

Jumpers

Function	Jumper	Description
SIMM Type	E3-E5 E2-E4, E3-E5 None	Static Column Fast Paged RAS/CAS
Total RAM	None E7-E9 E6-E8 E6-E8, E7-E9	1MB 2MB 4MB 8MB
RAM Size	E16-E18, E17-E19 E18-E20, E19-E21	256k SIMM 1MB SIMM
Parallel Port	E25-E26, E29-E30 E26-E27, E28-E29	LPT1 (IRQ7) LPT2 (IRQ7)
Serial Port 1 - COM1	E31-E32 E32-E33	Enabled Disabled
Serial Port 2 - COM2	E34-E35 E35-E36	Enabled Disabled
Video Display	E37-E38 E38-E39	Monochrome Colour
Keyboard Reset	E40-E41 E41-E42	Enabled Disabled
Math Coprocessor	E43-E44 E44-E45	Installed Not Installed
Printer Acknowledge	E46-E47 E47-E48	Leading Edge Trailing Edge
EPROM Size	E49-E50 E50-E51	64K 128K
Test	E52-E53	Leave Open

Upgrades

- **Battery :** DS1287 soldered on motherboard.
- **Maths Co-Processor :** This system supports the 80387-33 Math Co-processor. It is installed at location U79. No jumper settings are required.
- **RAM :** 4MB standard Maximum on Motherboard 8 MB. This board will accept RAS/CAS, Fast-Paged, or Static Column memory upto 8MB. See the jumpers section for necessary jumper changes when upgrading memory.