

Quick Reference Table

This table helps you quickly find information on specific jumpers and connectors.

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Mainboard Component Locations

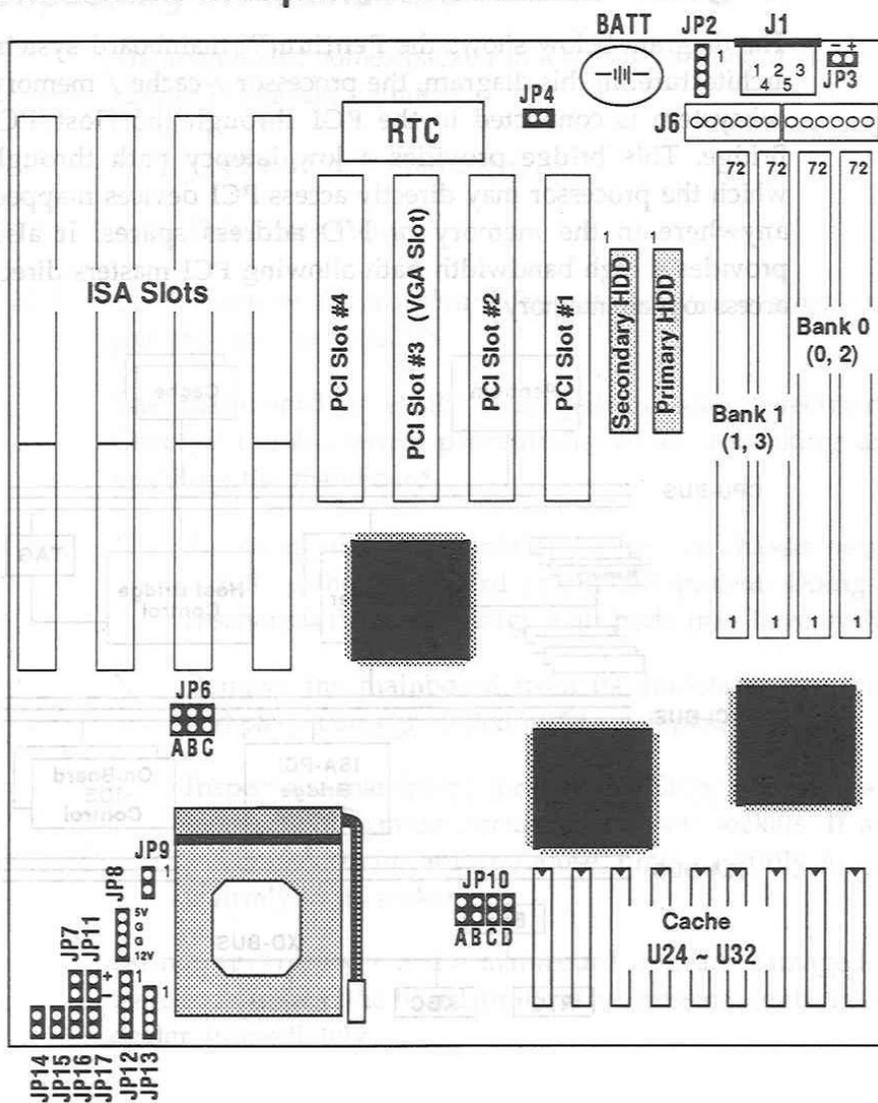


Figure 1-1. Mainboard Component Locations

Chapter 2

Hardware Configuration

Before you install the Pentium™ mainboard into the system chassis, you may find it convenient to first configure the mainboard's hardware. This chapter describes how to set jumpers and install memory modules, and where to attach components.

Power Precautions

Before you begin configuration, make sure you are working with an unplugged mainboard. Many components are powered by low-voltage current, but there still may be a dangerous electric current coming from the leads and power supply. You should take the following precautions:

- Turn off the power supply, and unplug the power cord before you begin.
- Unplug all cables that connect the mainboard to any external devices.

J11 - Secondary Hard Disk Connector

J12 - Primary Hard Disk Connector

Each Enhanced IDE port lets you attach two IDE Bus devices, such as hard disks, CD ROMs, and Tape drives.

JP2-External Battery Connector

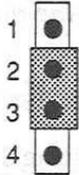
You can attach an external battery to JP2. The default setting is 2-3, for using the internal battery.

Pin	Description
1	External Battery Positive
2	Internal Battery Positive
3	Connect to CMOS
4	Ground

JP2 - CMOS RAM Discharge Jumper

Jumper JP2 lets you discharge the mainboard's CMOS memory and Real Time Clock (RTC). The CMOS memory maintains the system configuration information that is discussed in Chapter 3; the RTC provides the system with the date and time.

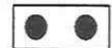
You should set Discharge mode for only a moment when you wish to discharge CMOS, and then make sure this jumper is set for Internal battery mode or connect an External battery to retain your new settings.

Description	JP2
External battery	Connect an external battery to pins 1-4
Internal battery Mode	
Discharge CMOS	

JP4 - Dallas 12887A CMOS Data Clear Jumper

If the mainboard has a Dallas 12887A installed, you can use JP4 to clear the mainboard's CMOS memory and Real Time Clock (RTC). The CMOS memory maintains the system configuration information that is discussed in Chapter 3; the RTC provides the system with the date and time.

You should set CMOS Data Clear mode for only a moment when you wish to clear CMOS, and then make sure this jumper is set for Normal mode to retain your new settings.

Description	JP4
Normal mode	
CMOS Data Clear	

JP7 - Flash EPROM Write Protect Jumper

Short JP7 to program 12-volt Flash ROM. Open JP7 to write-protect the Flash ROM programming.

Description	JP7
Write protect for Flash ROM or EPROM	
12 volt Flash programming	

Caution: Do not short JP7 if you are using EPROM. This may cause the EPROM to lose data.

CPU Installation

You install the CPU in the ZIF socket provided on the mainboard. Refer to Figure 1-1 for the location of the CPU.

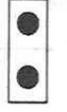
Install the CPU as follows:

Caution: Static electricity can seriously damage the CPU.

1. Review the section on static electricity precautions at the beginning of this manual.
2. Locate the CPU socket on the mainboard.
3. Make sure the ZIF socket arm is up in the "open" position.
4. Align the pins of the CPU to match the ZIF socket holes. Make sure that pin 1 of the CPU aligns with pin 1 of the ZIF socket.
5. Insert the CPU into the ZIF socket.
6. Press the ZIF socket arm downwards until it snaps into place at the side of the socket.
7. If you use a P54C CPU make sure that you Open jumper JP9 and that you set the CPU frequency with jumper JP6 as shown below. CPU installation is complete.

JP6 – CPU Speed Jumpers

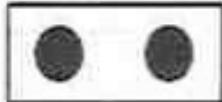
The mainboard has a clock generator that lets you choose the CPU frequency by settings jumper JP6. Set the CPU speed as shown below.

CPU clock	JP6		
	A	B	C
75 MHz			
90 MHz			
100 MHz			
120 MHz			
132 MHz			

Note: For the 120 MHz and 132 MHz speeds, you must use a P54CS CPU and Short jumper JP9.

JP9 – CPU Clock Multiplier Jumper

For the CPU clock multiplier, set jumper JP9 as shown below.

Clock Multiplier	JP9
P54C (X 1.5)	
P54CS (X 2)	