Switch Settings for MXA386-33 System Board

| Memory installed | SW1 | SW2 | SW3 |
|---|---|---|---|
| 1MB(4 SIMMs, 256K) | OFF | ON | OFF |
| 2MB(8 SIMMs, 256K) | ON | ON | OFF |
| 4MB(4 SIMMs, 1024K) | OFF | OFF | ON |
| 8MB(8 SIMMs, 1024K) | ON | OFF | ON |
| 16MB(4 SIMMs, 4096K) | OFF | ON | ON |
| 32MB(4 SIMMs, 4096K) | ON | ON | ON |

| Position | default | Function | |
|---|---|---|---|
| SW4 | OFF | (location 0C0000-0CFFFF) | OFF=Cache<br>ON =No Cache |
| SW5 | ON | (location 0D0000-0DFFFF) | OFF=Cache<br>ON =No Cache |
| SW6 | OFF | (location 0E0000-0FFFFF) | OFF=Cache<br>ON =No Cache |
| Sw7 | OFF | (F00000-FFFFFF) | OFF=ISA Bus Memory<br>ON =No ISA Bus Memory |
| SW8 | ON | ON =No 80387 installed<br>OFF=80387 installed | |
| SW9 | OFF | ON =Primary display color<br>OFF=Primary display monochrome | |
| SW10 | ON | ON =Cache Enable<br>OFF=Cache Disable | |

* Uses 20, 25 or 33 Mhz 80386 microprocessor and 80387 and/or Weitek 3167 co-processor only

* The memory is arranged in 2 Banks (0 and 1) of 4 SIMM slots each. The 4 SIMM banks closest to the I/O slots (u99 thru u102) are Bank 0

* Use 256KBX9, 1MBX9 or 4MBX9 SIMMs of 80ns or 70ns only