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Switch Settings for MX386-25/33 System Board


 (No Interleave only)  Switch SW1 Settings      Jumper Settings
  Memory installed     SW1  SW2  SW3  SW4    J19  J20  J1*  J2*
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  1MB(4 SIMMs, 256K)   OFF  ON   ON   ON     OUT  2-3  1-2  1-2
  2MB(8 SIMMs, 256K)   OFF  ON   OFF  ON     3-4  OUT  1-2  1-2
  4MB(4 SIMMs, 1024K)  OFF  ON   OFF  OFF    OUT  2-3  1-2  1-2
  8MB(8 SIMMs, 1024K)  OFF  OFF  ON   ON     5-6  OUT  1-2  1-2
 16MB(4 SIMMs, 4096K)  ON   OFF  OFF  ON     OUT  2-3  1-2  1-2
 32MB(4 SIMMs, 4096K)  ON   OFF  OFF  OFF    7-8  OUT  1-2  1-2


Switch SW0 (10 switches labelled SW0):

Sw1: ON =80387 not present
     OFF=80387 present

Sw2: ON=No add on board has memory configured between
       12 MB & 14 MB (default).

Sw3/Sw4: OFF/OFF=Memory up to 32 MB is cached
                 (Must use 128KB Cache)
         OFF/ON =up to 14MB is cached if Sw1 is OFF
         ON/OFF =up to 16 MB is cached (default)
                 (64KB cache only).
         ON/ON  =Upto 4MB Cac.

Sw5/Sw6/Sw7: All ON =80ns for MX386-25
                     60ns for MX386-33
             All OFF=100ns for MX386-25
                     80ns for MX386-33

Sw8: ROM shadowing disabled (default)

Sw9: reserved (not used)
Sw10: reserved (not used)

J3: 1-2 Monochrome
    2-3 Color

J4: 1-2 Disable I/O bus DRAM refresh (default)
    OUT Enable

J6: 1-2 Low speed
    OUT High speed

J9: 1-2 Cache disabled
    OUT Enabled
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