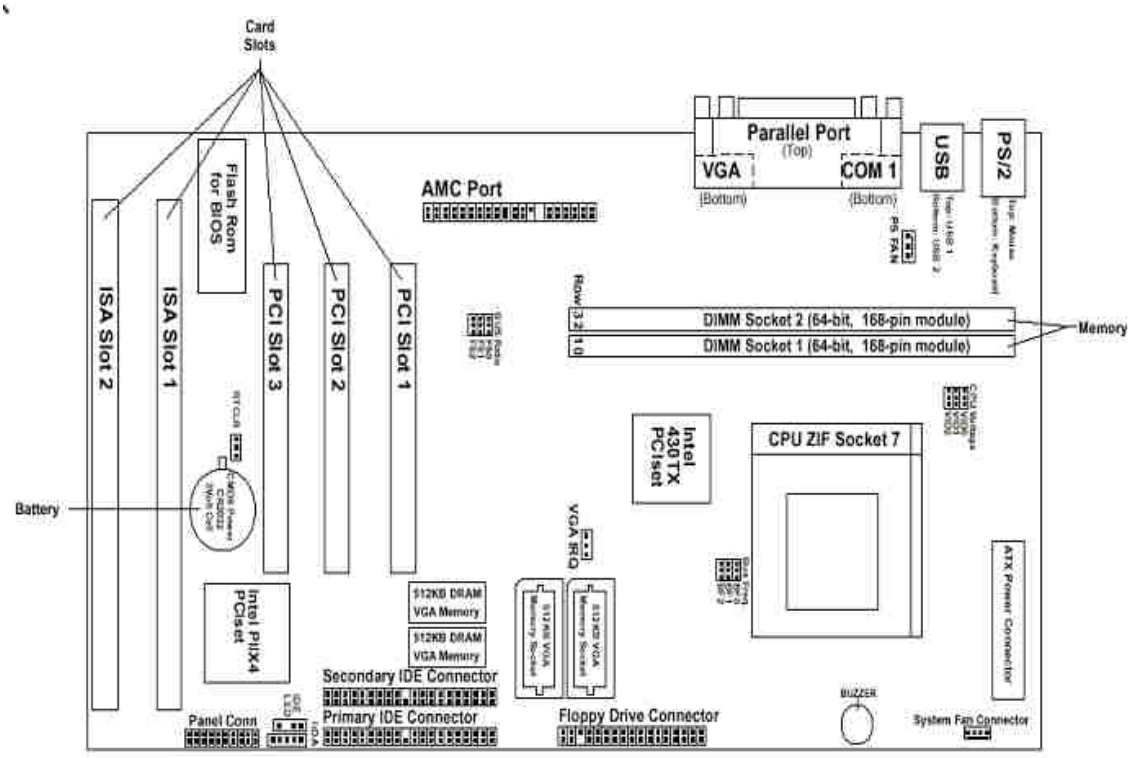
The Wayback Machine - https://web.archive.org/web/20140910160803/http://h10025.www1.hp.com/ewfrf/wc/document?cc=us&dlc=en&docname=bph…

Motherboard layout and jumper settings for the 430TX chipset models 8240 / 8246

Motherboard Layout

Figure 1: Motherboard Layout



Back to top

Real Time Clock Jumper settings

Contact HP

Product (e.g. Deskjet D2680)

Question or keywords

Tips

We are currently updating our customer support system and are unable to process your service order at this time Please check back after September 14th (10:00 AM MT) for your order status

Extend your warranty with HP Care Pack Services

out the HP Total Care Support Videos

ted support links

warranty status

repair order

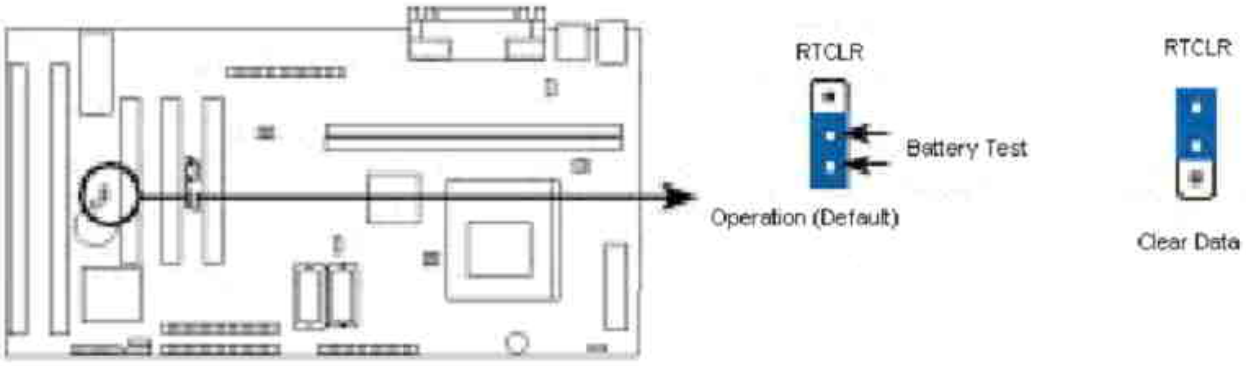
upport forums

olutions and collaborate with others on Support Forum

n the conversation

Follow us

Figure 2: RTCLR Jumper locations and settings



RTCLR Jumper Location and Settings

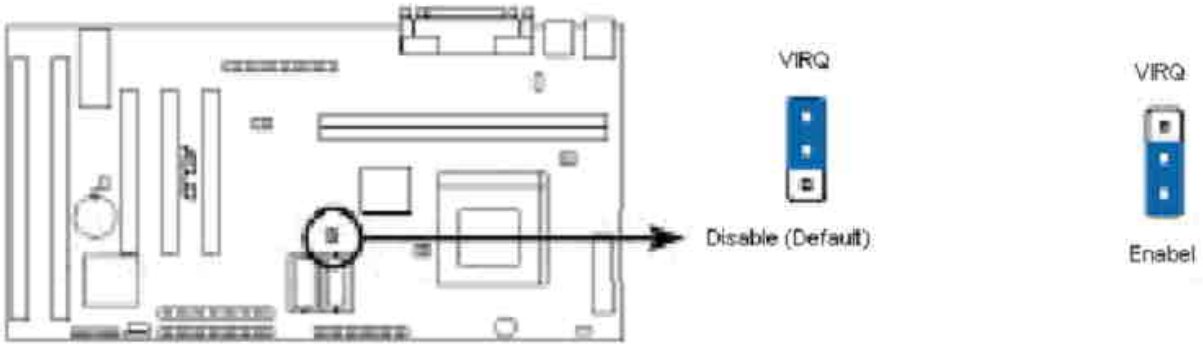
RTC RAM	RTCLR
Operation	2-3 (default)
Clear Data	1-2 momentarily

Real Time Clock Jumper Settings

Back to top

VGA interrupt jumper settings

Figure 3: VGA interrupt jumper settings and locations



VGA Interrupt Jumper Locations and Settings

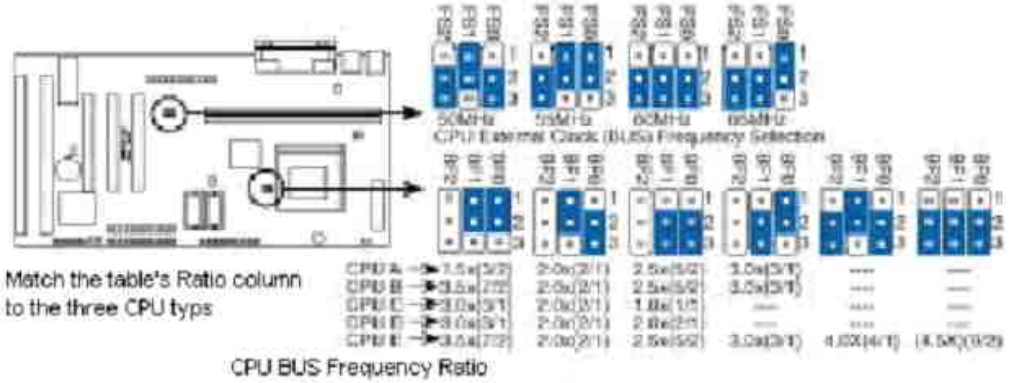
VIRQ	Setting
Disable	1-2 (default)
Enable	2-3

VGA Interrupt Jumper Settings

Back to top

CPU/System settings

Figure 4: CPU Jumper locations and settings



CPU Jumper Locations and Settings

CPU Freq. (MHz)	Ratio	Bus Freq. (MHz)	BUS Frequency FS2	FS1	FS0	Frequency Ratio BF2	BF1	BF0
233	3.5x	66	2-3	2-3	1-2	-----	1-2	1-2
200	3.0x	66	2-3	2-3	1-2	-----	2-3	1-2
166	2.5x	66	2-3	2-3	1-2	-----	2-3	2-3

CPU/SYSTEM speed settings

Back to top

Primary power and power supply fan connector

Figure 5: Primary power/power supply fan connector

PRIMARY POWER

PRIMARY POWER

Pin	Signal Name	Pin	Signal Name
1	+3.3 V	11	+3.3 V
2	+3.3 V	12	-12 V
3	Ground	13	Ground
4	+5 V	14	PW_ON
5	Ground	15	Ground
6	+5 V	16	Ground
7	Ground	17	Ground
8	PWRGD (Power Good)	18	-5 V
9	+5 VSB (Standby for real-time clock)	19	+5 V
10	+12 V	20	+5 V

Primary Power Connector

POWER SUPPLY FAN CONNECTOR

Pin	Signal Name
1	Ground
2	+12 V
3	Ground

Power Supply Fan Connector

Back to top

System fan connector

Figure 6: System fan connector

SYSTEM FAN CONNECTOR

Pin	Signal Name
1	+12 V
2	Ground
3	FAN_SEN

Back to top

Front panel connector

Figure 7: Front panel connector

Front Panel Connector

The front panel connector includes headers for the following connections; Power LED, Speaker, Reset switch, Power switch., Sleep switch.

Pin	Signal Name	Connector
1	no connection	-
2	+5V	Message LED
3	MSG LED	
4	SMI	SMI Suspend
5	ground	Switch Lead
6	PWR SW	ATX Power
7	ground	Switch
8	no connection	-
9	RESET	Reset
10	ground	Switch
11	SPKR	Speaker
12	ground - not used	
13	ground - not used	
14	+5V	Connection
15	no connection	-
16	no connection	-
17	no connection	-
18	PWR LED	Power
19	no connection	-
20	+5V	LED

Back to top

Parallel port

Figure 8: Parallel port

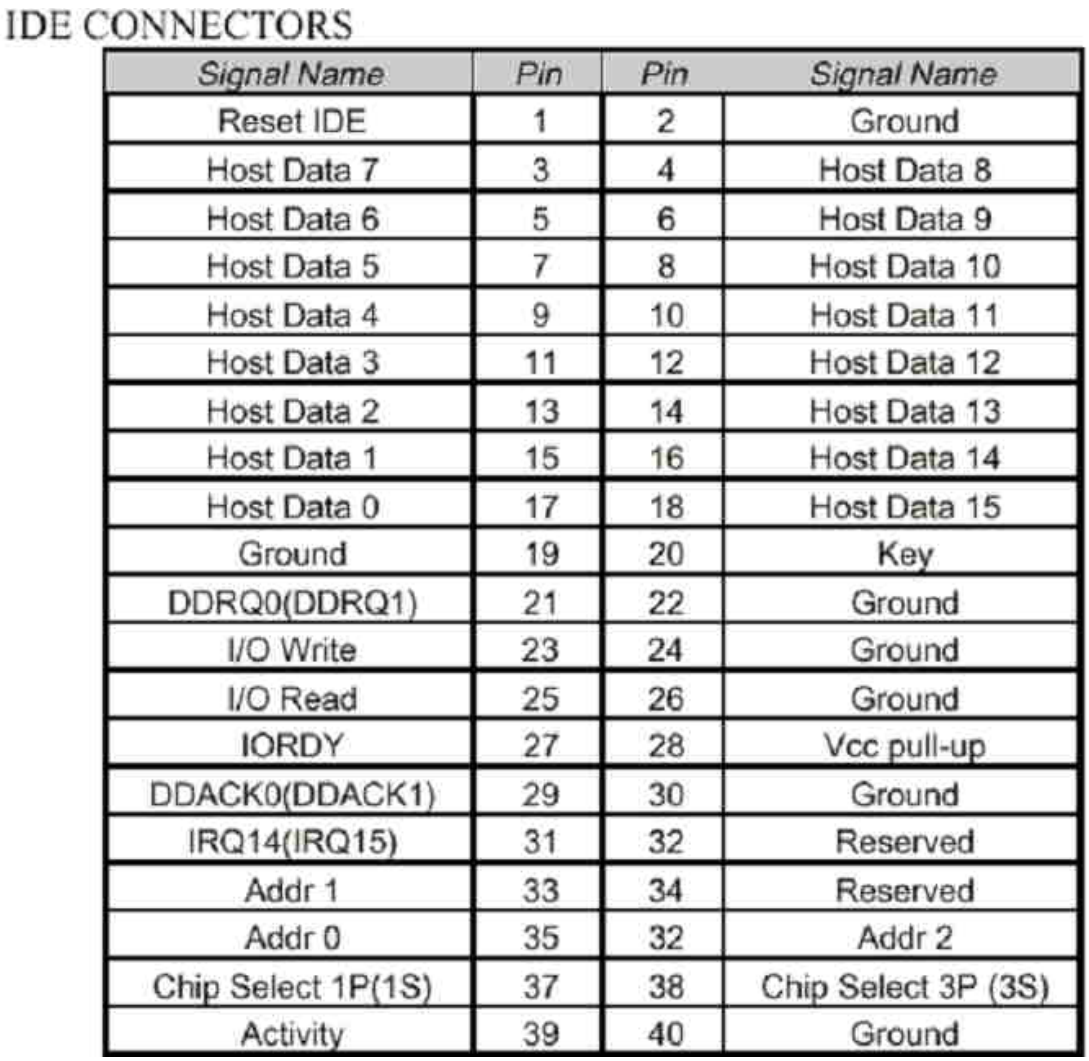
PARALLEL PORT

Signal Name	Pin	Pin	Signal Name
STROBE-	1	14	AUTO FEED
Data Bit 0	2	15	ERROR*
Data Bit 1	3	16	INIT*
Data Bit 2	4	17	SELECT IN*
Data Bit 3	5	18	Ground
Data Bit 4	6	19	Ground
Data Bit 5	7	20	Ground
Data Bit 6	8	21	Ground
Data Bit 7	9	22	Ground
ACK*	10	23	Ground
BUSY	11	24	Ground
Error	12	25	Ground
SELECT	13		

Back to top

IDE connectors

Figure 9: IDE connectors


IDE CONNECTORS

Signal Name	Pin	Pin	Signal Name
Reset IDE	1	2	Ground
Host Data 7	3	4	Host Data 8
Host Data 6	5	6	Host Data 9
Host Data 5	7	8	Host Data 10
Host Data 4	9	10	Host Data 11
Host Data 3	11	12	Host Data 12
Host Data 2	13	14	Host Data 13
Host Data 1	15	16	Host Data 14
Host Data 0	17	18	Host Data 15
Ground	19	20	Key
DDRQ0(DDRQ1)	21	22	Ground
I/O Write	23	24	Ground
I/O Read	25	26	Ground
IORDY	27	28	Vcc pull-up
DDACK0(DDACK1)	29	30	Ground
IRQ14(IRQ15)	31	32	Reserved
Addr 1	33	34	Reserved
Addr 0	35	32	Addr 2
Chip Select 1P(1S)	37	38	Chip Select 3P (3S)
Activity	39	40	Ground

Back to top

Floppy connector

Figure 10: Floppy connector

FLOPPY CONNECTOR

FLOPPY CONNECTOR

Signal Name	Pin	Pin	Signal Name
Ground	1	2	DENSEL
Ground	3	4	Reserved
Key	5	6	FDEDIN
Ground	7	8	Index
Ground	9	10	Motor Enable A
Ground	11	12	Drive Select B
Ground	13	14	Drive Select A
Ground	15	16	Motor Enable B
MSEN1	17	18	DIR
Ground	19	20	STEP
Ground	21	22	Write Data
Ground	23	24	Write Enable
Ground	25	26	Track 00
MSEN0	27	28	Write Protect
Ground	29	30	Read Data
Ground	31	32	Side 1 Select
Ground	33	34	Diskette Change

Back to top

United States
Mobile version

About HP
Contact us
Newsroom
Investor relations
Living Progress
Accessibility
Events
HP Labs
Jobs

Social Media
Consumer support forum
Enterprise business community
Corporate blogs

HP Partners
Become a partner
Find a reseller
PartnerOne

Customer Support
Power cord replacement
Download drivers
Register your product
Authorized service providers
Training & certification
Product recycling

Home | Email sign-up | Site map | Privacy | Cookies & ad choices | Terms of use | Recalls
© 2014 Hewlett-Packard Development Company, L.P.

View mobile site