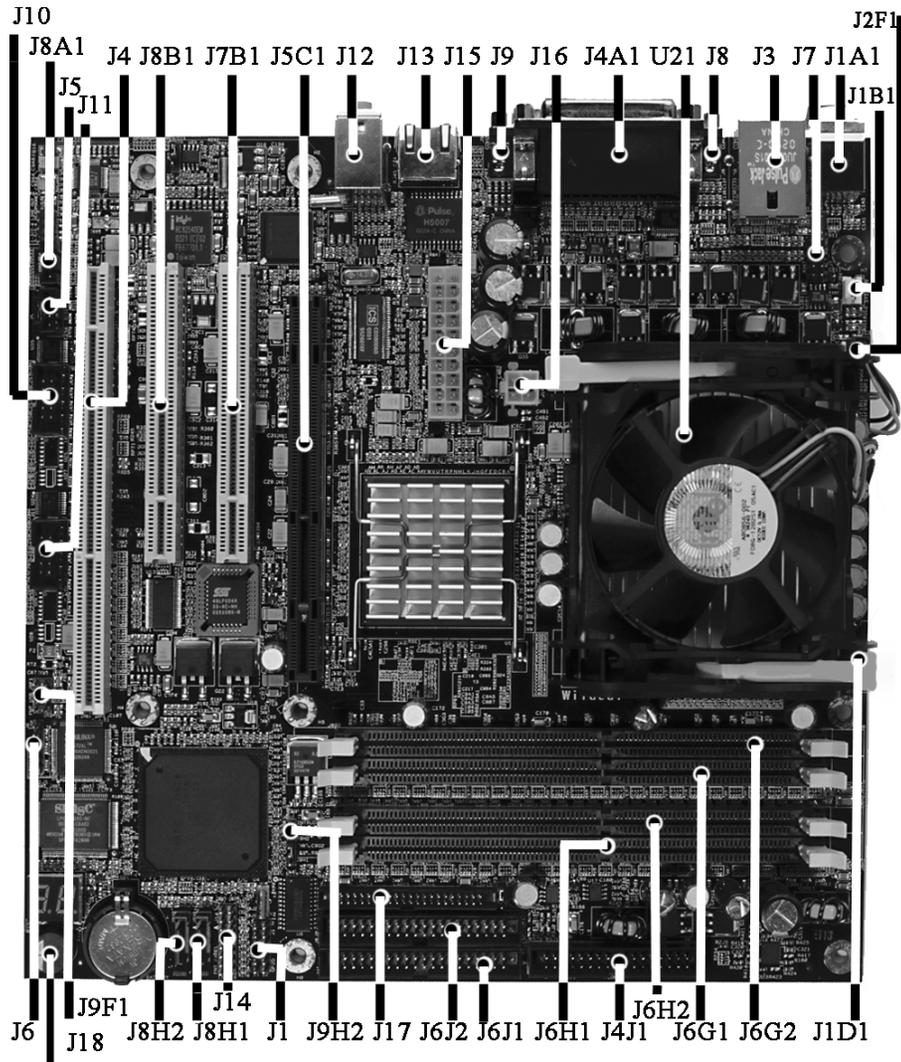
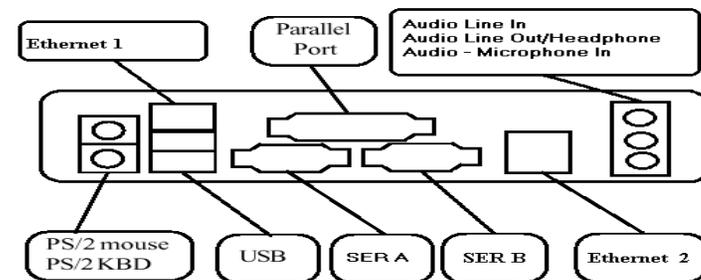


This quick installation guide was designed to help with the basic configuration for the Phoenix MicroATX. More detailed information can be found in the installation guide.



J4A1	LPT - Parallel
J4J1	Floppy Disk Drive Connector
J5	Audio - Aux. In Header
J5C1	AGP PRO Slot
J6	JTAG
J6G1	DDR Channel A DIMM Socket 1
J6G2	DDR Channel A DIMM Socket 0
J6H1	DDR Channel B DIMM Socket 1
J6H2	DDR Channel B DIMM Socket 0
J6J1	Primary IDE
J6J2	Secondary IDE
J7	Keyboard/Mouse Header
J7B1	PCI Connector 1
J8	SER A
J8A1	Audio – CD In Header
J8B1	PCI Connector 2
J8H1	SATA 1
J8H1	SATA 1
J9	SER B
J9F1	USB Header (Ports 2 & 3)
J9H2	Intruder detection Header
J10	SER C
J11	SER D
J12	Audio – Mic. In (pink), Line Out/Headphone (green) and Line In (blue)
J13	Ethernet 2 10/100/1000 (Optional) RJ45
J14	Front Panel Header
J15	ATX Power Connector
J16	ATX Power Aux. Connector
J17	Alt. Secondary IDE
J18	Buzzer/Speaker Alt. Connector
U21	CPU Socket

Connector	Description
J1	GPIO Header
J1A1	(Bottom)Keyboard – PS/2 (Top)Mouse – PS/2
J1B1	Rear Chassis Fan
J1D1	ITP
J2F1	CPU Fan
J3	(Bottom) USB (Ports 0 & 1) - (Top) Ethernet 1 (Optional 1Gb)
J4	PCI-X Connector



Back Panel

## System Memory Features:

- 2.6 V (only) 184-pin DDR SDRAM DIMMs with gold-plated contacts.
- Unbuffered, single-sided or double-sided DIMMs with the following restriction:  
- Double-sided DIMMs with x16 organization are not supported.
- Maximum total system memory: 4 GB; minimum total system memory: 64 MB .
- DDR266 MHz (PC2100), DDR333 MHz (PC2700) and DDR400 MHz (PC3200) DDR SDRAM DIMMs only.
- Serial Presence Detect (SPD).
- ECC and non-ECC DIMMs supported.

### Supported System Bus Frequency and Memory Speed Combinations

To use this type of DIMM...	The processor's system bus frequency must be...
DDR400	800 MHz
DDR333	800 or 533 MHz (Note)
DDR266	800, 533 or 400 MHz

Note: When using an 800 MHz system bus frequency processor, DDR333 memory is clocked at 320 MHz. This minimizes system latencies to optimize system throughput.

DIMM Capacity	# of Dev./ DIMM	# of Sides	DRAM Tech.	Front Side Population		Back Side Population	
				Count	Config	Count	Config
64 MB	8	SS	64 Mbit	8	8 M x 8		
64 MB	4	SS	128 Mbit	4	8 M x 16		
128 MB	16	DS	64 Mbit	8	8 M x 8	8	8 M x 8
128 MB	8	SS	128 Mbit	8	16 M x 8		
128 MB	4	SS	256 Mbit	4	16 M x 16		
256 MB	16	DS	128 Mbit	8	16 M x 8	8	16 M x 8
256 MB	8	SS	256 Mbit	8	32 M x 8		
256 MB	4	SS	512 Mbit	4	32 M x 16		
512 MB	16	DS	256 Mbit	8	32 M x 8	8	32 M x 8
512 MB	8	SS	512 Mbit	8	64 M x 8		
1024 MB	16	DS	512 Mbit	8	64 M x 8	8	64 M x 8

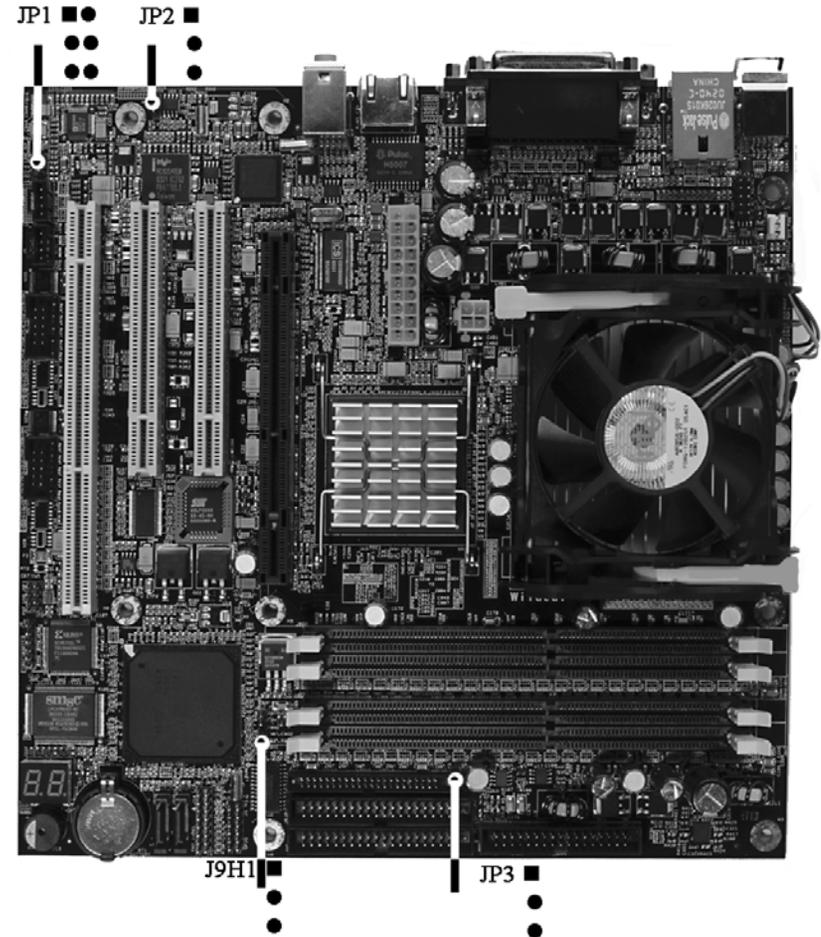
Throughput Level	Configuration	Characteristics
Highest  ↑	Dual Channel with Dynamic Mode	All DIMMs matched
	Dual Channel without Dynamic Mode	<ul style="list-style-type: none"> <li>• DIMMs matched from Channel A to Channel B</li> <li>• DIMMs not matched within channels</li> </ul>
	Single Channel with Dynamic Mode	Single DIMM or DIMMs matched within a channel
Lowest	Single Channel without Dynamic Mode	DIMMs not matched

## PROCESSOR:

The latest revision of the Phoenix MicroATX currently supports full series of Intel Pentium IV (mPGA478 400MHz, 533 MHz and 800MHz PSB) processors.

MN-P4CMX-Q1

## Phoenix MicroATX - Quick Installation Guide



Jumper	Description	Manufacturer's Settings	Jumper Position
JP1	Audio Output Mode	Line Out	3-5, 4-6
JP2	On-Board Ethernet 1 Enabling	Enabled	1-2
JP3	ATA disk voltage	5V	1-2
J9H1	CMOS Reset	Normal	1-2