

# Rocky-538TXV V6.2 jumper & connector manual

Note: X - shorted

## Dual / Single CPU Voltage Setting (JP6, JP7, JP10, JP11):

	JP6	JP7	JP10	JP11
<b>SINGLE</b> (Pentium)			X	X
<b>DUAL</b> (Pentium MMX, AMD K6, Cyrix 6x86MX)	X	X		

## CPU Bus multiplier (JP4):

Multiplier	JP4 5-6	JP4 7-8	JP4 9-10
1.5X / 3.5X			
2X	X		
2.5X	X	X	
3X			X
4X	X		X
4.5X	X	X	X
5X		X	X

## CPU CLK (JP4):

CPU CLK	JP4 3-4	JP4 11-12
55 MHz	X	X
60 MHz	X	
66 MHz		
75 MHz		X

## CPU Core Voltage (JP12):

CPU Core Voltage	JP12 1-2	JP12 3-4	JP12 5-6	JP12 7-8
3.5 V	X	X	X	X
3.4 V		X	X	X
3.3 V	X		X	X
3.2 V			X	X
3.1 V	X	X		X
3.0 V		X		X
2.9 V	X			X
2.8 V				X
2.7 V	X	X	X	
2.6 V		X	X	
2.5 V	X		X	
2.4 V		X	X	
2.3 V	X	X		
2.2 V				
2.1 V	X			

## PS/2 Mouse IRQ12 Setting (JP16):

The on board PS/2 mouse will use IRQ12 in operation.

	JP16
IRQ12 Enable for PS/2 Mouse Operating	X
PS/2 Mouse Disable. IRQ12 to bus	

## Watch-Dog Timer (JP13, JP16):

The Watch-Dog Timer is enabled by reading port 443H. It should be triggered before the time-out period ends, otherwise it will assume that program operation is abnormal and will issue a reset signal to start again, or activate NMI to CPU. The Watch-Dog Timer is disabled by reading port 043H

### Watch-dog Active type setting (JP13)

	JP13
Disable WDT	Open
Activate NMI to CPU when WDT time-out	1-2
Reset when WDT time-out	2-3

### WDT Time-out period (JP15)

Period	JP15 1-2	JP15 3-4	JP15 5-6	JP15 7-8
1 sec.			X	
2 sec.			X	X
10 sec.		X		
20 sec.		X		X
110 sec.	X			
220 sec.	X			X

### DiskOnChip Memory Address Setting (JP9):

Address	JP9
CE000	1-2
D6000	3-4
DE000	5-6
E0000	7-8

### Clear CMOS Setup (JP1):

	JP1
Normal	1-2
Clear CMOS	2-3

### Bios Flash Chip Write Voltage Setting (JP14):

	JP14
12V Flash Write Voltage	1-2
5V Flash Write Voltage*	2-3

\* - set by factory on 538TXV V6.2

### Factory Default Jumpers (JP17, JP18):

As for 538TXV V6.2

JP17	JP18
1-2	2-3

# Connectors

## CN13: COM2

PIN	DESCRIPTION	PIN	DESCRIPTION
1	DCD	6	DSR
2	RXD	7	RTS
3	TXD	8	CTX
4	DTR	9	RI
5	GND	10	NC

## CN6: External keyboard connector

PIN	DESCRIPTION
1	KEYBOARD CLOCK
2	KEYBOARD DATA
3	N/C
4	GROUND
5	+5V

## JP3: Front panel connector

PIN	DESCRIPTION	PIN	DESCRIPTION
2	+5V (POWER LED)	1	SPEAKER SIGNAL
4	N/C	3	N/C
6	GROUND	5	N/C
8	KEYLOCK SIGNAL	7	+5V
10	GROUND	9	RESET
12	GROUND	11	GROUND
14	N/C	13	HDD ACTIVE#
16	PS_ON	15	+5V
18	ATX power button	17	ATX power button
20	5V STANDBY	19	GROUND

## CN8: External Battery Connector

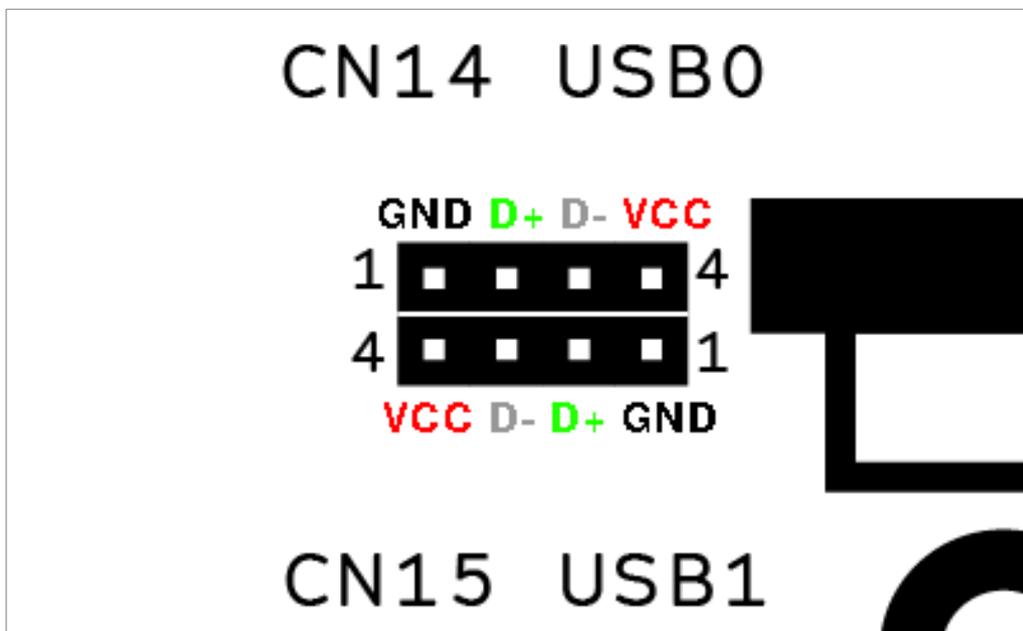
For internal battery, connect 1-2.

PIN	DESCRIPTION
1	External Battery +
2	Internal Battery +
3	N/C
4	Ground

## CN14, CN15: USB Ports

Warning - CN14 (upper) first pin is at the left, while CN15 (bottom) first pin is at the right!

PIN	DESCRIPTION
1	GND
2	DATA+
3	DATA-
4	VCC



## CN5: IrDA Infrared Interface Port

The ROCKY-538TXV has a built-in IrDA port which supports Serial Infrared (SIR) or Amplitude Shift Keyed IR (ASKIR) interface. When in use, the IrDA port must be set to SIR or ASKIR model through BIOS's Peripheral Setup's COM2. Then the normal RS-232 COM2 will be disabled.

PIN	DESCRIPTION
1	VCC
2	FIR-RX
3	IR-RX
4	GND
5	IR-TX
6	CIR-RX

## CN18: External VGA connector

PIN	DESCRIPTION	PIN	DESCRIPTION
1	RED	2	GND
3	GREEN	4	GND
5	BLUE	6	GND
7	H-SYNC	8	GND
9	V-SYNC	10	GND

## CN7, CN9: Fan connectors

CN7 – CPU fan, CN9 – Chassis fan

PIN	DESCRIPTION
1	Signal
2	12V
3	GND