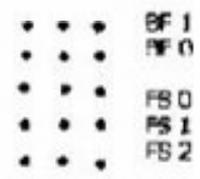
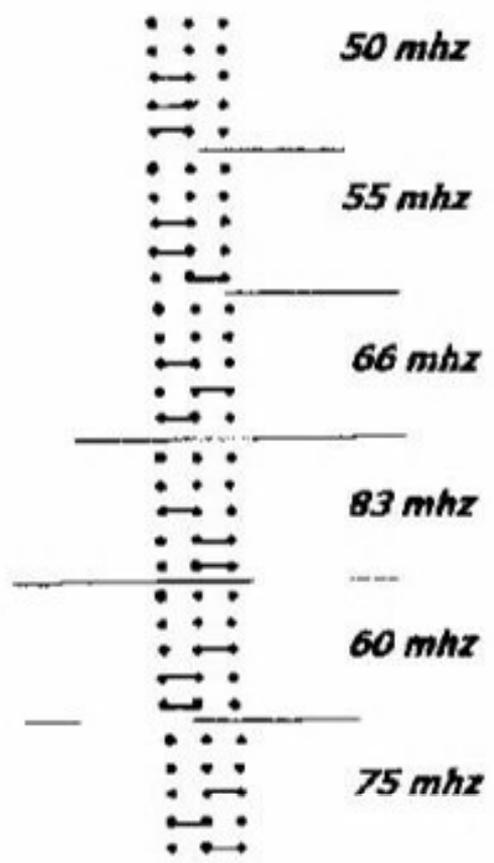
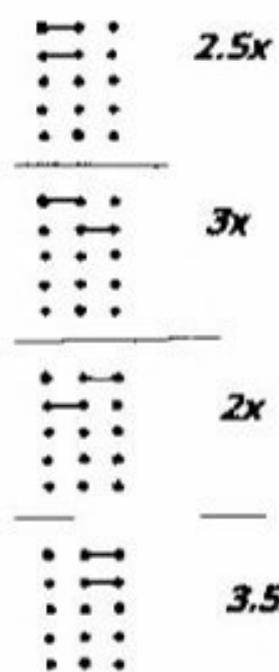


1 Definitions:  
2 A zero means jumper left (1-2) , a one means jumper right (2-3)  
3 I-133 is an Intel Pentium 133 processor (non MMX)  
4 C-686 is a Cyrix 6-86mx processor, pr200; actually runs at  $75 \times 2 = 150$  MHz, has MMX  
support per Cyrix  
5 C-MII is a Cyrix MII processor, 333gp; actually runs at  $83 \times 3 = 250$  (roughly) has MMX  
support per Cyrix  
6 A plus sign next to the speed represents " pr " meaning it's not actually running at  
that speed but slower  
7 " nob " means No Boot  
8 JP10 open = 2.8V core JP10 closed is 3.3V core. For Pentium MMXs open JP10.  
9 As you can see, Cyrix processors make good guinea pigs.  
10  
11 The raw data is provided first for your own reference. The diagram was made up in Paint  
(sorry, I'm no artist) and is based on my conclusions from the information obtained  
during the time spent on the bench. Processor voltage data will be posted here  
(someday) when I get a chance to spend more time with this board.  
12  
13 Again, use this information at your own risk.  
14

## mbojumpers.txt

Jumpers	I-133	C-686	C-MII
00000	100	50	125
00001	110	50	166+
00010	133	90+	166
00011	166	90+	210
00100	120	90+	200+
00101	150	90+	187
00110	nob	nob	nob
00111	nob	nob	nob
01000	75	nob	200+
01001	83	nob	166
01010	100	nob	200
01011	125	nob	250
01100	90	nob	100
01101	110	nob	225
01110	nob	nob	nob
01111	nob	nob	nob
10000	100	120+	120+
10001	110	133+	133+
10010	133	166+	166+
10011	166	200+	166
10100	120	150+	150+
10101	150	200+	200+
10110	nob	nob	nob
10111	nob	nob	nob
11000	75	166+	166
11001	83	200+	200
11010	100	nob	233
11011	125	nob	266
11100	90	200+	210
11101	110	nob	266
11110	nob	nob	nob
11111	nob	nob	nob

Leftmost 2 digits are multiplier  
Rightmost 3 digits are bus speed



These jumpers are at the front of the board, close to the SIMM slots