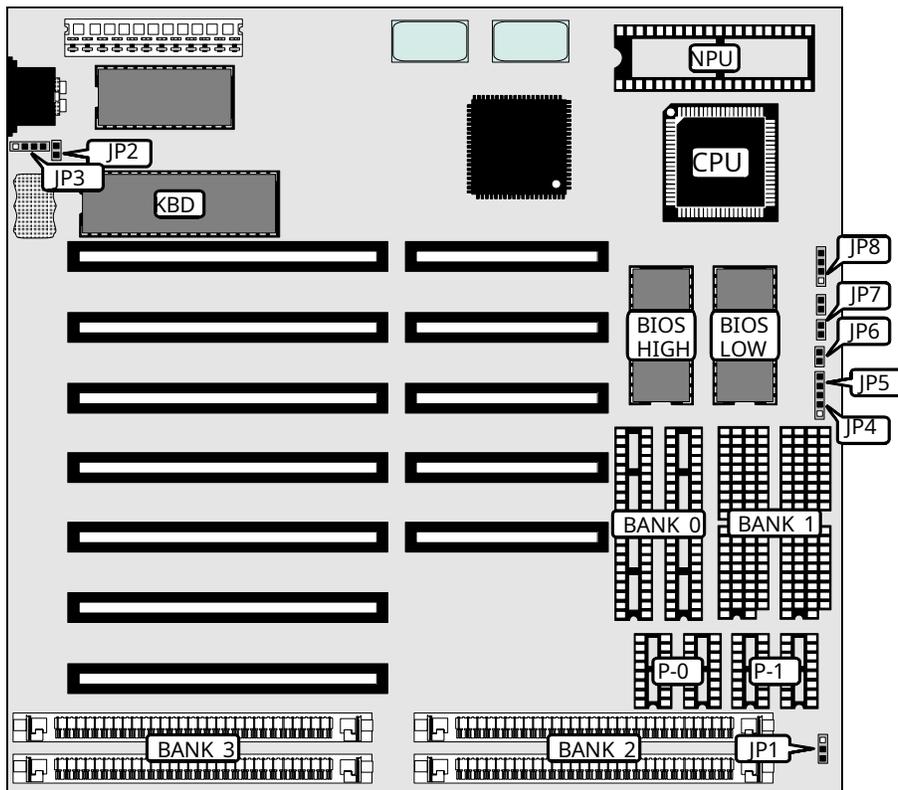


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HT 12 286

Processor 80286
Processor Speed 12/16MHz
Chip Set Headland
Max. Onboard DRAM 4MB
Cache None
BIOS AMI
Dimensions 220mm x 220mm
I/O Options None
NPU Options 80287



CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo switch	JP6	Speaker	JP8
Turbo LED	JP5	Power LED & keylock	JP4
Reset switch	JP7	External battery	JP3

Continued on next page . . .

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	JP1	N/A
í unknown function	JP2	

DRAM CONFIGURATION						
Size	Bank 0	P-0	Bank 1	P-1	Bank 2	Bank 3
512KB	(4) 44256	(2) 41256	NONE	NONE	NONE	NONE
640KB	(4) 44256	(2) 41256	(4) 4464	(2) 41256	NONE	NONE
1MB	(4) 44256	(2) 41256	(4) 44256	(2) 41256	NONE	NONE
2MB	NONE	NONE	NONE	NONE	(2) 1M x 9	NONE
4MB	NONE	NONE	NONE	NONE	(2) 1M x 9	(2) 1M x 9