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↑ Mainboard HOT-571

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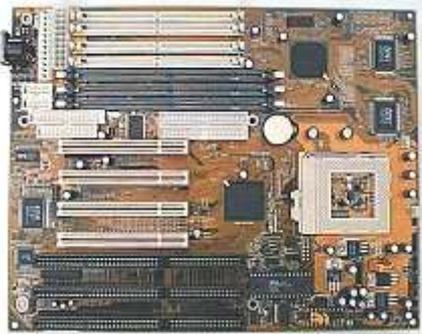
↑HOT-571 v1.x configuration tables

Subchapter: CPU table | Automatic CPU voltage detection | Manual CPU voltage setting in Bios Setup | Manual CPU voltage setting via jumper | system clock | multiplier | Miscellaneous Settings |

↑CPU table

Processor settings If the BIOS does not display the CPU correctly, a BIOS update is required.					
processor	CPU Class	System rate	Multiplier _	Vcore	Vio
Intel Pentium P55C MMX	233 MHz	66 MHz	3,5 x	2.8V	3.3V
Intel Pentium P55C MMX	200 MHz	66 MHz	3 x	2.8V	3.3V
Intel Pentium P55C MMX	166 MHz	66 MHz	2,5 x	2.8V	3.3V
Intel Pentium P54C	200 MHz	66 MHz	3 x	3.3V	3.3V
Intel Pentium P54C	166 MHz	66 MHz	2,5 x	3.3V	3.3V
Intel Pentium P54C	150 MHz	60 MHz	2,5 x	3.3V	3.3V
Intel Pentium P54C	133 MHz	66 MHz	2 x	3.3V	3.3V
Intel Pentium P54C	120 MHz	60 MHz	2 x	3.3V	3.3V
Intel Pentium P54C	100 MHz	66 MHz	1,5 x	3.3V	3.3V
Intel Pentium P54C	90 MHz	60 MHz	1,5 x	3.3V	3.3V
Intel Pentium P54C	75 MHz	50 MHz	1,5 x	3.3V	3.3V
AMD K6	266 MHz	66 MHz	4 x	2.1V	3.3V
AMD K6	233 MHz	66 MHz	3,5 x	3,2V	3.3V
AMD K6	200 MHz	66 MHz	3 x	2.9V	3.3V
AMD K6	166 MHz	66 MHz	2,5 x	2.9V	3.3V
AMD K5	PR166	66 MHz	2,5 x	3,52V	3,52V
AMD K5	PR133	66 MHz	2 x	3,52V	3,52V
AMD K5	PR120	60 MHz	2 x	3,52V	3,52V
AMD K5	PR100	66 MHz	1,5 x	3,52V	3,52V

AMD K5	PR90	60 MHz	1,5 x	3,52V	3,52V
AMD K5	PR75	50 MHz	1,5 x	3,52V	3,52V
Cyrix/IBM 6x86MX ****)**	PR266	66 MHz	3,5 x	2.9V	3.3V
Cyrix/IBM 6x86MX ****)**	PR233	75 MHz *) 66 MHz	2,5 x 3 x	2.9V	3.3V
Cyrix/IBM 6x86MX ****)**	PR200	75 MHz *) 66 MHz	2 x 2,5 x	2.9V	3.3V
Cyrix/IBM 6x86MX ****)**	PR166	60 MHz 66 MHz	2,5 x 2 x	2.9V	3.3V
Cyrix/IBM 6x86L	P200+	75 MHz *)	2 x	2.8V	3.3V
Cyrix/IBM 6x86L	P166+	66 MHz	2 x	2.8V	3.3V
Cyrix/IBM 6x86L	P150+	60 MHz	2 x	2.8V	3.3V
Cyrix/IBM 6x86	P200+	75 MHz *)	2 x	3,52V	3,52V
Cyrix/IBM 6x86	P166+	66 MHz	2 x	3,52V	3,52V
Cyrix/IBM 6x86	P150+	60 MHz	2 x	3,52V	3,52V
Cyrix/IBM 6x86	P133+	55 MHz	2 x	3,52V	3,52V
Cyrix/IBM 6x86	P120+	50 MHz	2 x	3,52V	3,52V
IDT-C6	240 MHz	60 MHz	4 x	3,52V	3,52V
IDT-C6	200 MHz	66 MHz	3 x	3,52V	3,52V
IDT-C6	180 MHz	60 MHz	3 x	3,52V	3,52V
IDT-C6	150 MHz	60 MHz	2,5 x	3,52V	3,52V

*) The chipset and PCI bus are overclocked with **a 75 MHz system clock .** Since the clock multiplier of a **Cyrix/IBM 6x86(L) P200+** CPU is fixed at 2, a system clock setting of 75 MHz is unavoidable in this case if the performance of this CPU is to be exhausted.
) With **Cyrix/IBM 6x86MX, the clock setting printed on the CPU must be selected.

↑Automatic CPU voltage detection

The voltage is set automatically when **jumper JP43** is closed and the **CPU Voltage** setting in the Bios **Chipset Features Setup** is set to **Auto .**

JP43

The table above shows the CPU voltages that are set when auto-detected. If the CPU used requires a different voltage, this must be set manually, for example with:

- Intel Pentium P54C (without MMX): the VRE type has a voltage range of 3.4 to 3.6V. Then you have to set 3.52V
- AMD K6 CPU with 233 MHz is also available with Vcore=3.3V.

- Cyrix/IBM 6x86 is also available as a 3.3V type.

↑Manual CPU voltage setting in Bios setup

If **jumper JP43 is closed** , the CPU voltage (Vcore) can be set to values from 2.0 to 3.5V with the **CPU Voltage** option in the **Chipset Features Setup .**
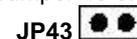
JP43

↑Manual CPU voltage setting via jumper

Example for 3.2V:

The CPU voltage can also be jumpered by opening **jumper JP43** and setting **jumpers 7 through 12 of jumper group SW1 according to the table below . (The CPU Voltage** option in the **Chipset Features Setup** no longer matters.)

Jumper 43 offen

JP43 [● ●]

and
SW1 closed by SW1 JP9

Manual CPU voltage setting		
Vcore	**Vio**	**SW1**
3,52V*)	3,52V*)	

		SW1
3.3V *)	3.3V *)	7 8 9 10 11 12
3,2V	3.3V	7 8 9 10 11 12
3.1V	3.3V	7 8 9 10 11 12
3,0V	3.3V	7 8 9 10 11 12
2.9V	3.3V	7 8 9 10 11 12
2.8V	3.3V	7 8 9 10 11 12

*) If Vcore and Vio are the same, it is a single voltage CPU with a single power supply (in contrast to the dual voltage CPU)

↑System rate

Der Systemtakt wird mit Jumper 1 bis 3 der Jumpergruppe SW1 eingestellt.
Beispiel für "66 MHz" Systemtakt: Jumper 1 bis 3 offen.

Takt-Einstellung für System und PCI-Bus		
Systemtakt	PCI-Bus-Takt	SW1
50 MHz	25 MHz	1 2 3
55 MHz	27,5 MHz	1 2 3
60 MHz	30 MHz	1 2 3
66 MHz	33 MHz	1 2 3
75 MHz *)	37,5 MHz	1 2 3
83 MHz *)	41,5 MHz	1 2 3

*) Bemerkung: Mit den Systemtakt-Einstellungen 75 MHz und 83 MHz wird der Chipsatz übertaktet. Die empfohlene Einstellung ist 66 MHz.

↑ Multiplikator

Das Verhältnis zwischen internem CPU-Takt und dem Systemtakt des Mainboards wird kurz **Multiplikator** bezeichnet. Er wird mit Jumper 4 bis 6 der Jumpergruppe SW1 eingestellt.
Beispiel für Multiplikator "1,5": Jumper 4 bis 6 offen.

Takt-Multiplikator (Verhältnis zwischen System- und CPU-Takt)	
Multiplikator	SW1
1,5	4 5 6
2	4 5 6
2,5	4 5 6
3	4 5 6

3,5	
4	
4,5	
5	
5,5	

↑ Sonstige Einstellungen

CMOS-Speicher löschen - JP40	
normale Position	
CMOS löschen (Rechner dabei ausgeschalten)	

Flash EPROM Programmierspannung - JP19	
12V (Intel, MX)	
5V (SST, Atmel, Winbond)	