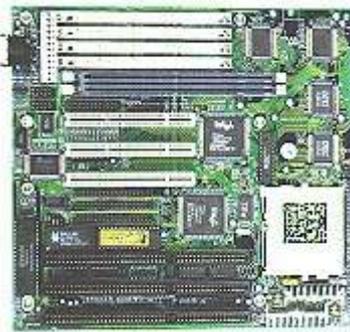
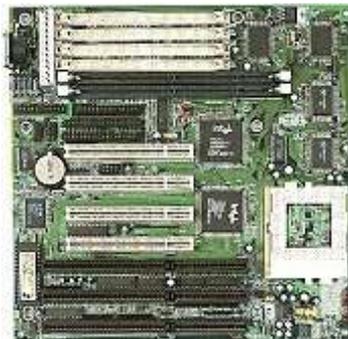


## ↑ Mainboard HOT-555, HOT-555A

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HOT-555 (Version 1.x)



HOT-555A (Version 3.x)

## ↑ HOT-555A V3.0/3.1 Configuration Tables

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### ↑ CPU table

<b>Processor settings</b>					
If the BIOS does not display the CPU correctly, a BIOS update is required.					
processor	CPU Class	System rate	Multiplier	Vcore	Vio
<b>Intel Pentium P55C MMX</b>	233 MHz	66 MHz	3,5 x	2.8V	3.3V
Intel Pentium P55C MMX	200 MHz	66 MHz	3 x	2.8V	3.3V
Intel Pentium P55C MMX	166 MHz	66 MHz	2,5 x	2.8V	3.3V
<b>Intel Pentium P54C</b>	200 MHz	66 MHz	3 x	3,3V oder 3,52V	
Intel Pentium P54C	166 MHz	66 MHz	2,5 x	3,3V oder 3,52V	
Intel Pentium P54C	150 MHz	60 MHz	2,5 x	3,3V oder 3,52V	
Intel Pentium P54C	133 MHz	66 MHz	2 x	3,3V oder 3,52V	
Intel Pentium P54C	120 MHz	60 MHz	2 x	3,3V oder 3,52V	
Intel Pentium P54C	100 MHz	66 MHz	1,5 x	3,3V oder 3,52V	
Intel Pentium P54C	90 MHz	60 MHz	1,5 x	3,3V oder 3,52V	
Intel Pentium P54C	75 MHz	50 MHz	1,5 x	3,3V oder 3,52V	
<b>AMD-K6</b>	233 MHz	66 MHz	3,5 x	3,2V	3.3V
AMD-K6	200 MHz	66 MHz	3 x	2.9V	3.3V
AMD-K6	166 MHz	66 MHz	2,5 x	2.9V	3.3V
<b>AMD-K5</b>	PR166	66 MHz	2,5 x	3,52V	
AMD-K5	PR133	66 MHz	2 x	3,52V	
AMD-K5	PR120	60 MHz	2 x	3,52V	
AMD-K5	PR100	66 MHz	1,5 x	3,52V	

AMD-K5	PR90	60 MHz	1,5 x	3,52V	
AMD-K5	PR75	50 MHz	1,5 x	3,52V	
Cyrix/IBM 6x86MX **)	PR266	66 MHz	3,5 x	2.9V	3.3V
Cyrix/IBM 6x86MX **)	PR233	75 MHz *) 66 MHz	2,5 x 3 x	2.9V	3.3V
Cyrix/IBM 6x86MX **)	PR200	75 MHz *) 66 MHz	2 x 2,5 x	2.9V	3.3V
Cyrix/IBM 6x86MX **)	PR166	60 MHz 66 MHz	2,5 x 2 x	2.9V	3.3V
<b>Cyrix/IBM 6x86L</b>	P200+	75 MHz *)	2 x	2.8V	3.3V
Cyrix/IBM 6x86L	P166+	66 MHz	2 x	2.8V	3.3V
Cyrix/IBM 6x86L	P150+	60 MHz	2 x	2.8V	3.3V
<b>Cyrix/IBM 6x86</b>	P200+	75 MHz *)	2 x	3,3V oder 3,52V	
Cyrix/IBM 6x86	P166+	66 MHz	2 x	3,3V oder 3,52V	
Cyrix/IBM 6x86	P150+	60 MHz	2 x	3,3V oder 3,52V	
Cyrix/IBM 6x86	P120+	50 MHz	2 x	3,3V oder 3,52V	
<b>IDT-C6</b>	200 MHz	66 MHz	3 x	3,52V	
IDT-C6	180 MHz	60 MHz	3 x	3,52V	
IDT-C6	150 MHz	60 MHz	2,5 x	3,52V	

\*) The chipset and PCI bus are overclocked with a **75 MHz system clock** . Since the clock multiplier of a **Cyrix/IBM 6x86(L) P200+** CPU is set to 2, a system clock setting of 75 MHz is unavoidable in this case if the full performance of this CPU is to be exploited.

\*\*\*) With **Cyrix/IBM 6x86MX**, the clock setting printed on the CPU must be selected.

## ↑ CPU voltage

Vcore	Vio	processor	JP39	JP33	JP34	JP43
2.8V	3.3V	Intel Pentium MMX (P55C) Cyrix/IBM 6x86L				
2.9V	3.3V	AMD-K6 166/200 MHz Cyrix/IBM 6x86MX				
3,2V	3,3V	AMD-K6 233 MHz				
3.3V *)		Intel Pentium P54C (STD) Cyrix/IBM 6x86 (3,3V)				
3,52V*)		Intel Pentium P54C (VRE) Cyrix/IBM 6x86 (3,52V) AMD-K5				

\*) If Vcore and Vio are the same, it is a single voltage CPU with a single power supply (in contrast to the dual voltage CPU)

## ↑ System rate

Clock setting for system and PCI bus		
System rate	PCI Bus Rate	JP37
50 MHz	25 MHz	
55 MHz	27,5 MHz	
60 MHz	30 MHz	
66 MHz	33 MHz	



- Sonstige Einstellungen
- Konfiguration v3.2
- Presse

