

PRODUKTE

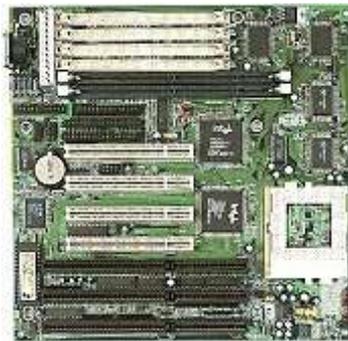
Produktfinder

↑ **Mainboard HOT-555, HOT-555A**

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HOT-555 (Version 1.x)



HOT-555A (Version 3.x)

↑ **HOT-555 V1.5x configuration tables**

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↑ **CPU table**

| Processor settings   |           |             |            |                 |      |
|--|-----------|-------------|------------|-----------------|------|
| If the BIOS does not display the CPU correctly, a BIOS update is required. |           |             |            |                 |      |
| processor  | CPU Class | System rate | Multiplier | Vcore           | Vio  |
| Intel Pentium P55C MMX   | 200 MHz*) | 66 MHz      | 3 x        | 2.8V            | 3.3V |
| Intel Pentium P55C MMX   | 166 MHz   | 66 MHz      | 2,5 x      | 2.8V            | 3.3V |
| <b>Intel Pentium P54C</b>  | 200 MHz   | 66 MHz      | 3 x        | 3,3V oder 3,52V |      |
| Intel Pentium P54C   | 166 MHz   | 66 MHz      | 2,5 x      | 3,3V oder 3,52V |      |
| Intel Pentium P54C   | 150 MHz   | 60 MHz      | 2,5 x      | 3,3V oder 3,52V |      |
| Intel Pentium P54C   | 133 MHz   | 66 MHz      | 2 x        | 3,3V oder 3,52V |      |
| Intel Pentium P54C   | 120 MHz   | 60 MHz      | 2 x        | 3,3V oder 3,52V |      |
| Intel Pentium P54C   | 100 MHz   | 66 MHz      | 1,5 x      | 3,3V oder 3,52V |      |
| Intel Pentium P54C   | 90 MHz    | 60 MHz      | 1,5 x      | 3,3V oder 3,52V |      |
| Intel Pentium P54C   | 75 MHz    | 50 MHz      | 1,5 x      | 3,3V oder 3,52V |      |
| <b>AMD-K5</b>  | PR166     | 66 MHz      | 2,5 x      | 3,52V           |      |
| AMD-K5   | PR133     | 66 MHz      | 2 x        | 3,52V           |      |
| AMD-K5   | PR120     | 60 MHz      | 2 x        | 3,52V           |      |
| AMD-K5   | PR100     | 66 MHz      | 1,5 x      | 3,52V           |      |
| AMD-K5   | PR90      | 60 MHz      | 1,5 x      | 3,52V           |      |
| AMD-K5   | PR75      | 50 MHz      | 1,5 x      | 3,52V           |      |
| <b>Cyrix/IBM 6x86L</b>   | P200+     | 75 MHz **)  | 2 x        | 2.8V            | 3.3V |
| <b>Cyrix/IBM 6x86L</b>   | P166+     | 66 MHz      | 2 x        | 2.8V            | 3.3V |

|                       |         |            |       |                 |      |
|-----------------------|---------|------------|-------|-----------------|------|
| Cyrix/IBM 6x86L       | P150+   | 60 MHz     | 2 x   | 2.8V            | 3.3V |
| <b>Cyrix/IBM 6x86</b> | P200+   | 75 MHz **) | 2 x   | 3,3V oder 3,52V |      |
| Cyrix/IBM 6x86        | P166+   | 66 MHz     | 2 x   | 3,3V oder 3,52V |      |
| Cyrix/IBM 6x86        | P150+   | 60 MHz     | 2 x   | 3,3V oder 3,52V |      |
| Cyrix/IBM 6x86        | P120+   | 50 MHz     | 2 x   | 3,3V oder 3,52V |      |
| <b>IDT-C6</b>         | 200 MHz | 66 MHz     | 3 x   | 3,52V           |      |
| IDT-C6                | 180 MHz | 60 MHz     | 3 x   | 3,52V           |      |
| IDT-C6                | 150 MHz | 60 MHz     | 2,5 x | 3,52V           |      |

\*) Intel Pentium MMX (P55C) 200 MHz on HOT-555 only from version 1.53

\*\*) The chipset and PCI bus are overclocked with a **75 MHz system clock** . Since the clock multiplier of a **Cyrix/IBM 6x86(L) P200+** CPU is set to 2, a system clock setting of 75 MHz is unavoidable in this case if the full performance of this CPU is to be exploited.

## ↑ CPU voltage

| Vcore   | Vio  | processor  | JP4 | JP13 | JP10 | JP36 |
|---------|------|--|-----|------|------|------|
| 2.8V    | 3.3V | Intel Pentium MMX (P55C)<br>Cyrix/IBM 6x86L        |     |      |      |      |
| 3.3V *) |      | Intel Pentium P54C (STD)<br>Cyrix/IBM 6x86 (3,3V)  |     |      |      |      |
| 3,45V*) |      | Intel Pentium P54C (VRE)<br>Cyrix/IBM 6x86 (3,52V) |     |      |      |      |
| 3.6V *) |      | AMD-K5   |     |      |      |      |

\*) If Vcore and Vio are the same, it is a single voltage CPU with a single power supply (in contrast to the dual voltage CPU)

### More voltage settings for dual-voltage CPUs

| Vio   | JP4 | JP13 |
|-------|-----|------|
| 3.3V  |     |      |
| 3,45V |     |      |
| 3.6V  |     |      |

| Vcore | JP10 | JP36 |
|-------|------|------|
| 2,5V  |      |      |
| 2.6V  |      |      |
| 2.7V  |      |      |
| 2.8V  |      |      |
| 2.9V  |      |      |
| 3,0V  |      |      |

## ↑ System rate

The system clock is set with the first three pairs of pins of SW1.

| Clock setting for system and PCI bus |              |                |
|--------------------------------------|--------------|----------------|
| System rate                          | PCI Bus Rate | SW1            |
| 50 MHz                               | 25 MHz       |                |
| 55 MHz                               | 27,5 MHz     |                |
| 60 MHz                               | 30 MHz       |                |
| 66 MHz                               | 33 MHz       |                |
| 75 MHz *)                            | 37,5 MHz     |                |
| 83 MHz *)                            | 41,5 MHz     | not adjustable |

\*) Note: The chipset is overlocked with the system clock settings of 75 MHz and 83 MHz. The recommended setting is 66MHz.

## ↑ multiplier

Das Verhältnis zwischen internem CPU-Takt und dem Systemtakt des Mainboards wird kurz **Multiplikator** bezeichnet und wird mit letzten beiden Pin-Paaren von SW1 eingestellt.

| Takt-Multiplikator<br>(Verhältnis zwischen System- und CPU-Takt) |     |
|--|-----|
| Multiplikator  | SW1 |
| 1,5  |     |
| 2  |     |
| 2,5  |     |
| 3  |     |

## ↑ Sonstige Einstellungen

| CMOS-Speicher löschen - JP21                |  |
|---|--|
| normale Position                            |  |
| CMOS löschen (Rechner dabei ausgeschalten*) |  |

\*) Falls das R.T.C.-Chip DS12B887 bestückt sein sollte, dann schalte man den Rechner bei gesetztem Jumper JP21 für kurze Zeit an.

| Flash EPROM Programmierspannung - JP18 |  |
|--|--|
| 12V (Intel, MX)                        |  |
| 5V (SST, Atmel, Winbond)               |  |

| Paßwort löschen - JP9 |  |
|-----------------------|--|
| normale Position      |  |



Navigation

