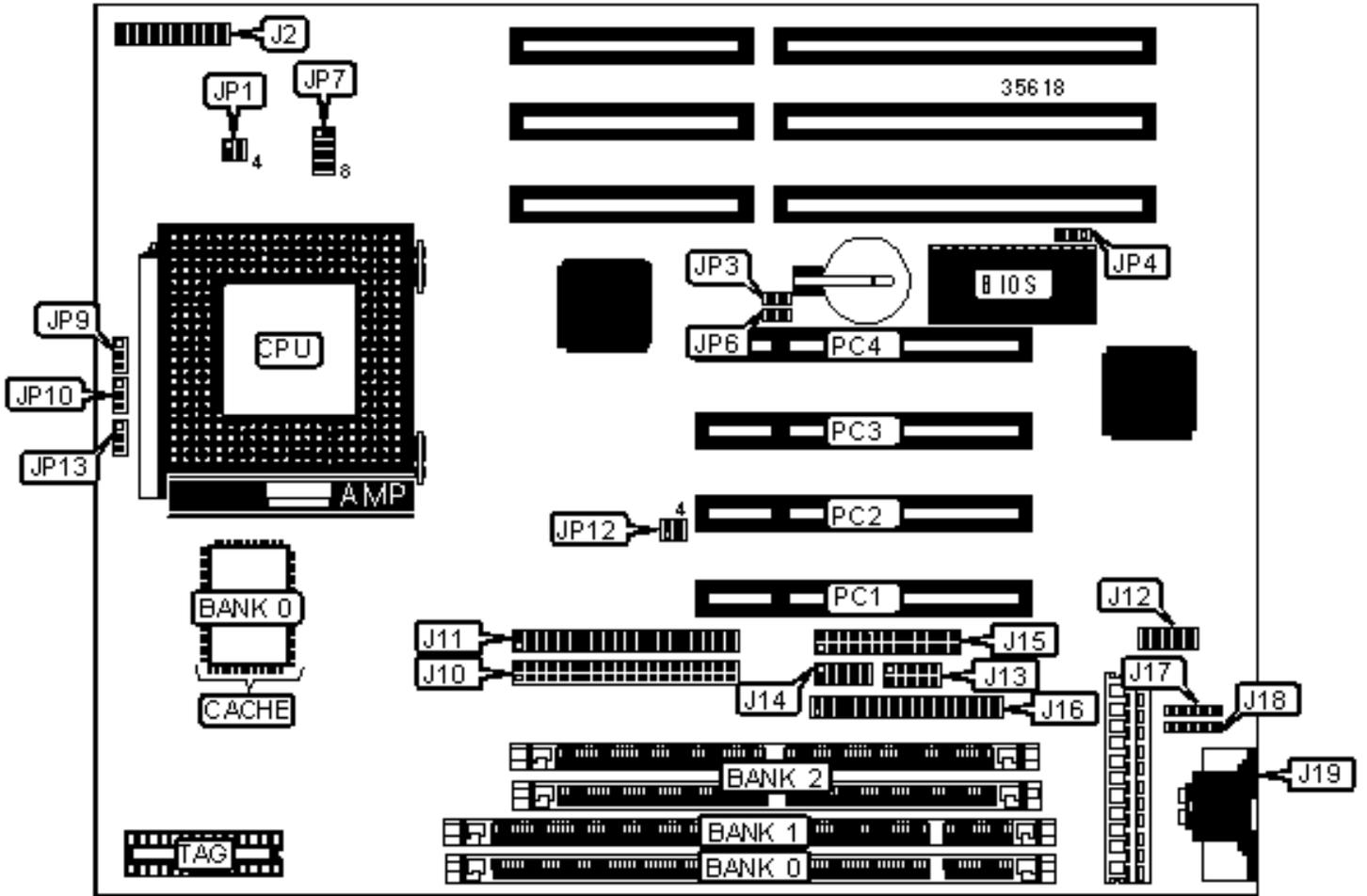


GEMLIGHT COMPUTER, LTD.

GMB-P57IPS (VER. 5.02)

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
IDE interface LED	J2/pins 1 & 2	Serial port 2	J13
Turbo LED	J2/pins 3 & 4	Serial port 1	J14
Suspend switch	J2/pins 5 & 6	Parallel port	J15
Reset switch	J2/pins 9 & 10	Floppy drive interface	J16
Power LED	J2/pins 11 - 13	PS/2 mouse interface	J17
Speaker	J2/pins 17 - 20	IR connector	J18
IDE interface 1	J10	PS/2 mouse port	J19
IDE interface 2	J11	32-bit PCI slots	PC1 - PC4
USB connector	J12		

USER CONFIGURABLE SETTINGS

Function	Label	Position
Flash BIOS voltage select 12v	JP4	Pins 2 & 3 closed
Flash BIOS voltage select 5v	JP4	Pins 1 & 2 closed

SIMM CONFIGURATION

Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36

48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36

DIMM CONFIGURATION		
Size	Bank 0	Bank 1
8MB	(1) 1M x 64	None
16MB	(1) 2M x 64	None
16MB	(1) 1M x 64	(1) 1M x 64
24MB	(1) 2M x 64	(1) 1M x 64
32MB	(1) 4M x 64	None
32MB	(1) 2M x 64	(1) 2M x 64
40MB	(1) 4M x 64	(1) 1M x 64
48MB	(1) 4M x 64	(1) 2M x 64
64MB	(1) 8M x 64	None
64MB	(1) 4M x 64	(1) 4M x 64
72MB	(1) 8M x 64	(1) 1M x 64
80MB	(1) 8M x 64	(1) 2M x 64
96MB	(1) 8M x 64	(1) 4M x 64
128MB	(1) 8M x 64	(1) 8M x 64

CMOS CONFIGURATION

Size	JP3	JP6
CMOS memory normal operation	Closed	Open
CMOS memory clear	Open	Closed

CACHE CONFIGURATION	
Size	Bank 0
512KB	(1) 64K x 64

CPU SPEED SELECTION (INTEL)						
CPU speed	Clock speed	Multiplier	JP9	JP10	JP12	JP13
90MHz	60MHz	1.5x	1 & 2	1 & 2	1 & 2	1 & 2
100MHz	66MHz	1.5x	1 & 2	1 & 2	Open	1 & 2
120MHz	60MHz	2x	2 & 3	1 & 2	1 & 2	1 & 2
133MHz	66MHz	2x	2 & 3	1 & 2	Open	1 & 2
150MHz	60MHz	2.5x	2 & 3	2 & 3	1 & 2	1 & 2
166MHz	66MHz	2.5x	2 & 3	2 & 3	Open	1 & 2
200MHz	66MHz	3x	1 & 2	2 & 3	Open	1 & 2
Note: Pins designated are in the closed position.						

CPU SPEED SELECTION (INTEL MMX)						
CPU speed	Clock speed	Multiplier	JP9	JP10	JP12	JP13
166MHz	66MHz	2.5x	2 & 3	2 & 3	Open	1 & 2
200MHz	66MHz	3x	1 & 2	2 & 3	Open	1 & 2
233MHz	66MHz	3.5x	1 & 2	1 & 2	Open	1 & 2
Note: Pins designated are in the closed position.						

CPU SPEED SELECTION (CX 6X86)

CPU speed	Clock speed	Multiplier	JP9	JP10	JP12	JP13
133MHz	55MHz	2x	2 & 3	1 & 2	1 & 2, 3 & 4	1 & 2
150MHz	60MHz	2x	2 & 3	1 & 2	1 & 2	1 & 2

Note: Pins designated are in the closed position.

CPU SPEED SELECTION (CX 6X86L)

CPU speed	Clock speed	Multiplier	JP9	JP10	JP12	JP13
166MHz	66MHz	2x	2 & 3	1 & 2	Open	1 & 2
200MHz	75MHz	2x	2 & 3	1 & 2	3 & 4	1 & 2

Note: Pins designated are in the closed position.

CPU SPEED SELECTION (CX 6X86MX)

CPU speed	Clock speed	Multiplier	JP9	JP10	JP12	JP13
166MHz	66MHz	2.5x	2 & 3	2 & 3	Open	1 & 2
200MHz	66MHz	3x	2 & 3	1 & 2	3 & 4	1 & 2
233MHz	75MHz	2.5x	2 & 3	2 & 3	3 & 4	1 & 2

Note: Pins designated are in the closed position.

CPU SPEED SELECTION (AM K5)

CPU speed	Clock speed	Multiplier	JP9	JP10	JP12	JP13
90MHz	60MHz	1.5x	1 & 2	1 & 2	1 & 2	1 & 2
100MHz	66MHz	1.5x	1 & 2	1 & 2	Open	1 & 2
120MHz	60MHz	2x	2 & 3	1 & 2	Open	1 & 2
133MHz	66MHz	2x	2 & 3	1 & 2	Open	1 & 2
150MHz	60MHz	2x	1 & 2	1 & 2	Open	1 & 2

166MHz	66MHz	2.5x	2 & 3	2 & 3	Open	1 & 2
Note: Pins designated are in the closed position.						

CPU SPEED SELECTION (AM K6)						
CPU speed	Clock speed	Multiplier	JP9	JP10	JP12	JP13
166MHz	66MHz	2.5x	2 & 3	2 & 3	Open	1 & 2
200MHz	66MHz	3x	1 & 2	2 & 3	Open	1 & 2
233MHz	66MHz	3.5x	1 & 2	1 & 2	Open	1 & 2
266MHz	66MHz	4x	2 & 3	1 & 2	Open	2 & 3
Note: Pins designated are in the closed position.						

CPU VOLTAGE SELECTION (SINGLE)		
Voltage	JP1	JP7
3.3v	1 & 2, 3 & 4	3 & 4, 5 & 6, 7 & 8
3.5v	1 & 2, 3 & 4	1 & 2, 3 & 4, 5 & 6, 7 & 8
Note: Pins closed are in the closed position.		

CPU VOLTAGE SELECTION (DUAL)			
V core	V I/O	JP1	JP7
2.2v	3.3v	Open	3 & 4
2.8v	3.3v	Open	7 & 8
2.9v	3.3v	Open	1 & 2, 7 & 8
3.2v	3.3v	Open	5 & 6, 7 & 8
Note: Pins closed are in the closed position.			