



## HP Consumer Support

# Motherboard Switch and Jumper Settings

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### CONFIGURATION SWITCH AND JUMPER SETTINGS

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### PROCESSOR SPEED SELECT JUMPER

To select the processor speed, there are five sets of jumpers (75 MHz, 90 MHz, 100 MHz, BF0, and BF1). When setting the processor speed, there is another jumper (PCISEL) that configures the proper PCI bus frequency.

To select the processor speed, install the jumpers as follows:

CPU Speed	Bus Freq	75 MHz	90 MHz	100 MHz	BF0	BF1	PCISEL
75	50 MHz	ON	OFF	OFF	1-2	OFF	OFF
90	60 MHz	OFF	ON	OFF	1-2	OFF	ON
100	66 MHz	OFF	OFF	ON	1-2	OFF	ON
120	60 MHz	OFF	ON	OFF	2-3	OFF	ON
133	66 MHz	OFF	OFF	ON	2-3	OFF	ON
150	60 MHz	OFF	ON	OFF	2-3	ON	ON
166*	66 MHz	OFF	OFF	ON	2-3	ON	ON

Table 1: CPU Speed Selection (\* = Default configuration)

### PROCESSOR BUS/CORE SPEED SELECT

The Bus/Core Frequency Ratio jumpers (BF0 and BF1) change the way the frequency divider works. A 100 MHz processor with the 1/2 option selected will have the bus speed running at only 50 MHz. This selection is not recommended since it will degrade the bus performance, but a 120 MHz processor will require the 1/2 option selected to set the bus speed at 60 MHz. A 166 MHz processor will require the 2/5 option selected to set the bus speed at 66 MHz.

Bus Speed	BF0	BF1
2/3 Bus/Core Frequency Ratio	1-2	OFF
1/2 Bus/Core Frequency Ratio	2-3	OFF
2/5 Bus/Core Frequency Ratio *	2-3	ON

Table 2: CPU Bus Speed settings (\* default setting)

### PROCESSOR VOLTAGE SELECT

The Pentium Processor family has various voltage choices depending on the CPU speed and the bin grade, they are referred to as VR and VRE. To select the proper voltage refer to the processor specification number in the documentation shipped with the processor, then follow the configuration below.

CPU Voltage	JP9 (VRE/VR)
3.38V	2-3
3.52V	1-2

Table 3: CPU Voltage Selection

### PCI FREQUENCY SELECT

The PCISEL Jumper provides a way to select the frequency of the PCI bus. When the jumper is

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**PCI Clock**

**JP2 (PCISEL)**

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OFF

33 MHz

ON

[Table 4: PCI Clock Jumper](#)

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**Type of CACHE**

**JP1 (Async-Burst/Pipe)**

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Async SRAM

1-2

Burst/Pipe SRAM

2-3

Table 5: Cache Selection