

division		explanation												
main chipset	North Bridge	Intel 865G												
	South Bridge	Intel 82801EB (ICH5)												
bios		AMI Flash bios (4Mbit)												
CPU support		- Intel Pentium4 mPGA 478 socket												
memory support		- Support 400/533/800MHz												
		- 184-pin Non-ECC DDR SDRAM DIMM socket * 2												
ON BOARD		- Supports 266/333/400MHz DDR DRAM (Dual Channel)												
		- Maximum extension: 2.0GB												
sound		Audio Controller + CMI9739A Codec in Intel 82801EB(ICH5)												
		<table border="1"> <thead> <tr> <th>speaker</th> <th>microphone jack</th> <th>line out jack</th> <th>line input jack</th> </tr> </thead> <tbody> <tr> <td>2 channels</td> <td>external microphone</td> <td>external speaker</td> <td>external playback device</td> </tr> <tr> <td>5 or 6 channels</td> <td>center or woofer</td> <td>Front left/right speaker</td> <td>surround speaker</td> </tr> </tbody> </table>	speaker	microphone jack	line out jack	line input jack	2 channels	external microphone	external speaker	external playback device	5 or 6 channels	center or woofer	Front left/right speaker	surround speaker
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2 channels	external microphone	external speaker	external playback device											
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expansion slot	LAN AGP	Intel 82562EZ												
		8X support slot * 1												
	PCI	32bit, 33MHz bus speed * 2												
	serial port	9-pin connector * 1 pc												
	parallel port	25-pin connector * 1												
support port	PS/2	6-pin mini-DIN connector * 1 each keyboard/mouse												
	IEEE 1394 USB	VIA6307 IEEE1394 (P1394a standard support) * 2ea (front) Back (2.0) * 2pcs, Front * 2pcs (optional)												
	memory stick IDE	Memory stick connector (SD card/MMC card supported)												
		40 pins * 2												
	S-ATA floppy connector	Serial ATA * 2 34 pin * 1 pc												
	LAN sound	RJ45 Jack * 1 - Rear: Speaker/Line In/Microphone * 1 each - Front: Headphone/SPDIF/Microphone * 1 each												
	monitor	Connector for connecting monitor * 1												
appearance														

* Based on board part number 143285

☐ **CPU specs**

division	explanation
type	mPGA478 (FC-PGA2)
internal speed	400/533/800 MHz
etc	1.725V Core Voltage not supported.

☐ **Intel Celeron mPGA478 Processor**

processor	bus clock	L2 cache
2.0	400 MHz	128KB
2.1	400 MHz	128KB
2.2	400 MHz	128KB
2.3	400 MHz	128KB
2.4	400 MHz	128KB
2.6	400 MHz	128KB

☐ **Intel Pentium4 mPGA478 Processor**

processor	bus clock	L2 cache
1.8AG	400 MHz	512 KB
2.0AG	400 MHz	512 KB
2.2G	400 MHz	512 KB
2.26G	533 MHz	512 KB
2.4G	533 MHz	512 KB
2.4CG	800 MHz	512 KB (HT support)
2.53G	533 MHz	512 KB
2.6G	533 MHz	512 KB
2.6CG	800 MHz	512 KB (HT support)
2.66G	533 MHz	512 KB
2.8G	533 MHz	
2.8CG	800 MHz	512 KB (HT support)
3.0G	800 MHz	512 KB (HT support)
3.06G	533 MHz	512 KB (HT support)
3.2G	800 MHz	512 KB (HT support)

► Terminology: HT function (Hyper-Threading) - Function that recognizes one physical CPU as two CPUs and operates more effectively during multitasking or 3D work.

□ **memory specifications**

division	explanation
standard	Non-ECC DDR SDRAM DIMMs
type	266/333/400 MHz
socket	184 pins * 2
max extension	2.0GB

□ **installable capacity**

DIMM size	Non-ECC Configuration
64MB	8Mbit x 64bit
128MB	16Mbit x 64bit
256MB	32Mbit x 64bit
512MB	64Mbit x 64bit
1.0 GB	128Mbit x 64bit

□ **Notes for upgrading (CPU system bus support)**

division	400 MHz CPU	533 MHz CPU	800 MHz CPU
266 MHz DDR	O	O	X
333 MHz DDR	X	O	O
400 MHz DDR	X	X	O

□ **Built-in memory for Dual Channel memory effect**

A type	B type	C type
		One 2
One 2	One 2	

▶ If the CPU system bus is 400/533MHz, even if the memory speed is 333/400MHz DDR, the speed operates at 266/333MHz.

▶ In case of mixed use of memories with different operating speeds, operation is based on the low memory operating speed.

FATIMA Jumper Settings

2003/10/08

jumper	function	1-2	2-3
JBIOS1	CMOS update availability	Impossible (possible when open)	-
JBAT1	CMOS initialization	impossible	possible
JSEL1	standby mode setting	S1 & S3	S1