

VESA CARD INSTALL OPERATION

The mother board consists of Three (3) VESA Local Bus slots, each of them can be served as Master/Slave. The following jumper selection is needed for installing the VESA Local Bus Add-On cards.

VL BUS OPTION

	CLOSE	OPEN
JP21	*For VESA clock > 33 MHz	*For VESA clock <= 33 MHz
JP22	For VESA 1 WS	*For VESA 0 WS

* Factory Default Setting

CACHE SRAM INSTALL SELECTION

CACHE SIZE	JP7	JP8	JP9	JP10
64KB	OFF	OFF	OPEN	2-3
128KB	OFF	ON	1-2	1-2
*256KB				
One Bank (Bank 0) 64KB x 4	ON	ON	1-2, 3-4	1-2
Two Bank (Bank 0 & 1) 32KB x 8	ON	ON	2-3	2-3

* maximum cache size for the mother board.

CPU (MICROPROCESSOR) INSTALL SELECTION

CPU	JP11	JP12	JP13	JP17	JP18	JP19
486SX	OPEN	2-3	2-3	OPEN	OPEN	OPEN
*486DX	OPEN	2-3	1-2, 3-4	1-2	OPEN	OPEN
486DX-SL	1-2	1-2	1-2, 3-4	1-2	5-6	1-2, 3-4
P24D	1-2, 4-5	1-2, 4-5	1-2, 3-4	1-2	3-4, 5-6	1-2, 3-4
P24T	1-2	1-2	1-2, 3-4	2-3	5-6	1-2, 3-4
M6	1-2, 3-4, 5-6	1-2, 3-4, 5-6	2-3	OPEN	2-3, 4-5	2-3, 4-5
M7	1-2, 3-4, 5-6	1-2, 3-4, 5-6	1-2, 3-4	1-2	2-3, 4-5	2-3
UMC 486	2-3	2-3	2-3	3-4	1-2	OPEN
AMD 486 DXL	2-3	2-3	1-2, 3-4	1-2, 3-4	1-2	OPEN

* Factory default setting.

CPU POWER SETTING

	JP14	JP15	JP16	JP23
3.3 V	1-2	1-2	1-2	CLOSE
3.45 V	1-2	1-2	1-2	OPEN
*5.0 V	2-3	2-3	2-3	OPEN

*Factory default setting.

WARNING FOR 3.3 V CPU

- 1) For using 3.3 V CPU, a 3.3 V Voltage Regulator LT1085 should be used on U24
- 2) Adjust Jumpers JP14 - JP16, & JP23 as shown in CPU Power Setting of INSTALLATION Section.

CPU SPEED SELECTION (CLOCK SPEED SELECTION)

CPU CLOCK	JP4	JP5	JP6
25 MHz	OFF	OFF	ON
*33 MHz	ON	ON	ON
40 MHz	OFF	ON	ON
50 MHz	ON	OFF	OFF

*Factory default setting.

V JUMPER/CONNECTOR SELECTION

Please kindly take note about this session for we will discuss all the jumpers/connectors which are on the F4DXL-UC4D mother board.

5.1 CONNECTOR SUMMARY

CONNECTOR	FUNCTION
KB1	KEYBOARD CONNECTOR
JP1	CONNECTOR FOR EXTERNAL BATTERY
JP26/RESET	HARDWARE RESET SWITCH CONNECTOR
JP27/TBSW	TURBO SWITCH CONNECTOR
D1/TBLED	TURBO LED CONNECTOR
JP28/SPEAKER	SPEAKER CONNECTOR
JP29/KEYLOCK	KEYBOARD LOCK CONNECTOR

5.2 CONNECTOR DEFINITION

JP1 CONNECTOR FOR EXTERNAL BATTERY

PIN	ASSIGNMENT
ON BOARD BATTERY : 2-3	NORMAL
ON BOARD BATTERY : 3-4	RESET CMOS
EXTEND BATTERY : 1	POSITIVE TERMINAL
EXTEND BATTERY : 4	NEGATIVE TERMINAL

JP26/RESET HARDWARE RESET SWITCH CONNECTOR

PIN	ASSIGNMENT
CLOSE	RESET
OPEN	NORMAL

JP27/TBSW TURBO SWITCH CONNECTOR

PIN	ASSIGNMENT
OFF (OPEN)	NON-TURBO MODE
ON (CLOSE)	TURBO MODE

D1/TBLED TURBO LED CONNECTOR

PIN	ASSIGNMENT
1	LED+ (ANODE)
2	LED- (CATHODE)

JP28/SPEAKERSPEAKER CONNECTOR

PIN	ASSIGNMENT
1	SPEAKER DATA
2	NOT USED
3	GROUND
4	+5V

JP29/KEYLOCK KEYBOARD LOCK CONNECTOR

PIN	ASSIGNMENT
1	+5V
2	NOT USED
3	GROUND
4	LOCK
5	GROUND

5.3 JUMPER SETTING**LOCAL BUS DEVICE CONTROL JP21, JP22**

JP21	CLOSE,	=	*VESA CLOCK > 33MHz
	OPEN	=	VESA CLOCK < 33MHz
JP22	OPEN	=	*OWS
	CLOSE,	=	IWS

FLASH ROM SELECTION JP2

JP2	2-3	=	FOR 12.0 V FLASH ROM
	1-2	=	*FOR 5.0 V FLASH ROM

* Factory default setting

KEYBOARD CONTROLLER SELECTION JP3

JP3	CLOSE	=	*Internal keyboard controller
	OPEN	=	External keyboard controller

For using Internal keyboard controller, remove R15, R19, R26, R58 and close JP3

For using External keyboard controller remove R15, R20, R25

* Factory default setting