Intel® Desktop Board
DQ67EP
Specification Update

Revision History

Revision	Revision History	Date
-001	This document is the first Specification Update for the Intel® Desktop Board DQ67EP	March 2011

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This document is an update to the specifications contained in the *Intel® Desktop Board DQ67EP Technical Product Specification* (Order Number: G15493). It is intended for hardware system manufacturers and software developers of applications, operating systems, or tools. It will contain Specification Changes, Errata, Specification Clarifications, and Documentation Changes.

Refer to the *2nd Generation Intel® Core™ Processor Family Specification Update* (Order Number: 457991) for specification updates concerning the *2nd Generation Intel® Core™ Processor Family* that may apply to the desktop board DQ67EP. Unless otherwise noted in this document, it should be assumed that any Processor errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Refer to the *Intel® 6 Series Chipset and Intel® C200 Series* Chipset *Specification Update* (Order Number: 440377) for specification updates concerning the Intel® Q67 Express Chipset that may apply to the desktop board DQ67EP. Unless otherwise noted in this document, it should be assumed that any Platform Controller Hub errata for a given stepping are applicable to the Altered Assembly (AA) revision(s) associated with that stepping.

Terminology

Specification Changes are modifications to the current published specifications. These changes will be incorporated in the next release of the specifications.

Errata are design defects or errors. Characterized errata may cause the desktop board behavior to deviate from published specifications. Hardware and software designed to be used with any given Altered Assembly (AA) and BIOS revision level must assume that all errata documented for that AA and BIOS revision level are present on all desktop boards.

Specification Clarifications describe a specification in greater detail or further highlight a specification's impact to a complex design situation. These clarifications will be incorporated in the next release of the specifications.

Documentation Changes include typos, errors, or omissions from the current published specifications. These changes will be incorporated in the next release of the specifications.

General Information

Basic Desktop Board DQ67EP Identification Information

AA Revision	BIOS Revision	Notes
G12529-302	SWQ6710H.86A.0038	1,2
G12529-304	SWQ6710H.86A.0041	1,2

Notes:

1. The AA number is found on a small label on the component side of the board.

2. The Intel® Q67 PCH Express Chipset used on this AA revision consists of the following component:

Device	Stepping	S-Spec Numbers
82Q67 Platform Controller Hub (PCH)	B2	SLH85
82Q67 Platform Controller Hub (PCH)	B3	SLJ4D

Summary of Changes

The following table indicates the Specification Changes, Errata, Specification Clarifications, or Documentation Changes that apply to the Intel® Desktop Board DQ67EP. Intel intends to fix some of the errata in a future revision of the desktop board, and to account for the other outstanding issues through documentation or specification changes as noted.

The following notations are used in the table:

Doc: Document change or update that will be implemented.

Plan Fix: This erratum may be fixed in a future revision of the desktop board, driver, or BIOS.

Fixed: This erratum has been previously fixed.

No Fix: There are no plans to fix this erratum.

Shaded: This erratum is either new or modified from the previous version of the document.

No.	Plans	Errata
		There are no characterized erratum for this product