

## Sheet1

DCS-286 motherboard has a number of jumpers and DIP switches which are setup as follows:

<b>Jumper</b>	<b>Setting</b>	<b>Location</b>
W1	1-2 short	1 wait state (set 2-3 short for 0 wait states)
W2	1-2 short	Unknown (silkscreen markings state 1-2 = A, 2-3 = B)
W6	1-2 short	BIOS = 27256 Kbits each (2-3 = 27128 Kbit chips)
W7	open	Color/Mono (short = mono)
W9	1-2 short	Parity check (1-2 = enabled, 2-3 = disabled)
SW1-1	off	Hi-speed
SW1-2	on	I/O bus half speed
SW1-3	off	RSEL0
SW1-4	off	RSEL1