JP5	JP6	JP7	JP8	JP9	JP10	JP11	JP12	JP13
								HITM A12
UP B14				CLKMUL R17	NMI B15	GND	SRESET C10	

JP5	JP6	JP7	JP8	JP9	JP10	JP11	JP12	JP13
								HITM A12
UP B14				CLKMUL R17	NMI B15	GND	SRESET C10	

JP5	JP6	JP7	JP8	JP9	JP10	JP11	JP12	JP13
								HITM A12
UP B14				CLKMUL R17	NMI B15	GND	SRESET C10	

JP5	JP6	JP7	JP8	JP9	JP10	JP11	JP12	JP13
								HITM A12
UP B14				CLKMUL R17	NMI B15	GND	SRESET C10	

JP14	JP15	JP16	JP17	JP18	JP19	JP20	JP21	JP22
WB/WT B13	INV A10			FERR_1 C14				
						IGNNE A15		
		SMIACT C12	SMI B10	FERR_2 A13				

ntel DX2/DX4-S

JP14	JP15	JP16	JP17	JP18	JP19	JP20	JP21	JP22
WB/WT B13	INV A10			FERR_1 C14				
						IGNNE A15		
		SMIACT C12	SMI B10	FERR_2 A13				

86DX2 66MHz WB

JP14	JP15	JP16	JP17	JP18	JP19	JP20	JP21	JP22
WB/WT B13	INV A10			FERR_1 C14				
						IGNNE A15		
		SMIACT C12	SMI B10	FERR_2 A13				

86DX2 100MHz WB

JP14	JP15	JP16	JP17	JP18	JP19	JP20	JP21	JP22
WB/WT B13	INV A10			FERR_1 C14				
						IGNNE A15		
		SMIACT C12	SMI B10	FERR_2 A13				

JP23

FERR_2
A13

FERR_2
A13

FERR_2
A13

FERR_2
A13