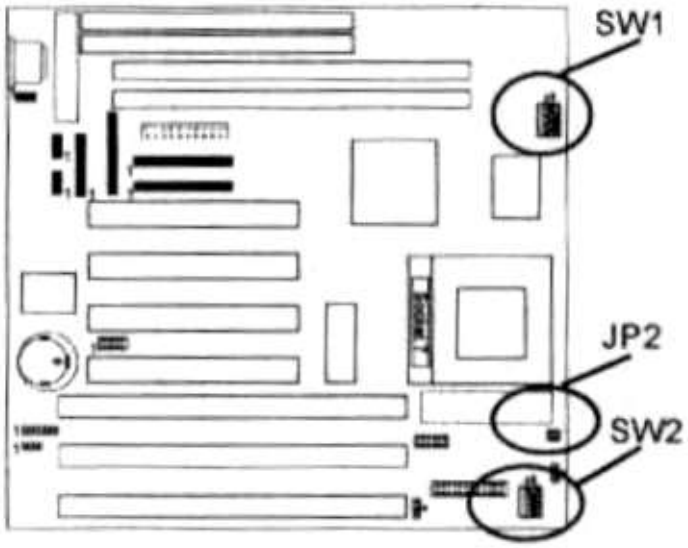
Chaintech 5SIM CPU Configuration

Assembled 2025-07-26 by Dusty – chcl.se



For all tables below, 1 refers to the ON position, and 0 refers to the OFF position.

CPU Bus Frequency & Frequency Ratio Configuration

CPU FSB Freq.	SW1-1	SW1-2	SW1-3	SW1-4
50 MHz	1	1	1	0
55 MHz	1	1	0	0
60 MHz	0	1	1	0
66 MHz	1	0	1	0
***75 MHz*	0	1	0	1
***83 MHz*	1	0	0	1

**PCI runs at half FSB, hence these options will overclock the bus. SW1-4, when set to 1, will run PCI in asynchronous mode to improve stability. You can try with this disabled, but beware of dragons.

CPU Freq. Ratio	SW1-5	SW1-6	SW1-7
1.5	0	0	0
2.0	1	0	0
2.5	1	1	0
3.0	0	1	0
3.5	0	0	0
4.0	1	0	1
4.5	1	1	1
**5.0*	0	1	1
**5.5*	0	0	1
**6.0*	1	0	0

*Not manufacturer approved, from a 2007 post to de.comp.hardware.cpu+mainboard.misc

CPU speed is the frequency ratio multiplied by the FSB frequency.

Voltage Configuration

For single voltage CPUs, JP2 should be jumpered according to the printing on the motherboard, and the core voltage switches set to 2.8V. This makes both I/O Vcc and Core Vcc 3.5V.

For dual voltage processors, remove jumpers from JP2, and configure the core voltage switches according to the below table. This makes I/O Vcc 3.3 V, and the Core Vcc what you configured.

Core Voltage	**SW2-1**	**SW2-2**	**SW2-3**	**SW2-4**	**SW2-5**
**2.0V*	1	1	1	1	0
**2.1V*	0	1	1	1	0
**2.2V*	1	0	1	1	0
**2.3V*	0	0	1	1	0
**2.4V*	1	1	0	1	0
**2.5V*	0	1	0	1	0
**2.6V*	1	0	0	1	0
**2.7V*	0	0	0	1	0
***2.8V*	1	1	1	0	0
2.8V	0	0	0	0	1
2.9V	0	1	1	0	0
**3.0V*	1	0	1	0	0
3.1V	0	0	1	0	0
3.2V	1	1	0	0	0
**3.3V*	0	1	0	0	0
**3.4V*	1	0	0	0	0
**3.5V*	0	0	0	0	0

*Not manufacturer approved, from a 2007 post to de.comp.hardware.cpu+mainboard.misc
**From the same post, but doesn't match the manual. Not recommended.