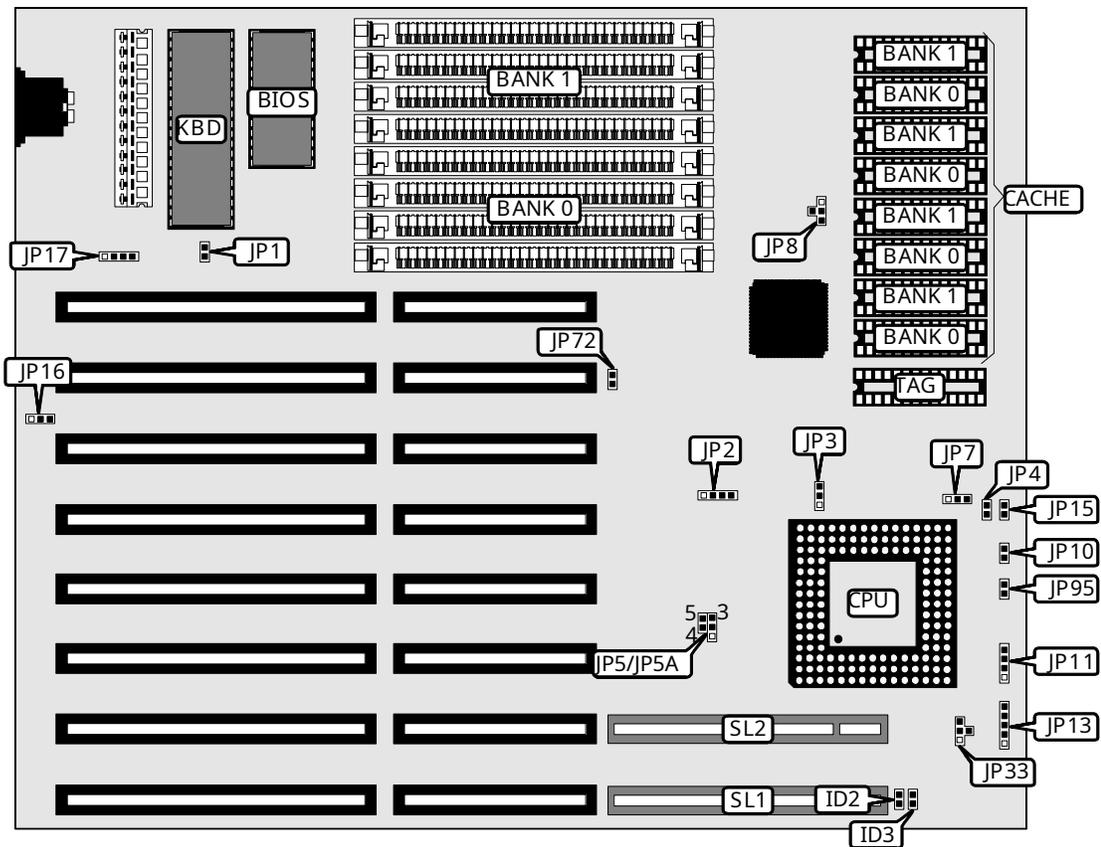


# MECER B860 (REV. A)

<b>Processor</b>	CX486M6/80486SX/80487SX/CX486M7/AM486DX/80486DX/ AM486DX2/80486DX2/P24C/P24T
<b>Processor Speed</b>	25/33/40/50(internal)/50/66(internal)MHz
<b>Chip Set</b>	Unidentified
<b>Video Chip Set</b>	None
<b>Maximum Onboard Memory</b>	128MB
<b>Maximum Video Memory</b>	None
<b>Cache</b>	64/128/256KB
<b>BIOS</b>	AMI/Award/MR
<b>Dimensions</b>	254mm x 218mm
<b>I/O Options</b>	32-bit VESA local bus slots (2), green PC connector
<b>NPU Options</b>	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo LED	JP10	Green PC LED	JP39
Speaker	JP11	Green PC connector	JP40
Power LED & keylock	JP13	Green PC connector	JP41
Reset switch	JP15	Turbo switch	JP95
External battery	JP17	32-bit VESA local bus slots	SL1 & SL2

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# VISIONEX GREEN-B [4GPV3] MOTHERBOARD

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USER CONFIGURABLE SETTINGS		
Function	Label	Position
PQFP CPU disabled	JP1	Closed
PQFP CPU enabled	JP1	Open
í CMOS memory normal operation	JP16	Pins 1 & 2 closed
CMOS memory clear	JP16	Pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	None
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	None
5MB	(4) 256K x 9	(4) 1M x 9
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	None
17MB	(4) 256K x 9	(4) 4M x 9
17MB	(4) 4M x 9	(4) 256K x 9
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9
64MB	(4) 16M x 9	None
65MB	(4) 256K x 9	(4) 16M x 9
65MB	(4) 16M x 9	(4) 256K x 9
68MB	(4) 1M x 9	(4) 16M x 9
68MB	(4) 16M x 9	(4) 1M x 9
80MB	(4) 4M x 9	(4) 16M x 9
80MB	(4) 16M x 9	(4) 4M x 9
128MB	(4) 16M x 9	(4) 16M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	None	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION		
Size	JP7	JP8
64KB	Pins 2 & 3 closed	Pins 2 & 4 closed
128KB	Pins 1 & 2 closed	Pins 2 & 3 closed
256KB	Pins 1 & 2 closed	Pins 1 & 2 closed

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# VISIONEX GREEN-B [4GPV3] MOTHERBOARD

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CPU SPEED SELECTION								
Speed	ID3	JP5	JP5A	JP33			JP38	JP72
25MHz	Open	2 & 3	4 & 5	2 & 4				Closed
33MHz	Open	2 & 3	4 & 5	2 & 4				Open
40MHz	Closed	1 & 2	3 & 5	2 & 3				Open
50iMHz	Open	2 & 3	4 & 5	2 & 4				Closed
50MHz	Closed	1 & 2	N/A	1 & 2				Open
66iMHz	Open	2 & 3	4 & 5	2 & 4				Open

Note: Pins designated should be in the closed position. If using a PGA CPU, use JP5 settings. If a PQFP CPU is installed, use JP5A settings.

CPU TYPE SELECTION			
Type	JP2	JP3	JP4
80486SX (PQFP)	Open	Open	Open
80486SX (PGA)	Pins 2 & 3 closed	Open	Open
80487SX	Pins 1 & 2, 3 & 4 closed	Pins 1 & 2 closed	Closed
80486DX	Pins 1 & 2, 3 & 4 closed	Pins 2 & 3 closed	Open
80486DX2	Pins 1 & 2, 3 & 4 closed	Pins 2 & 3 closed	Open
P24C	Pins 1 & 2, 3 & 4 closed	Pins 1 & 2 closed	Closed
P24T	Pins 1 & 2, 3 & 4 closed	Pins 1 & 2 closed	Closed

Note: The location of the PQFP is unidentified.

VL BUS WAIT STATE SELECTION	
Setting	ID2
0	Open
1	Closed