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CHAPTER 1 INTRODUCTION

1-1 SYSTEM FEATURES

- ☐ Supports INTEL P54C, P55C, AMD K5, and Cyrix M1 processors.
- ☐ Write-back/through, direct-mapped cache with size of 256KB/512KB/1MB.
- ☐ Supports synchronous SRAM module and asynchronous SRAM module.
- ☐ Provides 4-banks of 64-bit wide DRAMs up to 256MB with 256KB/512KB/ 1MB/2MB/4MB/8MB/16MB addressing page mode DRAMs.
- ☐ Supports EDO DRAM and mixing of EDO SIMMs with fast page mode DRAM SIMMs among different banks.
- ☐ Offers three PCI master slots, four 16-bit ISA slots.
- ☐ Dual IDE connectors support up to four devices in two channels.
- ☐ On-board high-speed I/O function (software configurable):
 - one F.D.C. port supports two devices
 - two 16550A fast UART compatible serial ports
 - one EPP/ECP mode parallel port.
 - provides selections of all primary and secondary ISA addressing of COM1-4,LPT1-3 and FDC disable or enable from "BIOS SETUP" configuration.
- ☐ "True" GREEN power management for CPU stop clock state.
- ☐ Board dimension: 220mm x 280mm (8.66" x 11.02").

REMARK

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1-2 CHECK LIST OF THE PACKING

The mainboard comes securely packed in a durable box and shipping carton.

Each mainboard containing:

<u>Q'TY</u>	<u>Description</u>
1	Mainboard : ATC-1545.
1	Cable : Enhanced IDE connector.
1	Cable : F.D.D. connector.
1	Cable : Serial port.
1	Cable : Serial/Parallel.
1	Manual : User`s manual.

If any of the above items are missing or damaged, please contact the dealer from whom you purchased.

NOTE : Leave the mainboard in its original packing until you are ready to install it.

CHAPTER 2 INSTALLATION

2-1 SRAM INSTALLATION

ATC-1545 can support cache memory with the types of 32K x 8, 64K x 8, and 128K x 8 SRAM. Regarding the related locations of DATA SRAM, TAG SRAM, and jumpers.

2-1-1 CACHE CONFIGURATION SIZE

Cache Size	TAG SRAM	DATA SRAM	JP28	JP29
256K	16Kx8 8K x 8	32K x 8	open	open
512K	32K x 8	64K x 8	open	close
1MB	32K x 8	128K x 8	close	close

2-1-2 TYPE SELECTION OF CACHE SRAM

Cache Type	JP21
3.3V / BURST	1-2
Mixed Mode	2-3

2-2 PIPELINE BURST SRAM INSTALLATION

Plug the Pipeline BURST SRAM module into the COAST slot located besides CPU socket, then set JP21 to "1-2" and set JP9 as following:

Cache Type	JP9
BURST SRAM	2-3
W / O BURST SRAM	open

The "BIOS SETUP" should be revised as follows:

SRAM Type : Sync.

SYNC SRAM Support : Pipeline

Cache Read Burst : 3 - 1 - 1 - 1

Cache Write Burst : 3 - 1 - 1 - 1

2-3 CPU INSTALLATION

ATC-1545 can support INTEL Pentium 75/90/100/120/133MHz microprocessors. For installation, please notice CPU pin 1 must align with the ZIF socket 7 Pin 1 location.

2-3-1 CPU TYPE SELECTION

CPU TYPE	JP16	JP24	JP25	JP26	JP27
INTEL P54C	close	close	close	close	close

Int. CPU Clock	JP13	JP15	JP20
75MHz	open	open	1-2
90MHz	1-2	open	1-2
100MHz	1-2	1-2	1-2
120MHz	1-2	open	2-3
133MHz	1-2	1-2	2-3

If you use higher internal CPU clock, please refer to 2-3-2.

2-3-2 CPU CLOCK SETTING

The following setting is for CPU speed higher than 133MHz.

External CPU Clock	JP13	JP15
50(MHz)	open	open
60(MHz)	1-2	open
66(MHz)	1-2	1-2

INTERNAL CPU CLOCK	JP20	JP23
INTEL External Clock x 1.5	1-2	open
INTEL External Clock x 2.0	2-3	open
INTEL External Clock x 2.5	2-3	1-2
INTEL External Clock x 3.0	2-3	2-3
CYRIX M1 EXTERNAL CLOCK X 3.0	1-2	open
CYRIX M1 EXTERNAL CLOCK X 2.0	2-3	open

2-4 OTHER JUMPERS SETTING

	JP18
EPROM	1-2
FLASH PROM	2-3

	J11
External Battery	2-3
Clear CMOS	3-4

J19
HDD LED