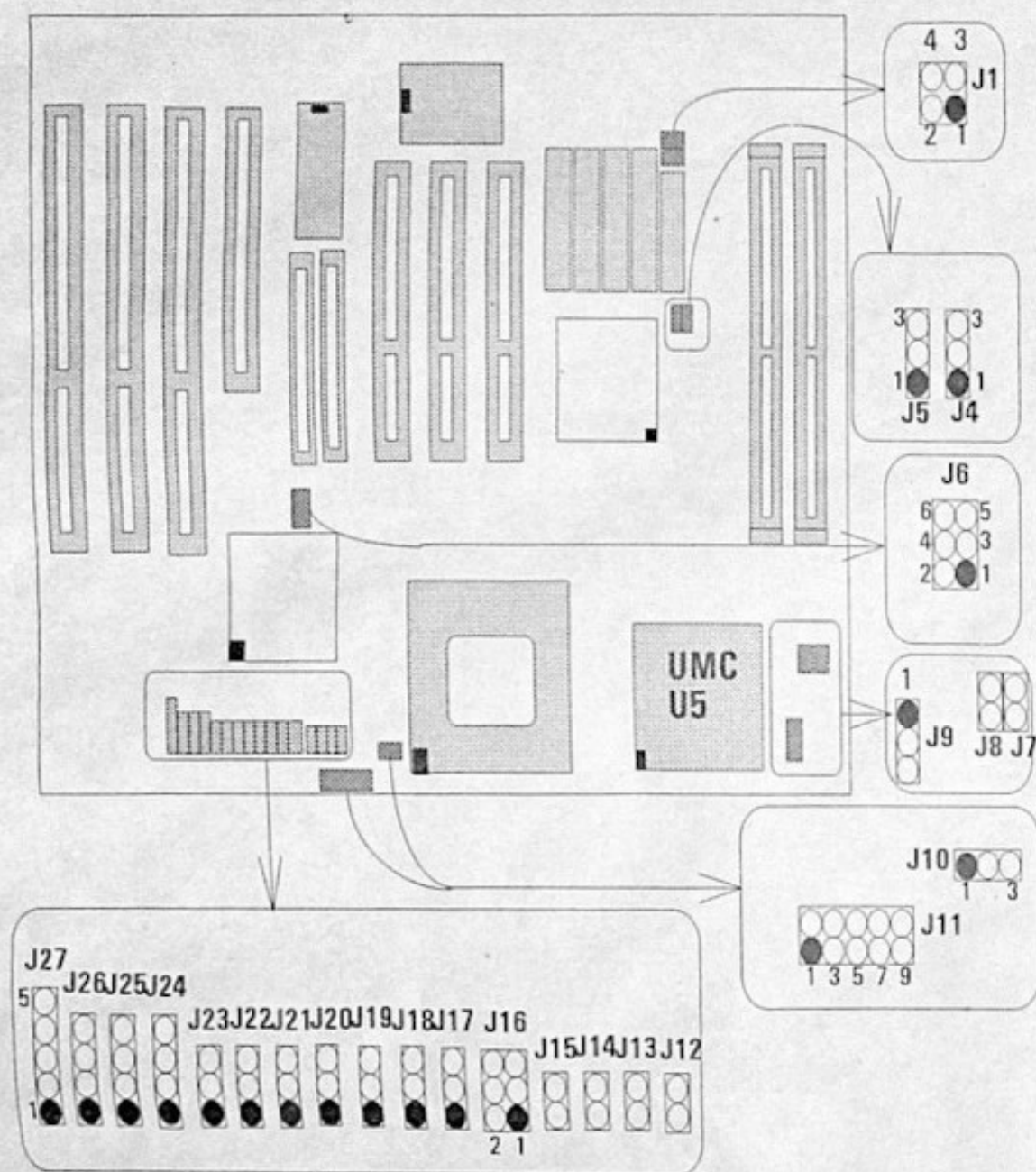


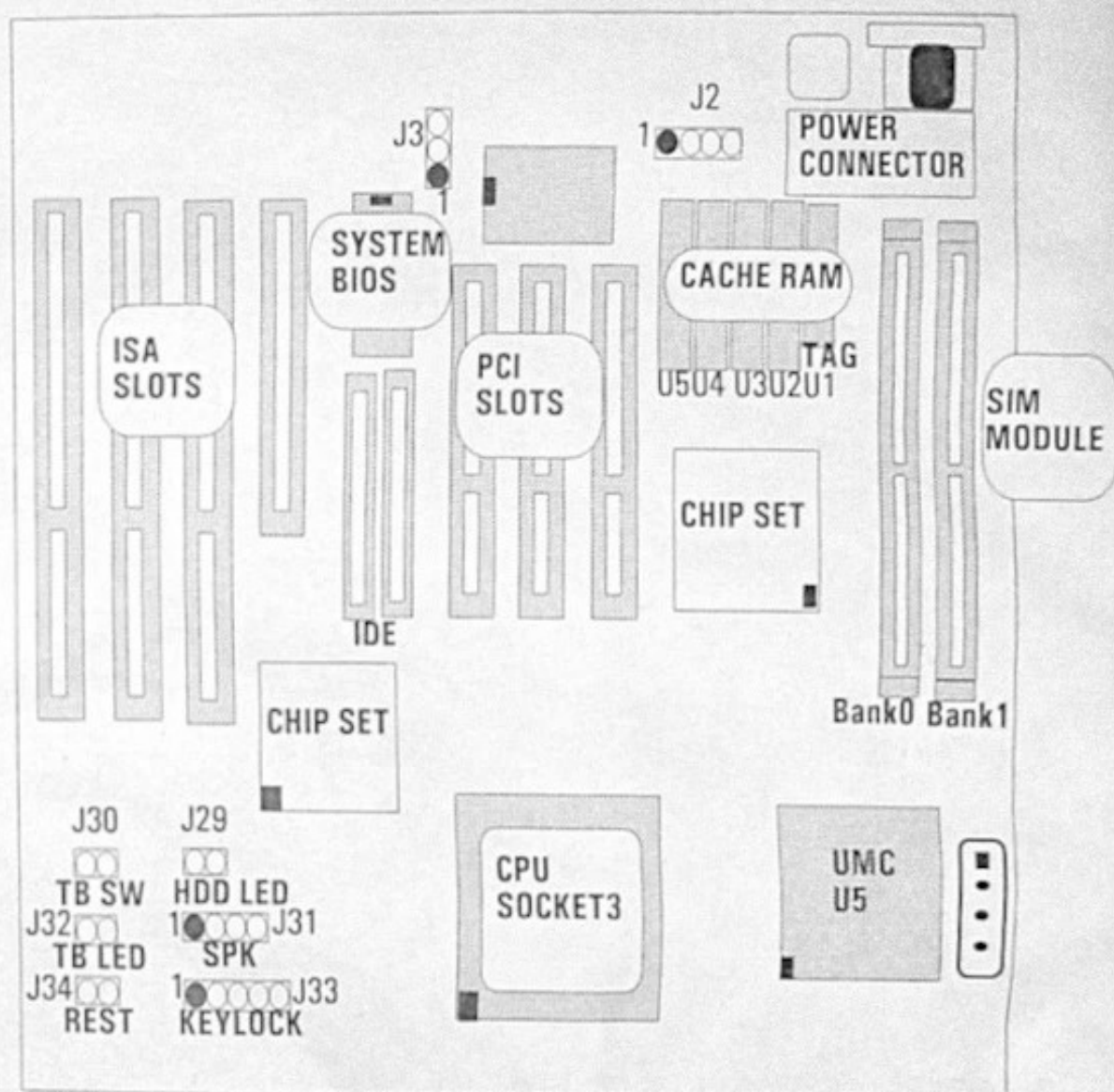
JUMPER LOCATION OF UMC 486 PCI/ISA M/B



COLOR JUMPER CAPS CLASSIFICATION:

- | | |
|-----------|-------------------------------|
| 1. White | : CPU Type Selection |
| 2. Yellow | : System Clock Selection |
| 3. Red | : Regulator Voltage Selection |
| 4. Black | : Factory Setting |

OTHER JUMPERS & CONNECTORS LOCATION DIAGRAM



FUNCTION CONNECTORS & OTHER JUMPERS SETTING

Connector	Description
J2	External Battery Connector 2-3: Use Internal Battery (Default) 3-4: Clear C-MOS BIOS Setup Data
J3	1-2: FLASH ROM 2-3: EPROM (Default)

B. INTEL CPU setting

CPU type	J4	J5	J12	J14	J15	J16	J17	J18
486DX/DX2	1-2	1-2	open	open	open	1-2	open	open
SLDX/DX2/DX4	1-2	1-2	open	open	open	3-4	open	open
P24T	1-2	1-2	open	open	open	3-4,5-6	2-3	1-2
P24D	1-2	1-2	1-2	1-2	1-2	3-4	open	open

CPU type	J20	J22	J23	J24	J25	J26	J27
486DX/DX2	open	open	open	open	1-2,3-4	3-4	open
SLDX/DX2/DX4	1-2	open	1-2	open	1-2,3-4	3-4	2-3,4-5
P24T	1-2	open	1-2	1-2	1-2,3-4	2-3	2-3,4-5
P24D	1-2	2-3	1-2	open	1-2,3-4	3-4	2-3,4-5

C. AMD CPU setting

CPU type	J4	J5	J12	J14	J15	J16	J17	J18
Am5x86-P75	1-2	1-2	1-2	1-2	1-2	3-4	open	open
Enhanced Am486	1-2	1-2	1-2	1-2	1-2	3-4	open	open
486DX/DX2/DX4	1-2	2-3	open	open	open	1-2	open	open
Am486DXLT	1-2	2-3	open	open	open	1-2	open	open

CPU type	J20	J22	J23	J24	J25	J26	J27	J21
Am5x86-P75	1-2	2-3	1-2	open	1-2,3-4	3-4	2-3,4-5	1-2
Enhanced Am486	1-2	2-3	1-2	open	1-2,3-4	3-4	2-3,4-5	open
486DX/DX2/DX4	open	open	open	open	1-2,3-4	3-4	open	open
Am486DXLT	open	1-2	2-3	3-4	1-2,3-4	1-2,3-4	open	open

CHEAPTER 2 INSTALLATION

2-1 SIM MODULES INSTALLATION

The UMC 486 PCI/ISA mainboard can be expanded from 1MB to 128MB by using 256K, 1MB, 2MB, 4MB, 8MB, 16MB, 32MB, and 64MB of 72-pin SIM module.

2-2 SRAM INSTALLATION

This mainboard can support cache memory from 128K to 512K bytes with the types of 32Kx 8, 64Kx 8, or 128Kx 8 SRAM.

Cache Configuration Size

Cache Size	TAG SRAM (U1)	DATA SRAM (U2-U5)	J1
128K	8Kx 8	32Kx 8	open
256K	32Kx 8	64Kx 8	3-4
512K	32Kx 8	128Kx 8	1-2,3-4

2-3 CPU INSTALLATION

The following table listed Jumpers related locations are shown in page A,B.

2-3-1 CPU type setting

A. On-board UMC U5 LQFP CPU setting

CPU type	J7	J8	J9
UMC U5	2-3	1-2	1-2

D. Other CPUs setting

CPU type	J4	J5	J12	J14	J15	J16	J17	J18
Cyrix or IBM	2-3	1-2	open	open	open	3-4	1-2	2-3
Cyrix M1SC	1-2	1-2	1-2	1-2	1-2	3-4	open	open
UMC486	1-2	2-3	open	open	open	3-4	open	open

CPU type	J20	J22	J23	J24	J25	J26	J27
Cyrix or IBM	2-3	open	1-2	2-3	1-2,3-4	3-4	1-2,3-4
Cyrix M1SC	1-2	2-3	1-2	open	1-2,3-4	3-4	2-3,4-5
UMC486	1-2	open	1-2	open	1-2,3-4	3-4	2-3,4-5

2-3-2 CPU speed setting (J6)

CPU SPEED	1-2	3-4	5-6
25MHz	CLOSE	OPEN	OPEN
33MHz	CLOSE	CLOSE	CLOSE
40MHz	CLOSE	CLOSE	OPEN
50MHz	OPEN	OPEN	CLOSE

2-3-3 CPU voltage setting

CPU Voltage	J11	J10
3.45V	1-3,2-4	2-3
3.6V	7-9,8-10	2-3
4V	3-5,4-6	2-3
5V	open	1-2