

2-2-1 CPU TYPE SELECTION

A. INTEL PENTIUM CPU

JP6	1-2
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CPU Core Voltage	VRE	STD
JP5	1-2**	3-4*

* STD : 3.315V ~ 3.600V

** VRE : 3.400V ~ 3.600V

(The fourth line of the mark on the under-side of the processor contains a code that identifies the voltage level type. V is VRE, S is standard.)

Intel Pentium CPU,
the first letter after '7'
denotes voltage type.

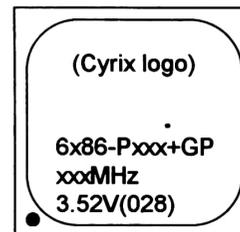


INTERNAL CPU CLOCK	JP3	JP4	JP8	JP9	Ext.x Frq.
75MHz	open	open	1-2	1-2	50x1.5
90MHz	close	open	1-2	1-2	60x1.5
100MHz	open	close	1-2	1-2	66x1.5
120MHz	close	open	1-2	2-3	60x2.0
133MHz	open	close	1-2	2-3	66x2.0
150MHz	close	open	2-3	2-3	60x2.5
166MHz	open	close	2-3	2-3	66x2.5
180MHz	close	open	2-3	1-2	60x3.0
200MHz	open	close	2-3	1-2	66x3.0

B. Cyrix 6x86 CPU

CPU	JP5	JP6
Cyrix6x86 (028)	1-2	1-2

INTERNAL CPU CLOCK	JP3	JP4	JP8	JP9	Ext.x Frq.
P120+ @ 100MHz	open	open	1-2	2-3	50x2.0
P150+ @ 120MHz	close	open	1-2	2-3	60x2.0
P166+ @ 133MHz	open	close	1-2	2-3	66x2.0

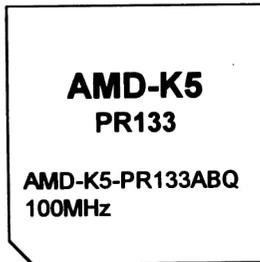


The bottom line of the mark on the processor contains a code 028 that identifies the voltage level type. If the code is 016 or others, or no marking then please contact your dealer.

C. AMD-K5 CPU

CPU	JP5	JP6
AMD K5	1-2	1-2

INTERNAL CPU CLOCK	JP3	JP4	JP8	JP9	Ext.x Frq.
PR75ABR 75MHz	open	open	1-2	1-2	50x1.5
PR90ABQ 90MHz	close	open	1-2	1-2	60x1.5
PR100ABQ 100MHz	open	close	1-2	1-2	66x1.5
PR120ABQ 90MHz	close	open	1-2	1-2	60x1.5
PR133ABQ 100MHz	open	close	1-2	1-2	66x1.5
PR150ABQ 120MHz	close	open	1-2	2-3	60x2.0

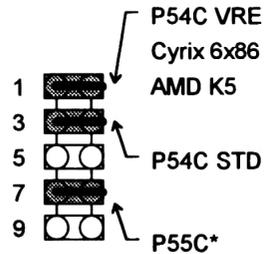


2-2-2 CPU VOLTAGE SETTING

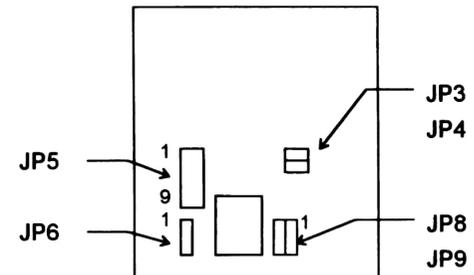
JP6 is for setting single or dual CPU voltage mode. JP5 is used for various CPU voltage value types, please refer to CPU's marking on/under CPU and its user's manual.

CPU MODE	Single	Dual
JP6		
	1-2	2-3

CPU CORE VOLTAGE	JP5
INTEL	STD
(P54C)	VRE
P55C*	2.8V*
Cyrix	6x86(028)
AMD	K5



* Please confirm this with your supplier before you install P55C type CPU.

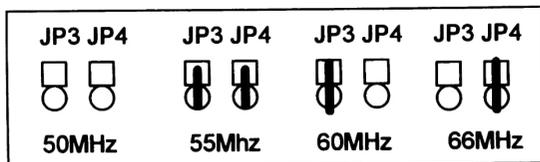


2-2-3 CPU CLOCK SETTING

The following setting is for new release CPUs.

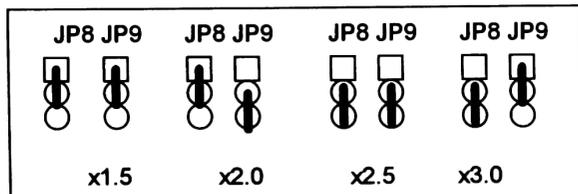
EXTERNAL CPU CLOCK	JP3	JP4
50(MHz)	open	open
55(MHz)	close	close
60(MHz)	close	open
66(MHz)	open	close

External
CPU Clock :



INTERNAL CPU CLOCK	JP8	JP9
INTEL EXTERNAL CLOCK X 1.5	1-2	1-2
INTEL EXTERNAL CLOCK X 2.0	1-2	2-3
INTEL EXTERNAL CLOCK X 2.5	2-3	2-3
INTEL EXTERNAL CLOCK X 3.0	2-3	1-2

Internal
CPU Clock :



2-3 SYSTEM MEMORY INSTALLATION

ATC-1000+ provides four 72-pin SIM sockets for system memory expansion from 4MB to 128MB. These four SIMs are arranged to two banks, Bank0 (SIM 1, 2) and Bank1 (SIM 3, 4), please refer to page A. Each bank provides 64-bit wide data path.

This mainboard accepts Fast Page Mode DRAM, and EDO Mode (Extended Data Out) DRAM, with a speed at least 70 nanosecond. You should plug DRAM modules into two sockets (same bank) or four sockets at one time. Each pair of modules must be the same size, type and speed; no matter single-side or double-side module. Please plug in Bank 0 firstly if you only have 2 modules. The mainboard supports mixing of EDO SIMMs with fast page mode DRAM SIMMs among different banks, please plug EDO in Bank 0, if you have two types of DRAM.

※ System Memory Combinations Options ※

BANK0 SIM 1, 2	BANK1 SIM 3, 4	Total Memory SIM 1- 4
2MBx2	-	4MB
-	2MBx2	4MB
4MBx2	-	8MB
-	4MBx2	8MB
8MBx2	-	16MB
-	8MBx2	16MB
4MBx2	4MBx2	16MB

- continue -

BANK0 SIM 1, 2	BANK1 SIM 3, 4	Total Memory SIM 1- 4
4MBx2	8MBx2	24MB
8MBx2	4MBx2	24MB
16MBx2	-	32MB
-	16MBx2	32MB
8MBx2	8MBx2	32MB
4MBx2	16MBx2	40MB
16MBx2	4MBx2	40MB
8MBx2	16MBx2	48MB
16MBx2	8MBx2	48MB
32MBx2	-	64MB
-	32MBx2	64MB
16MBx2	16MBx2	64MB
4MBx2	32MBx2	72MB
32MBx2	4MBx2	72MB
8MBx2	32MBx2	80MB
32MBx2	8MBx2	80MB
16MBx2	32MBx2	96MB
32MBx2	16MBx2	96MB
64MBx2	-	128MB
-	64MBx2	128MB
32MBx2	32MBx2	128MB

CHAPTER 3 AWARD BIOS SETUP

This chapter explains the system BIOS setup, and how to update new BIOS. All BIOS screens showed in the following pages are default values, your system dealer will set up these values according to your demand of computer.

ATC-1000+ uses Intel Flash ROM to make BIOS easier to be updated by the floppy disk-based program and to committe Microsoft Windows 95 plug & play feature.

※ JP1, JP2 Setting are for Update System BIOS by using Intel Flash ROM.

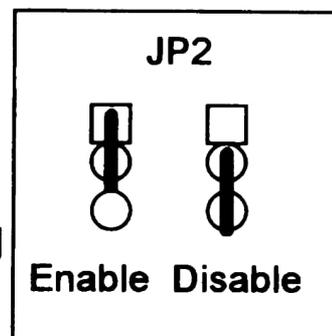
JP2 : Intel Flash ROM update

1-2	Enable
2-3	Disable *

* is default setting

When you want to update Flash ROM you should set JP2 to '1-2'.

When you finished BIOS updated you should set '2-3' again.



If you would like to update CMOS date also, please set JP1.

JP1 : CMOS update

open	NORMAL *
close	CLEAR CMOS

* is default setting

