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Description

**Mainboard Specification**

- Processor: Socket 3 ZIF socket/CPU clock support 25/33/40/50 MHz. On board regulator that support 3.15/3.3/3.45/3.6/4.0 Volt
- Intel P24T/P24D/i486DX4/i486DX2/i486DX/i486SX2/i486SX:
- Cyrix Cx486DX2/Cx486DX/Cx486S/Cx486DX2-V/Cx486DX4/Cx5x86.
- AMD Am486DX4/Am486DX2/Am486DX/Enhanced Am486.
- UMC U5-S.
- TI Tx486DX2.

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**General Jumpers Setting**

JP17 JP16

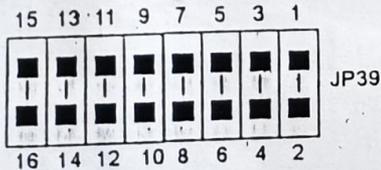


JP17	JP16	CPU Type Select
1-2	1-2	Intel/AMD CPU Cyrix Cx5x86
1-2	2-3	Cyrix Cx486 CPU/TI Tx486DX2
2-3	1-2	UMC CPU
2-3	2-3	Reserved

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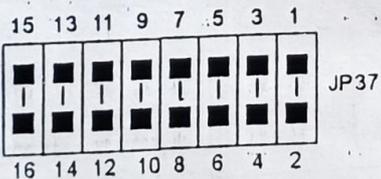
CPU Type: Jumper Setting (O: Open / ■: Short)

Intel 80486DX2-P24D  
AMD Enhanced Am486  
Cyrix Cx5x86 (M1-SC)



\*JP37, JP38 Left Open all.

Cyrix Cx486S -M6  
Cx486DX -M7  
Cx486DX2 -M7  
Cx486DX2 -V  
Cx486DX4 -GP  
TI Tx486DX2



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Description

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CPU Clock	CPU Type
50 MHz	Intel 80486DX-50 Intel 80486DX4-100 (Clock Multiplier = 2) Cyrix Cx486DX-50
40 MHz	AMD Am486DX-40/Am486DX2-80/Am486DX4-120 AMD Enhanced Am486DX2-80/Am486DX4-120 Cyrix Cx486S-40/Cx486DX-40/Cx5x86-80 UMC U5S-40 TI Tx486DX2-80
33 MHz	Intel 80486SX-33/80486DX-33/80486DX2-66 Intel 80486DX4-100 (Clock Multiplier = 3) AMD Am486DX-33/Am486DX2-66/Am486DX4-100 AMD Enhanced Am486DX2-66/Am486DX4-100 Cyrix Cx486S-33/Cx486DX-33/Cx486DX2-66/Cx486DX-100/Cx5x86-100 UMC U5S-33 TI Tx486DX2-66
25 MHz	Intel 80486SX-25/80486DX-25/80486DX2-50 Intel 80486DX4-75 (Clock Multiplier = 3) AMD Am486DX-25/Am486DX2-50 Cyrix Cx486S-25/Cx486DX-25/Cx486DX2-50/Cx5x86-75 UMC U5S-25

**NOTE:**  
For new CPU that are not listed above, please see the data sheet of the CPU or confirm from your vendor.

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CPU Voltage Setting vs. 3 Volt CPU Type:

CPU Voltage	CPU Type
3.15 Volt	Reserved
3.3 Volt	Cyrix Cx486DX2-V50
3.45 Volt	Intel 80486DX4 AMD Am486 AMD Enhanced Am486 Cyrix Cx 5x86/Cx486DX4 TI Tx486DX2
3.6 Volt	Cyrix Cx486DX2-V66
4.0 Volt	Cyrix Cx486DX2-V80

**CAUTION:**  
1. Only one position can be shorted at one time.  
2. Check your CPU vendor with the detail voltage rating specification of CPU.

# AMENDMENT

Page \_\_\_\_\_ Description \_\_\_\_\_

3-4 NOTE:  
To support LBA or LARGE mode of HDDs, there must be some software involved. All these software are located in the Award HDD Service Routine (INT 13h). It may be failed to access a HDD with LBA (LARGE) mode selected if you are running under a Operating System which replaces the whole INT 13h.

3-13 Item 12 deleted.

22 Burst Copy-Back Option	Disabled Enabled	This option support L2 cache burst copy-back function.
23 IBC DEVSEL # Decoding	Medium Slow Fast	This option support chipset to provide three optional DEVSEL# subtractive decoding sampling point for PCI specification.

## 3-20 Power-On Boot

After you have made all the changes to CMOS values and the system can not boot with the CMOS values selected in Setup, restart the system by turning it OFF then ON or pressing the "RESET" button on the system case. You may also restart the system by simultaneously pressing Ctrl, Alt, and Delete keys.

The System BIOS support a HDD auto-detection function that is not necessary to enter the Setup in setting the HDD. After booting, the BIOS will auto-detect the HDD and display a message of "CMOS checksum error-Defaults loaded". At this time press F1 key to continue, the BIOS will load SETUP DEFAULT value for system running. It also allows you to press the DEL key to load BIOS DEFAULT running under optional condition.

E-1 Addendum page.

# Appendix E : PCB Layout Placement

