II. Jumper configuration

1. CPU Type Setting

Hardware Setting: OPEN   Close   Close 1-2   Close 2-3
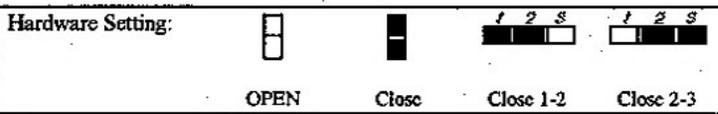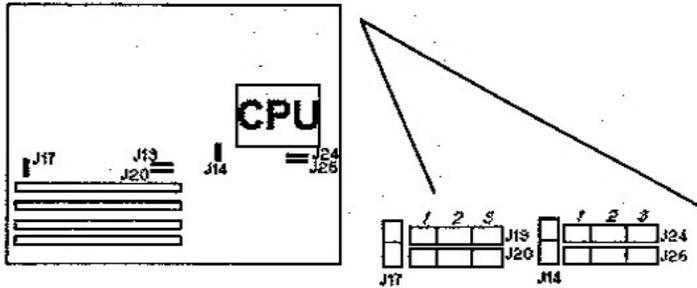
The following illustrations list the jumper setting for the major Pentium or compatible CPUs.

For Intel Pentium™ 75MHz to 200MHz CPU

Intel Pentium	Jumpers (J14, J17, J19, J20, J24, J26)
200MHz = 3 x 66MHz	(jumper diagram: J17, J19, J20, J14, J24, J26)
166MHz = 2.5 x 66MHz	(jumper diagram: J17, J19, J20, J14, J24, J26)
150MHz = 2.5 x 60MHz	(jumper diagram: J17, J19, J20, J14, J24, J26)
133MHz = 2 x 66MHz	(jumper diagram: J17, J19, J20, J14, J24, J26)
120MHz = 2 x 60MHz	(jumper diagram: J17, J19, J20, J14, J24, J26)
100MHz = 1.5 x 66MHz	(jumper diagram: J17, J19, J20, J14, J24, J26)

Intel Pentium	Jumpers (J14, J17, J19, J20, J24, J26)
90MHz = 1.5 x 60MHz	(jumper diagram: J17, J19, J20, J14, J24, J26)
75MHz = 1.5 x 50MHz	(jumper diagram: J17, J19, J20, J14, J24, J26)

For Cyrix 6x86 120MHz to 200MHz CPU

Cyrix 6x86	Jumpers (J14, J17, J19, J20, J24, J26)
P166+ = 2 x 66MHz	(jumper diagram: J17, J19, J20, J14, J24, J26)
P150+ = 2 x 60MHz	(jumper diagram: J17, J19, J20, J14, J24, J26)
P133+ = 2 x 55MHz	(jumper diagram: J17, J19, J20, J14, J24, J26)
P120+ = 2 x 50MHz	(jumper diagram: J17, J19, J20, J14, J24, J26)

For AMD-K5™ 75MHz to 166MHz CPU

AMD-K5™	Jumpers (J14, J17, J19, J20, J24, J26)
PR166 = 2 x 66MHz	(jumper diagram: J17, J19, J20, J14, J24, J26)
PR150 = 2 x 60MHz	(jumper diagram: J17, J19, J20, J14, J24, J26)
PR133 = 1.5 x 66MHz	(jumper diagram: J17, J19, J20, J14, J24, J26)
PR120 = 1.5 x 60MHz	(jumper diagram: J17, J19, J20, J14, J24, J26)

AMD-K5™	Jumpers (J14, J17, J19, J20, J24, J26)	
PR100 = 1.5 x 66MHz		
PR90 = 1.5 x 60MHz		
PR75 = 1.5 x 50MHz		

These powerful CPUs are designed with clock multiplier concept whereby the CPU core is running at a multiple of the system bus speed. This design technique reduces the requirement on the other system components while making higher performance possible. For example, the Intel 200 Mhz Pentium CPU core is running at 200 Mhz while the system bus is running at 66 Mhz. In other words the CPU core is operating with a 3X internal multiplier

2. Cache Memory Selection
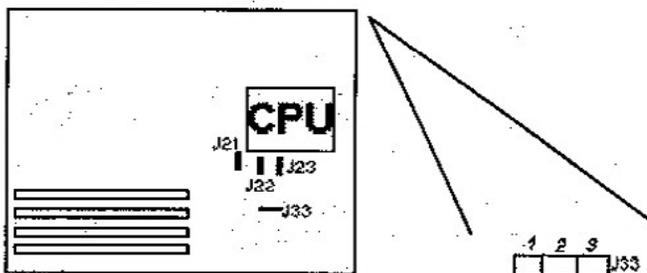
Cache Type	J22	
Pipeline Burst (PB)	Close	
Burst	Open	

Cache Size	J21	J23
256KB	Close	Open
512KB	Open	Close

Cache Size	Cache Data RAM (U2, U5)	Cache Tag RAM (U14)	J33
256KB	32K x 32 SRAM	8K x 8 / 32K x 8 SRAM	2-3
512KB	64K x 32 SRAM	16K x 8 / 32K x 8 SRAM	1-2

The above selection is shown for your reference only as your PCI6503 should have the cache size fixed at the factory.
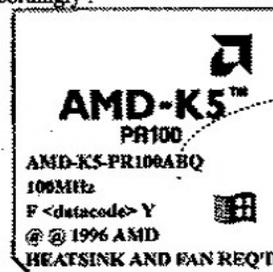
3. Voltage Selection
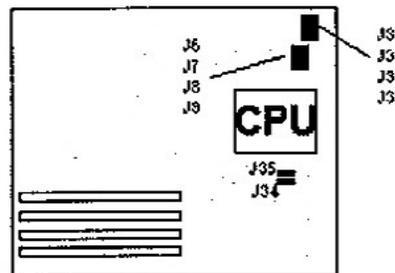
AMD CPU

3.45V	3.3V	2.9V	2.7V	2.5V

Check up the voltage requirement of your AMD CPU as below diagram and select the voltage accordingly :


Operating Voltage:
B = 3.45V
C = 3.3V
F = 3.3V
H = 2.9V
J = 2.7V
K = 2.5V

Intel Pentium, Cyrix 6x86 & Cyrix 6x86L CPUs

| 3.3V (Pentium, Cyrix 6x86)	2.5V (Cyrix 6x86L)	2.8V (P55C)
J38, J39, J36, J37	J38, J39, J36, J37	J38, J39, J36, J37
J6, J7, J8, J9	J6, J7, J8, J9	J6, J7, J8, J9
J35, J34	J35, J34	J35, J34

Caution: Depending on the specific CPU & the clock speed, the temperature of the Heatsink of the Voltage Regulators (2.5v-3.0v, 3.3v-3.4v) might reach 100°c or higher during operation.
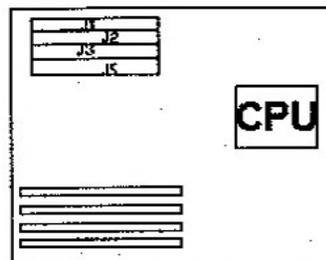
4. CMOS Reset
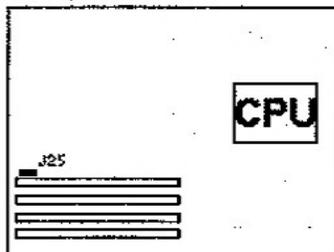
J25: Normal - Open
      Clear   - Close (briefly)

5. ECP DMA Channel Selector
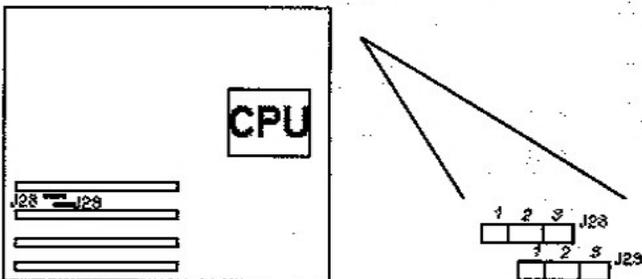
DMA Channel	J28	J29
1	2-3	2-3
3	1-2	1-2

6. Memory Configuration

The SIMM Module can be installed into Socket 1 and 3 or/and Socket 2 and 4.

RAM Size	Socket 1 (J1)	Socket 2 (J2)	Socket 3 (J3)	Socket 4 (J5)
8MB	4MB	×	4MB	×
	×	4MB	×	4MB
16MB	4MB	4MB	4MB	4MB
	8MB	×	8MB	×
	×	8MB	×	8MB
24MB	4MB	8MB	4MB	8MB
	8MB	4MB	8MB	4MB
32MB	16MB	×	16MB	×
	×	16MB	×	16MB
	8MB	8MB	8MB	8MB
40MB	4MB	16MB	4MB	16MB
	16MB	4MB	16MB	4MB
48MB	8MB	16MB	8MB	16MB
	16MB	8MB	16MB	8MB
64MB	32MB	×	32MB	×
	×	32MB	×	32MB
	16MB	16MB	16MB	16MB
72MB	4MB	32MB	4MB	32MB
	32MB	4MB	32MB	4MB
80MB	8MB	32MB	8MB	32MB
	32MB	8MB	32MB	8MB
96MB	16MB	32MB	16MB	32MB
	32MB	16MB	32MB	16MB
128MB	32MB	32MB	32MB	32MB

Note: × - not used

7. Monochrome/Color Display Selector

Display	J15
COLOR	Close (Default)
MONO	OPEN

8. Flash EPROM Read/Write Selector

	J18
Normal Operation	1-2 (Default)
Programming Enabled	2-3

Set the jumper 2-3 before programming the Flash EPROM. When you finish updating, set the J18 back to the normal operation.

III. Connector Configuration

1. Power Connector (JP1)

Pin Number	Function
1	Power Good
2	+5V
3	+12V
4	-12V
5	GND
6	GND
7	GND
8	GND
9	-5V
10	+5V
11	+5V
12	+5V

2. Keyboard Connector (J4)

Pin Number	Function
1	Clock
2	Data
3	NC
4	Ground
5	+5V

3. Reset Switch Connector (JP10)

Setting	Function
Close	Reset the System
Open (Default)	Normal

4. Keylock Connector (JP9)

Pin Number	Function
1	LED (+)
2	NC
3	LED (-)
4	KeyLock Input
5	GND

5. Speaker Connector (JP7)

Pin Number	Function
1	+5V
2	+5V
3	Speaker Output
4	Speaker Output

6. Hard Disk LED Connector (JP8)

Pin Number	Function
1	LED (+)
2	LED (-)

7. Turbo LED Connector (JP6)

Pin Number	Function
1	LED (+)
2	LED (-)

8. Board Diagram