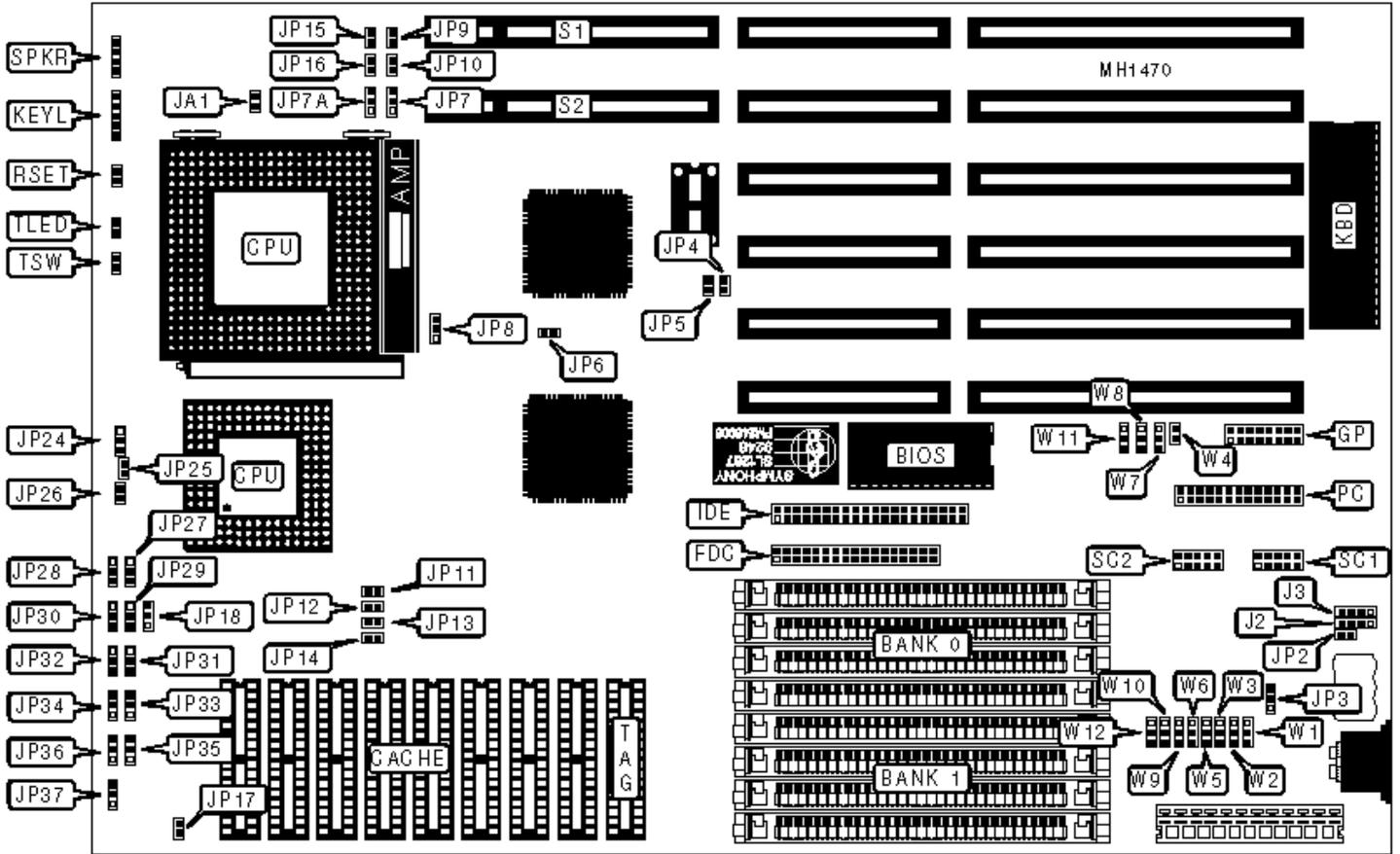


# UNIDENTIFIED

## VEGA VS3486AIO-2VL

### Configuration



## CONNECTIONS

Purpose	Location	Purpose	Location
Floppy drive interface	FDC	32-bit VESA local bus slot	S1
Game port	GP	32-bit VESA local bus slot	S2
IDE interface	IDE	Serial port 1	SC1
External battery	J2	Serial port 2	SC2
Power LED & keylock	KEYL	Speaker	SPKR
Parallel port	PC	Turbo LED	TLED
Reset switch	RSET	Turbo switch	TSW

Note: A DS146818A must be installed and the DS1287 removed, to use an external battery.

## USER CONFIGURABLE SETTINGS

Function	Jumper	Position
» CMOS memory normal operation	J3	pins 1 & 2 closed
CMOS memory clear	J3	pins 3 & 4 closed
» Battery select internal	JP2	Closed
Battery select external	JP2	Open
BIOS type select regular (27C512)	JP3	pins 1 & 2 closed
BIOS type select flash (28F001BX)	JP3	pins 2 & 3 closed
» Factory configured - do not alter	JP32	pins 2 & 3 closed
Parallel port (PC) select IRQ5	W7	pins 1 & 2 closed
Parallel port (PC) select IRQ7	W7	pins 2 & 3 closed
» Serial port 2 (SC2) select IRQ3	W8	pins 1 & 2 closed
Serial port 2 (SC2) select IRQ4	W8	pins 2 & 3 closed
» Serial port 1 (SC1) select IRQ3	W11	pins 1 & 2 closed
Serial port 1 (SC1) select IRQ4	W11	pins 2 & 3 closed

»	Floppy drive enabled	W12	pins 1 & 2 closed
	Floppy drive disabled	W12	pins 2 & 3 closed
Note: J3 & JP2 will not function unless the DS1287 RTC is replaced with a DSA146818A RTC.			

<b>SERIAL PORT 1 (SC1) CONFIGURATION</b>			
COM	I/O Address	W1	W2
COM1	3F8h	pins 1 & 2 closed	pins 1 & 2 closed
COM2	2F8h	pins 1 & 2 closed	pins 2 & 3 closed
COM3	3E8h	pins 2 & 3 closed	pins 1 & 2 closed
Disabled	N/A	pins 2 & 3 closed	pins 2 & 3 closed

<b>SERIAL PORT 2 (SC2) CONFIGURATION</b>			
COM	I/O Address	W3	W5
COM1	3F8h	pins 1 & 2 closed	pins 1 & 2 closed
COM2	2F8h	pins 1 & 2 closed	pins 2 & 3 closed
COM4	2E8h	pins 2 & 3 closed	pins 1 & 2 closed
Disabled	N/A	pins 2 & 3 closed	pins 2 & 3 closed

<b>PARALLEL PORT (PC) CONFIGURATION</b>			
LPT	I/O Address	W6	W9
LPT1	378h	pins 1 & 2 closed	pins 1 & 2 closed
LPT2	278h	pins 1 & 2 closed	pins 2 & 3 closed
LPT3	3BCh	pins 2 & 3 closed	pins 1 & 2 closed
Disabled	N/A	pins 2 & 3 closed	pins 2 & 3 closed

<b>IDE INTERFACE CONFIGURATION</b>
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Mode	JA1	W4	W10
Enabled	Closed	Closed	pins 1 & 2 closed
Disabled	Open	Open	pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 256K x 9	(4) 4M x 9
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Max Cachable	Cache	TAG
64KB	8MB	(8) 8K x 8	(1) 8K x 8
256KB	32MB	(8) 32K x 8	(1) 32K x 8
1MB	64MB	(8) 128K x 8	(1) 128K x 8

CACHE JUMPER CONFIGURATION					
Size	JP27	JP28	JP29	JP30	JP31
64KB	pins 1 & 2				
256KB	pins 2 & 3	pins 1 & 2	pins 1 & 2	pins 2 & 3	pins 2 & 3

1024KB	pins 2 & 3				
Note:Pins designated should be in the closed position.					

<b>CPU TYPE CONFIGURATION</b>			
Jumper	80386DX	80486SX/80487SX	80486DX/DX2/ODP
JP6	Open	Closed	Closed
JP17	Closed	Open	Open
JP18	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed
JP24	N/A	pins 2 & 3 closed	pins 1 & 2 closed
JP25	N/A	Open	Closed
JP26	N/A	Open	Closed
JP33	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed
JP34	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed
JP35	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed
JP36	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed
JP37	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed

<b>CPU SPEED CONFIGURATION</b>					
Jumper	20MHz	25/50I MHz	33/66I MHz	40MHz	50MHz
JP4	Closed	Open	Closed	Open	Open
JP5	Closed	Closed	Open	Open	Closed
JP7	pins 1 & 2	pins 2 & 3			
JP7A	pins 1 & 2	pins 2 & 3			
JP8	pins 2 & 3	pins 1 & 2			
JP11	Open	Open	Open	Open	Closed
JP12	Open	Open	Open	Open	Closed

JP13	Open	Open	Open	Open	Closed
JP14	Open	Open	Open	Open	Closed
Note: Pins designated should be in the closed position.					

<b>VESA CPU TYPE (ID0 &amp; ID1) CONFIGURATION</b>		
CPU Type	JP9 (ID0)	JP10 (ID1)
80386	Closed	Open
80486	Open	Closed

<b>VESA WAIT STATE (ID2) CONFIGURATION</b>		
Wait states	CPU speed	JP16 (ID2)
0 wait states	£ 33MHz	Open
1 wait state	> 33MHz	Closed

<b>VESA BUS SPEED (ID3) CONFIGURATION</b>	
CPU speed	JP15 (ID3)
£ 33MHz	Open
> 33MHz	Closed