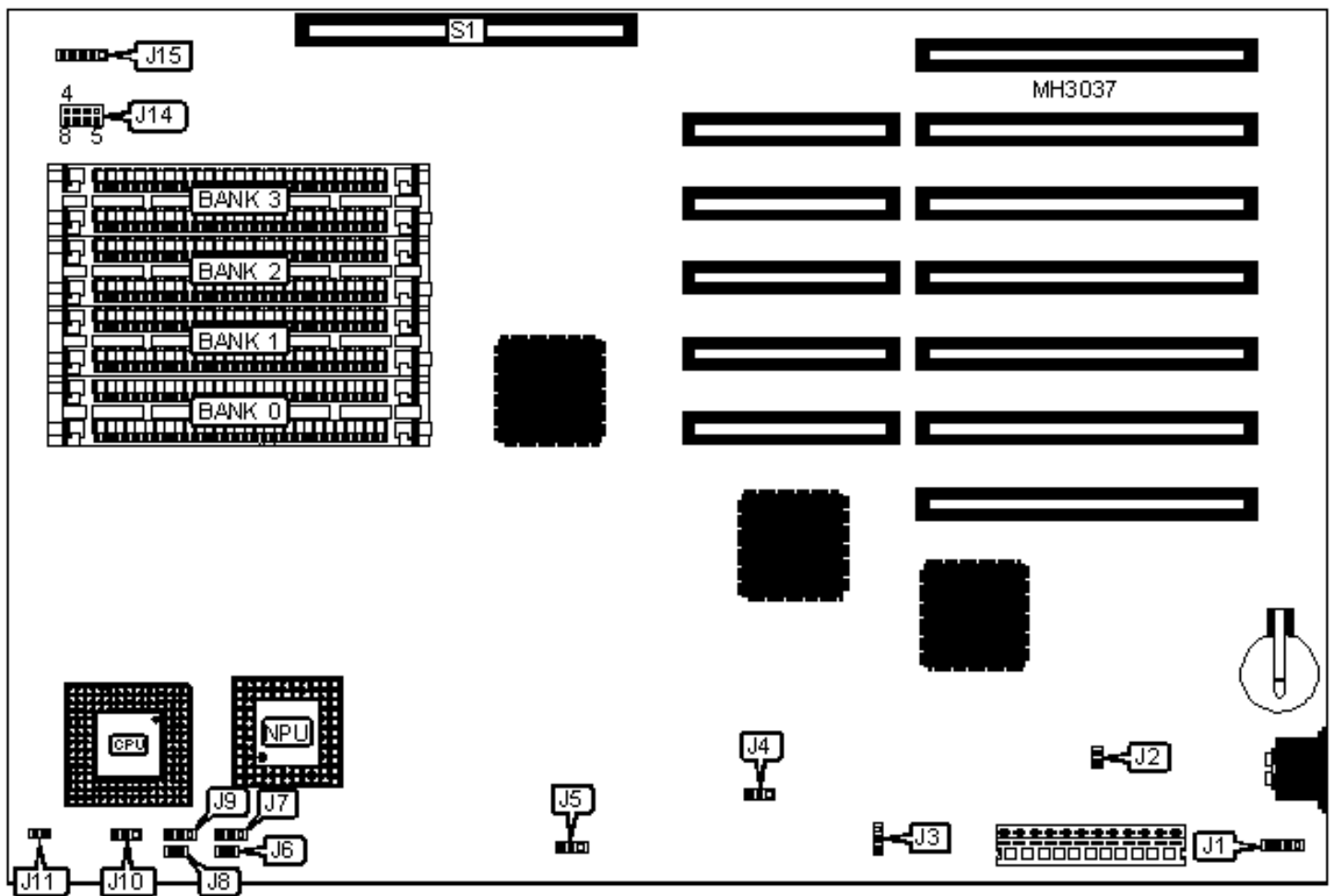


UNIDENTIFIED

386/25 CACHE

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
External battery	J1	Turbo switch	J14 (pins 7 & 8)
Speaker	J14 (pins 1 - 4)	Power LED & keylock	J15
Reset switch	J14 (pins 5 & 6)	External memory card	S1

USER CONFIGURABLE SETTINGS

Function		Jumper	Position
»	Battery type select external	J1	Closed
	Battery type select internal	J1	pins 2 & 3 closed
	Monitor type select color	J2	Closed
	Monitor type select monochrome	J2	Open
»	Factory configured - do not alter	J4	pins 2 & 3 closed
»	Factory configured - do not alter	J10	pins 1 & 2 closed
»	Factory configured - do not alter	J11	pins 1 & 2 closed

DRAM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2	Bank 3
512KB	(2) 256K x 9	NONE	NONE	NONE
1MB	(2) 256K x 9	(2) 256K x 9	NONE	NONE
1.5MB	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9	NONE
2MB	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9
2MB	(2) 1M x 9	NONE	NONE	NONE
2.5MB	(2) 256K x 9	(2) 1M x 9	NONE	NONE
2.5MB	(2) 1M x 9	(2) 256K x 9	NONE	NONE
3MB	(2) 256K x 9	(2) 256K x 9	(2) 1M x 9	NONE

3MB	(2) 256K x 9	(2) 1M x 9	(2) 256K x 9	NONE
3MB	(2) 1M x 9	(2) 256K x 9	(2) 256K x 9	NONE
3.5MB	(2) 256K x 9	(2) 256K x 9	(2) 1M x 9	(2) 256K x 9
3.5MB	(2) 256K x 9	(2) 1M x 9	(2) 256K x 9	(2) 256K x 9
3.5MB	(2) 1M x 9	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9
4MB	(2) 1M x 9	(2) 1M x 9	NONE	NONE
4.5MB	(2) 256K x 9	(2) 1M x 9	(2) 1M x 9	NONE
4.5MB	(2) 1M x 9	(2) 1M x 9	(2) 256K x 9	NONE
4.5MB	(2) 1M x 9	(2) 256K x 9	(2) 1M x 9	NONE
5MB	(2) 256K x 9	(2) 256K x 9	(2) 1M x 9	(2) 1M x 9
5MB	(2) 256K x 9	(2) 1M x 9	(2) 1M x 9	(2) 256K x 9
5MB	(2) 1M x 9	(2) 1M x 9	(2) 256K x 9	(2) 256K x 9
10MB	(2) 1M x 9	(2) 256K x 9	(2) 1M x 9	(2) 256K x 9
6.5MB	(2) 256K x 9	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9
6.5MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	(2) 256K x 9
6.5MB	(2) 1M x 9	(2) 256K x 9	(2) 1M x 9	(2) 1M x 9
8MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
32KB	(4) 8K x 8	NONE	(1) 8K x 8
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
Note: The location of CACHE is unidentified.			

CACHE JUMPER CONFIGURATION			
Size	J5	J8	J9

32KB	pins 1 & 2 closed	Open	pins 1 & 2 closed
64KB	pins 2 & 3 closed	Closed	pins 2 & 3 closed

CACHE MEMORY LINE CONFIGURATION		
Size	J6	J7
4 Bytes	Closed	pins 1 & 2 closed
8 Bytes	Closed	pins 2 & 3 closed
16 Bytes	Open	pins 2 & 3 closed

BIOS ROM TYPE CONFIGURATION	
TYPE	J3
32Kb x 8	pins 2 & 3 closed
64KB x 8	pins 1 & 2 closed