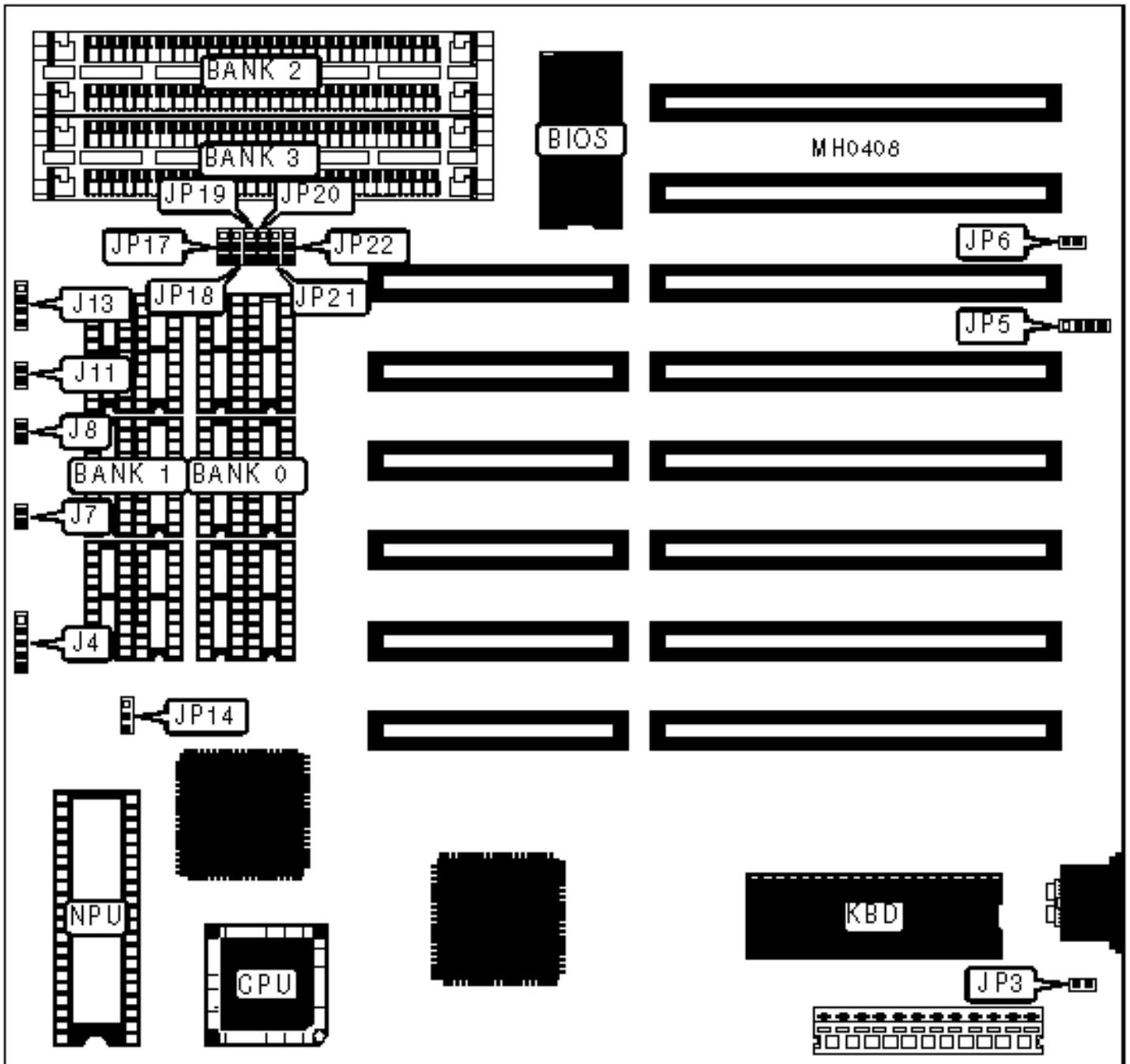


UNIDENTIFIED

MB286T 12/16

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
Power LED & keylock	J4	Turbo LED	J8
External battery	JP5	Reset switch	J11
Turbo switch	J7	Speaker	J13

USER CONFIGURABLE SETTINGS

Function	Jumper	Position
» Monitor type select color	JP3	closed
Monitor type select monochrome	JP3	open
» CMOS memory normal operation	JP6	closed
CMOS memory clear	JP6	open
» Factory configured - do not alter	JP14	unknown

Note: Location of JP3 is unverified.

BASE MEMORY SELECT

Banks	JP17,JP18,JP19,JP20,JP21 & JP22
Base memory : Banks 2 & 3	pins 1 & 2 closed
Base memory : Banks 0 & 1 (Expansion memory : Banks 2 & 3)	pins 2 & 3 closed

Note: Exact arrangement & size of DIPP sockets unverified.

DRAM CONFIGURATION

Size	DIP Bank 0	DIP Bank 1	SIMM Bank 2	SIMM Bank 3
512KB	(4) 44256 & (2) 41256	NONE	NONE	NONE
1MB	(4) 44256 & (2) 41256	(4) 44256 & (2) 41256	NONE	NONE
2MB	(4) 44256 & (2) 41256	(4) 44256 & (2) 41256	(2) 256K x 9	(2) 256K x 9

3MB	(4) 44256 & (2) 41256	(4) 44256 & (2) 41256	(2) 1M x 9	NONE
5MB	(4) 44256 & (2) 41256	(4) 44256 & (2) 41256	(2) 1M x 9	(2) 1M x 9
Note: This table for use when banks 0 & 1 are used for base memory and banks 2 & 3 are used for expansion memory.				

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
512KB	NONE	NONE	(2) 256K x 9	NONE
1MB	NONE	NONE	(2) 256K x 9	(2) 256K x 9
2MB	NONE	NONE	(2) 1M x 9	NONE
2.5MB	NONE	NONE	(2) 256K x 9	(2) 1M x 9
4MB	NONE	NONE	(2) 1M x 9	(2) 1M x 9
Note: This table for use when banks 2 & 3 are used for base memory only.				