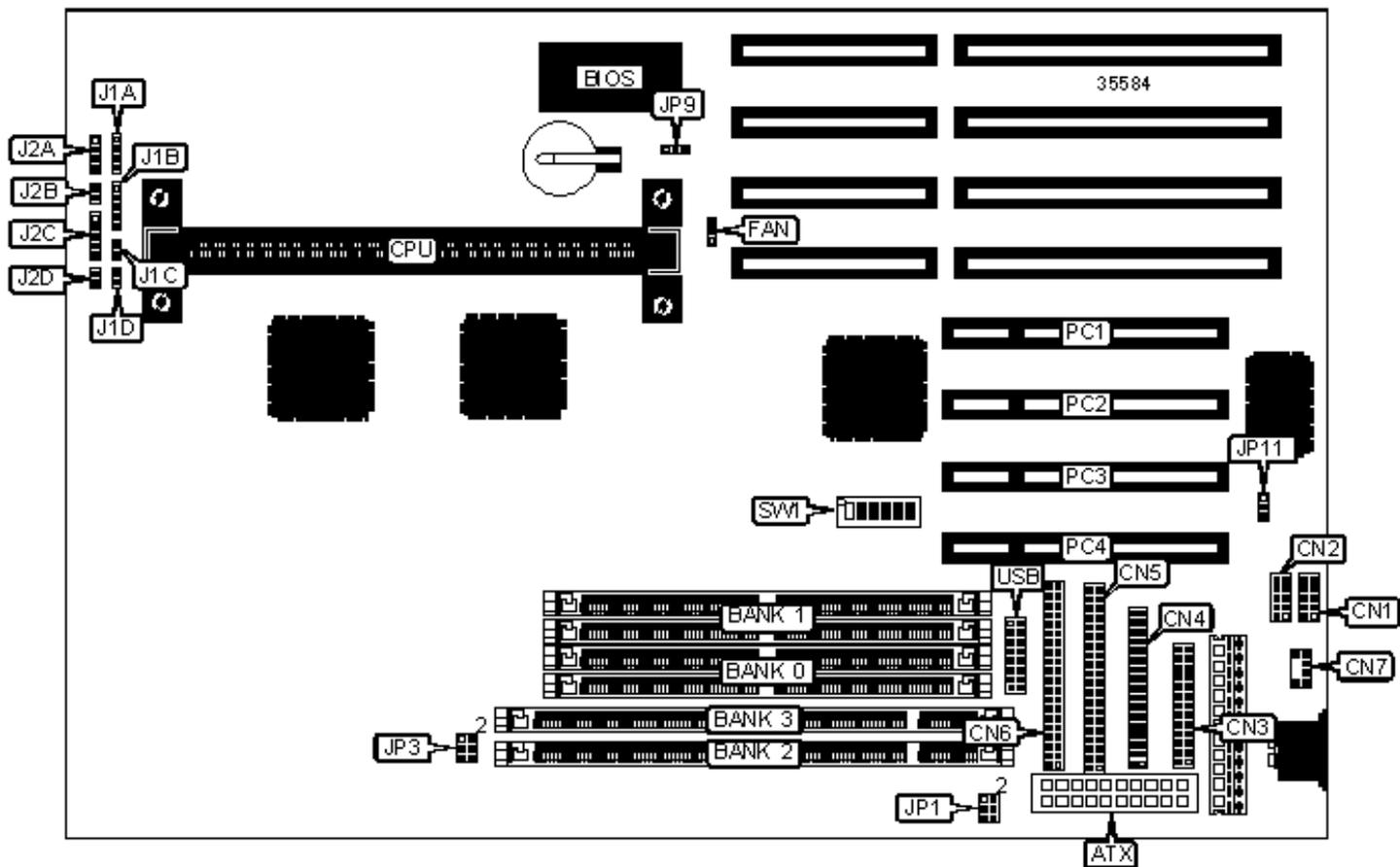


SOLTEK, INC.

SL-61A

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
ATX power connector	ATX	IR connector	J1B
Serial port 2	CN1	Soft off power supply	J1C
Serial port 1	CN2	Green PC connector	J1D
Parallel port	CN3	Speaker	J2A
Floppy drive interface	CN4	Reset switch	J2B
IDE interface 1	CN5	Power LED & keylock	J2C
IDE interface 2	CN6	Turbo LED	J2D
PS/2 mouse interface	CN7	32-bit PCI slots	PC1 – PC4
Chassis fan power	FAN	USB connector	USB
IDE interface LED	J1A		

USER CONFIGURABLE SETTINGS

Function	Label	Position
» Factory configured - do not alter	JP2	Closed
» Factory configured - do not alter	JP6	Open
» Factory configured - do not alter	JP7	Closed
» Factory configured - do not alter	JP8	Open
» CMOS memory normal operation	JP9	Pins 1 & 2 closed
» CMOS memory clear	JP9	Pins 2 & 3 closed
» Power supply select AT	JP11	Pins 1 & 2 closed
» Power supply select ATX	JP11	Pins 2 & 3 closed

Note: The location of the factory configured jumpers are unidentified.

DIMM/ SIMM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2	Bank 3
8MB	(2) 1M x 36	None	None	None
8MB	None	None	(1) 1M x 64	None
16MB	(2) 2M x 36	None	None	None
16MB	None	None	(1) 2M x 64	None
16MB	(2) 1M x 36	(2) 1M x 36	None	None
16MB	(2) 1M x 36	None	None	(1) 1M x 64
16MB	None	None	(1) 1M x 64	(1) 1M x 64
24MB	None	(2) 2M x 36	(1) 1M x 64	None
32MB	(2) 4M x 36	None	None	None
32MB	None	None	(1) 4M x 64	None
32MB	(2) 2M x 36	None	None	(1) 2M x 64
32MB	None	(2) 2M x 36	(1) 2M x 64	None
40MB	(2) 4M x 36	(2) 1M x 36	None	None
40MB	(2) 4M x 36	None	None	(1) 1M x 64
48MB	(2) 2M x 36	None	None	(1) 4M x 64
48MB	(2) 4M x 36	(2) 2M x 36	None	None

DIMM/SIMM CONFIGURATION (CON'T)

Size	Bank 0	Bank 1	Bank 2	Bank 3
48MB	(2) 4M x 36	None	None	(1) 2M x 64
64MB	(2) 8M x 36	None	None	None
64MB	None	None	None	(1) 8M x 64
64MB	(2) 4M x 36	(2) 4M x 36	None	None
64MB	(2) 4M x 36	None	None	(1) 4M x 64
72MB	(2) 8M x 36	(2) 1M x 36	None	None

72MB	(2) 8M x 36	None	None	(1) 1M x 64
80MB	(2) 2M x 36	None	None	(1) 8M x 64
80MB	(2) 8M x 36	(2) 2M x 36	None	None
80MB	(2) 8M x 36	None	None	(1) 2M x 64
96MB	(2) 4M x 36	None	None	(1) 8M x 64
96MB	(2) 8M x 36	(2) 4M x 36	None	None
96MB	(2) 8M x 36	None	None	(1) 4M x 64
128MB	None	None	(1) 16M x 64	None
128MB	(2) 16M x 36	None	None	None
128MB	(2) 8M x 36	(2) 8M x 36	None	None
128MB	(2) 8M x 36	None	None	(1) 8M x 64
136MB	None	None	(1) 16M x 64	(1) 1M x 64
136MB	(2) 16M x 36	(2) 1M x 36	None	None
144MB	None	None	(1) 16M x 64	(1) 2M x 64
144MB	(2) 16M x 36	(2) 2M x 36	None	None
160MB	None	None	(1) 16M x 64	(1) 4M x 64
160MB	(2) 16M x 36	(2) 4M x 36	None	None
192MB	None	None	(1) 16M x 64	(1) 8M x 64
192MB	(2) 16M x 36	(2) 8M x 36	None	None
256MB	None	None	(1) 16M x 64	(1) 16M x 64
256MB	(2) 16M x 36	(2) 16M x 36	None	None
288MB	(2) 16M x 36	(2) 16M x 36	(1) 4M x 64	None
320MB	(2) 16M x 36	(2) 16M x 36	(1) 8M x 64	None
384MB	(2) 16M x 36	(2) 16M x 36	(1) 16M x 64	None
Note: Board accepts EDO memory.				

DIMM VOLTAGE CONFIGURATION

Voltage		JP1	JP3
»	3.3v	Open	Pins 1 & 2, 3 & 4, 5 & 6 closed
	5v	Pins 1 & 2, 3 & 4, 5 & 6 closed	Open

CACHE CONFIGURATION

Note: 256KB/512KB cache is located on the Pentium II CPU.

CPU SPEED SELECTION

CPU speed	Clock speed	Multiplier	SW1/1	SW1/2	SW1/3
233MHz	66MHz	3.5x	On	Off	Off
266MHz	66MHz	4x	On	Off	Off

CPU SPEED SELECTION (CON'T)

CPU speed	Clock speed	Multiplier	SW1/4	SW1/5	SW1/6
233MHz	66MHz	3.5x	On	Off	Off
266MHz	66MHz	4x	Off	On	On