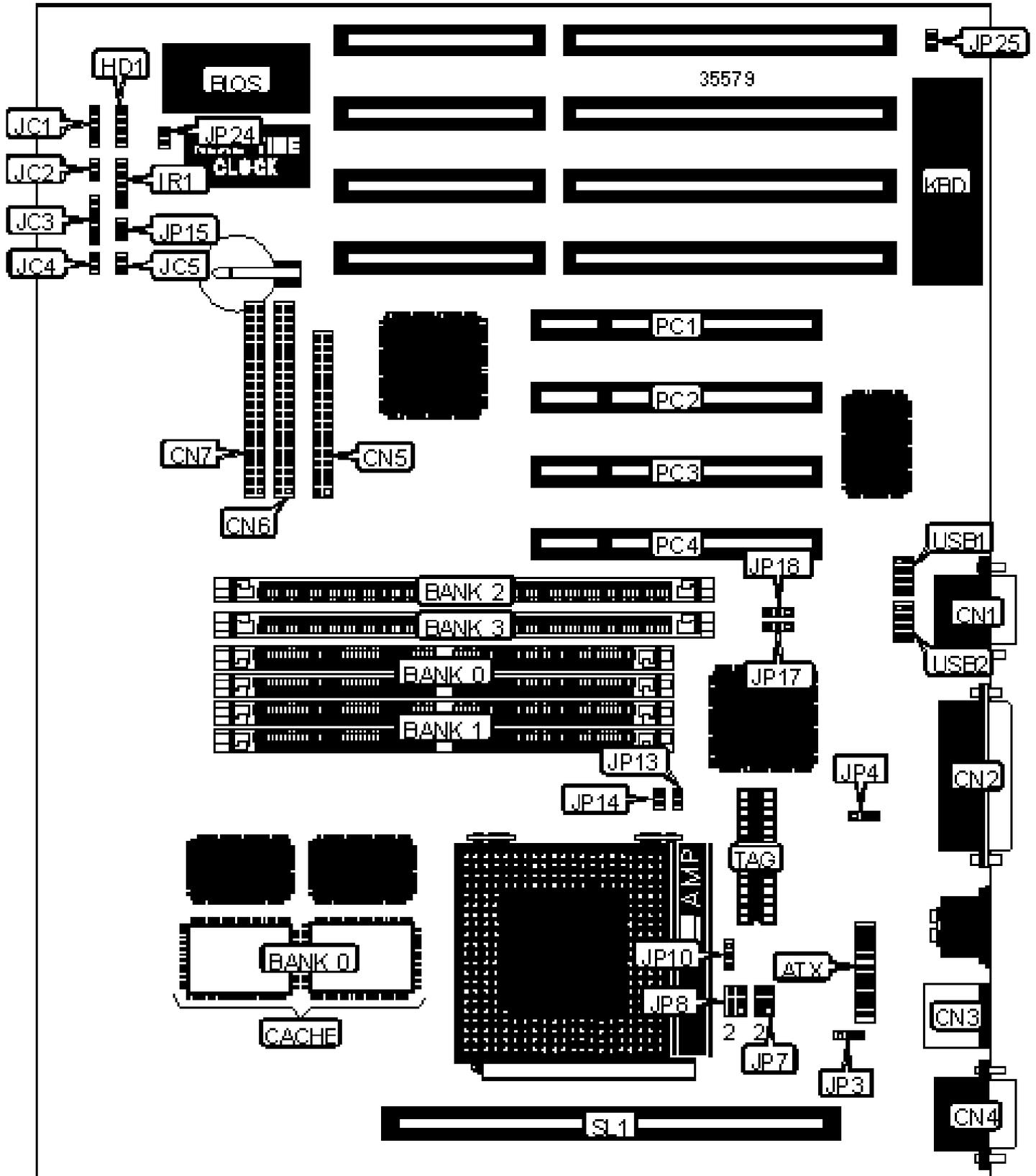


SOLTEK COMPUTER, INC.

SL-55A2, SL-55A5

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
ATX power connector	ATX	Speaker	JC1
Serial port 2	CN1	Reset switch	JC2
Parallel port	CN2	Power LED & keylock	JC3
PS/2 mouse port	CN3	Turbo LED	JC4
Serial port 1	CN4	Green PC connector	JC5
Floppy drive interface	CN5	Soft on power connector	JP15
IDE interface 2	CN6	32-bit PCI slots	PC1 - PC4
IDE interface 1	CN7	Cache slot	SL1
IDE interface LED	HD1	USB connector 1	USB1
IR connector	IR1	USB connector 2	USB2

USER CONFIGURABLE SETTINGS

Function	Label	Position
» Factory configured - do not alter	JP5	Closed
» CMOS memory normal operation	JP24	Open
CMOS memory clear	JP24	Closed
» Monitor type select color	JP25	Closed
Monitor type select monochrome	JP25	Open

Note: The location of JP5 is unidentified.

DRAM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2	Bank 3
8MB	None	None	(1) 1M x 64	None
8MB	(2) 1M x 36	None	None	None

16MB	None	None	(1) 2M x 64	None
16MB	None	None	(1) 1M x 64	(1) 1M x 64
16MB	(2) 2M x 36	None	None	None
16MB	(2) 1M x 36	(2) 1M x 36	None	None
24MB	None	None	(1) 2M x 64	(1) 1M x 64
24MB	(2) 1M x 36	(2) 2M x 36	None	None
32MB	None	None	(1) 4M x 64	None
32MB	None	None	(1) 2M x 64	(1) 2M x 64
32MB	(2) 4M x 36	None	None	None
32MB	(2) 2M x 36	(2) 2M x 36	None	None
40MB	None	None	(1) 4M x 64	(1) 1M x 64
40MB	(2) 4M x 36	(2) 1M x 36	None	None
48MB	None	None	(1) 4M x 64	(1) 2M x 64
48MB	(2) 4M x 36	(2) 2M x 36	None	None
64MB	None	None	(1) 8M x 64	None
64MB	None	None	(1) 4M x 64	(1) 4M x 64
64MB	(2) 8M x 36	None	None	None
64MB	(2) 4M x 36	(2) 4M x 36	None	None

DRAM CONFIGURATION (CON'T)

Size	Bank 0	Bank 1	Bank 2	Bank 3
72MB	None	None	(1) 8M x 64	(1) 1M x 64
72MB	(2) 8M x 36	(2) 1M x 36	None	None
80MB	None	None	(1) 8M x 64	(1) 2M x 64
80MB	(2) 8M x 36	(2) 2M x 36	None	None
96MB	None	None	(1) 8M x 64	(1) 4M x 64

96MB	(2) 8M x 36	(2) 4M x 36	None	None
128MB	None	None	(1) 8M x 64	(1) 8M x 64
128MB	(2) 8M x 36	(2) 8M x 36	None	None

Note: Board accepts EDO memory. Banks are 0 & 1, 2 & 3 are interchangeable.

CACHE CONFIGURATION

Size	Bank 0	SL1	TAG
256KB	(2) 32K x 32	Not installed	Unidentified
512KB	(2) 32K x 32	256KB module installed	Unidentified

CACHE JUMPER CONFIGURATION

Size	JP10
256KB	Pins 1 & 2 closed
512KB	Pins 2 & 3 closed

CPU SPEED SELECTION (CYRIX)

CPU speed	Clock speed	Multiplier	JP13	JP14	JP17	JP18
120MHz	50MHz	2x	Closed	Open	1 & 2	1 & 2
133MHz	55MHz	2x	Closed	Open	2 & 3	1 & 2
150MHz	60MHz	2x	Closed	Open	1 & 2	2 & 3
166MHz	66MHz	2x	Closed	Open	2 & 3	2 & 3

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (AMD)

CPU speed	Clock speed	Multiplier	JP13	JP14	JP17	JP18
75MHz	50MHz	1.5x	Open	Open	1 & 2	1 & 2

90MHz	55MHz	1.5x	Open	Open	2 & 3	1 & 2
90MHz	60MHz	1.5x	Open	Open	1 & 2	2 & 3
100MHz	50MHz	1.5x	Open	Open	1 & 2	1 & 2
100MHz	55MHz	1.5x	Open	Open	2 & 3	1 & 2
100MHz	66MHz	1.5x	Open	Open	2 & 3	2 & 3
120MHz	60MHz	2x	Open	Open	1 & 2	2 & 3
133MHz	66MHz	2x	Open	Open	2 & 3	2 & 3
150MHz	60MHz	2x	Closed	Open	1 & 2	2 & 3
166MHz	66MHz	2x	Closed	Open	2 & 3	2 & 3

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (INTEL)

CPU speed	Clock speed	Multiplier	JP13	JP14	JP17	JP18
75MHz	50MHz	1.5x	Open	Open	1 & 2	1 & 2
90MHz	60MHz	1.5x	Open	Open	1 & 2	2 & 3
100MHz	66MHz	1.5x	Open	Open	2 & 3	2 & 3
120MHz	60MHz	2x	Closed	Open	1 & 2	2 & 3
133MHz	66MHz	2x	Closed	Open	2 & 3	2 & 3
150MHz	60MHz	2.5x	Closed	Closed	1 & 2	2 & 3
166MHz	66MHz	2.5x	Closed	Closed	2 & 3	2 & 3
180MHz	60MHz	3x	Open	Closed	1 & 2	2 & 3
200MHz	66MHz	3x	Open	Closed	2 & 3	2 & 3

Note: Pins designated should be in the closed position.

CPU VOLTAGE SELECTION

Voltage	JP3	JP4	JP7	JP8
---------	-----	-----	-----	-----

2.5v (dual voltage)	1 & 2	1 & 2	1 & 3, 2 & 4	1 & 3, 2 & 4
2.8v (dual voltage)	2 & 3	1 & 2	1 & 3, 2 & 4	1 & 3, 2 & 4
3.38v (single voltage)	1 & 2	2 & 3	3 & 5, 4 & 6	3 & 5, 4 & 6
3.52v (single voltage)	2 & 3	2 & 3	3 & 5, 4 & 6	3 & 5, 4 & 6

Note: Pins designated should be in the closed position.