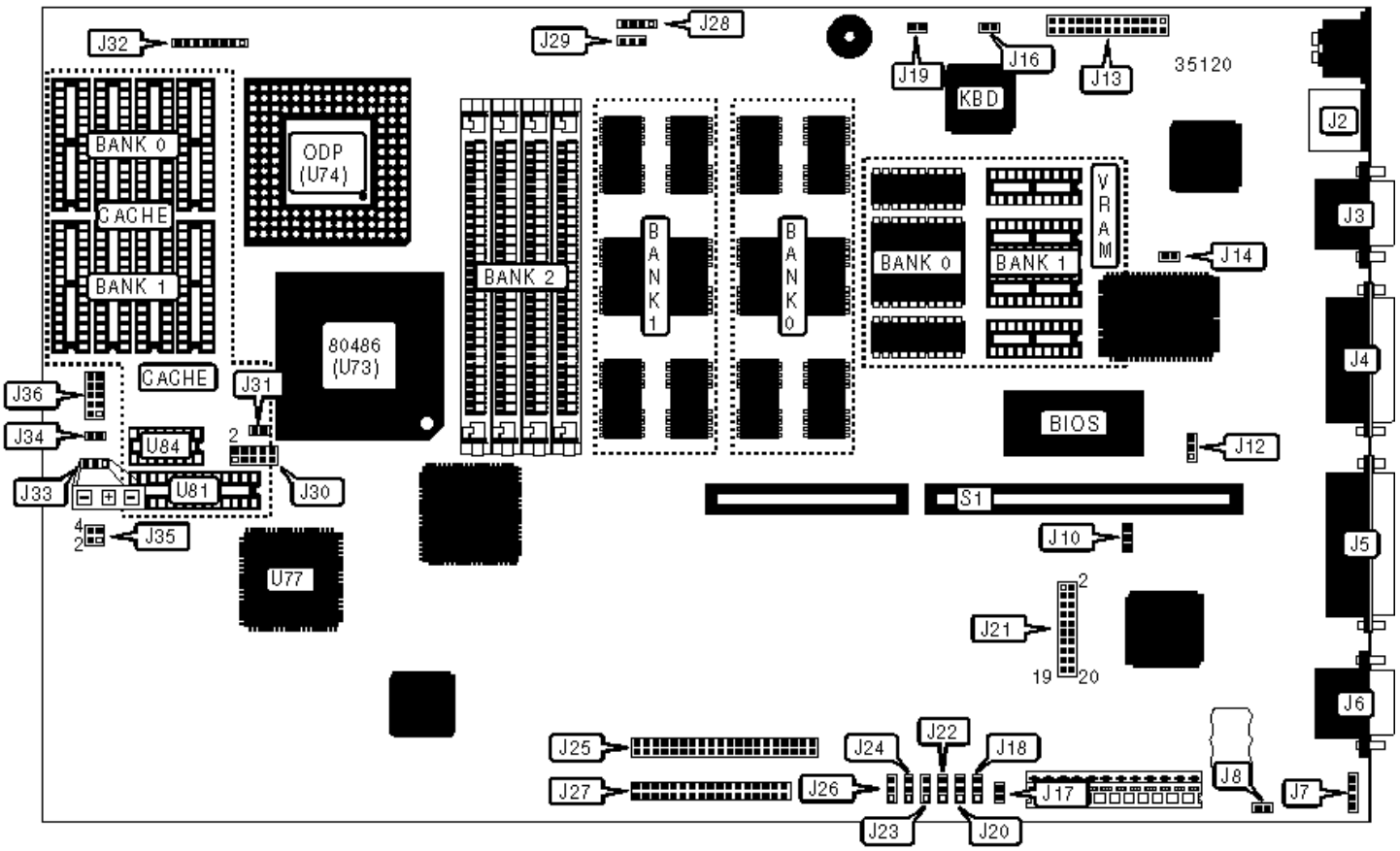
**PACKARD BELL**

**486SX**

**Configuration**



## CONNECTIONS

| Purpose | Location | Purpose | Location |
|---|---|---|---|
| PS/2 mouse | J2 | Fax/modem daughterboard | J21 |
| VGA port | J3 | IDE interface | J25 |
| Game port | J4 | Floppy drive interface | J27 |
| Parallel port | J5 | External speaker connector | J28 |
| Serial port B | J6 | Multi-function connector | J32 |
| External battery | J7 | CPU/chassis fan power (+12VDC) | J33 |
| VGA feature connector | J13 | Riser card | S1 |
| Keylock | J19 | | |

## USER CONFIGURABLE SETTINGS

| | Function | Jumper | Position |
|---|---|---|---|
| » | Internal battery enabled | J8 | Closed |
| | Internal battery disabled | J8 | Open |
| » | Onboard Game port enabled | J12 | Pins 2 & 3 closed |
| | Onboard Game port disabled | J12 | Pins 1 & 2 closed |
| » | Monitor type select color | J16 | Closed |
| | Monitor type select monochrome | J16 | Open |
| » | PS/2 mouse port enabled | J17 | Closed |
| | PS/2 mouse port disabled | J17 | Open |
| » | Onboard IDE interface enabled | J24 | Pins 1 & 2 closed |
| | Onboard IDE interface disabled | J24 | Pins 2 & 3 closed |
| » | Onboard floppy drive interface enabled | J26 | Pins 1 & 2 closed |
| | Onboard floppy drive interface disabled | J26 | Pins 2 & 3 closed |
| » | Onboard speaker enabled | J29 | Pins 1 & 2 closed |

| | | | |
|---|---|---|---|
| | Onboard speaker disabled | J29 | Pins 2 & 3 closed |

## DRAM CONFIGURATION

| Size | Bank 0 | Parity 0 | Bank 1 | Parity 1 | Bank 2 |
|---|---|---|---|---|---|
| 4MB | (2) 441000 | (1) 411000 | (2) 441000 | (1) 411000 | None |
| 5MB | (2) 441000 | (1) 411000 | (2) 441000 | (1) 411000 | (4) 256K x 9 |
| 8MB | (2) 441000 | (1) 411000 | (2) 441000 | (1) 411000 | (4) 1M x 9 |
| 20MB | (2) 441000 | (1) 411000 | (2) 441000 | (1) 411000 | (4) 4M x 9 |

Note:Bank 0 and 1 are soldered to the board. Locations of Parity 0 and 1 are unknown.

## VIDEO CONFIGURATION

| Mode | J10 | J14 |
|---|---|---|
| VGA port (J3) | Pins 1 & 2 closed | Closed |
| VGA feature connector (J13) | Pins 1 & 2 closed | Open |
| Disabled | Pins 2 & 3 closed | N/A |

## VRAM CONFIGURATION

| Size | Bank 0 | Bank 1 |
|---|---|---|
| 512KB | (4) 44256 | None |
| 1024KB | (4) 44256 | (4) 44256 |

Note:If Bank 1 sockets are omitted from board, board was factory installed with 1MB of VRAM. Bank 0 is soldered to the board.

## SERIAL PORT CONFIGURATION

| Modem (J21) | Port B (J5) | ISA Modem | J18 | J20 | J101/JMOD * |
|---|---|---|---|---|---|
| Disabled | COM2 | None | N/A | 1 & 2 | Closed |
| Disabled | COM2 | COM1 | 2 & 3 | 1 & 2 | Closed |

| | | | | | |
|---|---|---|---|---|---|
| Disabled | COM2 | COM3 | 1 & 2 | 1 & 2 | Closed |
| COM1 | COM2 | None | 1 & 2 | 1 & 2 | Open |
| COM3 | COM2 | None | 2 & 3 | 1 & 2 | Open |
| Disabled | COM4 | None | N/A | 2 & 3 | Closed |
| Disabled | COM4 | COM1 | 2 & 3 | 2 & 3 | Closed |
| Disabled | COM4 | COM3 | 1 & 2 | 2 & 3 | Closed |
| COM1 | COM4 | None | 1 & 2 | 2 & 3 | Open |
| COM3 | COM4 | None | 2 & 3 | 2 & 3 | Open |

, connecting pins 10 and 11 together diagonally. Permanent damage to the board will occur if the JMOD jumper is installed improperly. Boards with J101 do not use the JMOD jumper (J101 is a direct replacement for JMOD).

## PARALLEL PORT CONFIGURATION

| | LPT | IRQ | J22 | J23 |
|---|---|---|---|---|
| » | LPT1 | IRQ7 | Pins 1 & 2 closed | Pins 1 & 2 closed |
| | LPT1 | IRQ5 | Pins 1 & 2 closed | Pins 2 & 3 closed |
| | LPT2 | IRQ7 | Pins 2 & 3 closed | Pins 1 & 2 closed |
| | LPT2 | IRQ5 | Pins 2 & 3 closed | Pins 2 & 3 closed |

## CPU TYPE CONFIGURATION

| CPU | J30/1 & 2 | J30/3 & 4 | J30/5 & 6 | J30/7 & 8 | J30/9&10 | J31 | J34 |
|---|---|---|---|---|---|---|---|
| 80486SX | Open | Closed | Open | Open | Open | Open | Open |
| 80487SX | Closed | Open | Closed | Open | Closed | Open | Open |
| 80486DX | Closed | Open | Closed | Closed | Open | Open | Open |
| ODP486SX | Closed | Open | Closed | Closed | Open | Closed | Closed |
| 80486DX2 | Closed | Open | Closed | Closed | Open | Open | Closed |

## CPU SPEED CONFIGURATION

| Speed | J35/pins 1 & 2 | J35/pins 3 & 4 |
|---|---|---|
| 16MHz | Closed | Closed |
| 20MHz | Open | Closed |
| 25MHz | Closed | Open |
| 33MHz | Open | Open |

| CACHE CONFIGURATION | | | | |
|---|---|---|---|---|
| Size | Bank 0 | Bank 1 | TAG (U81) | Dirty Bit (U84) |
| 64KB | (4) 8K x 8 | (4) 8K x 8 | (1) 32K x 8 | (1) 64K x 1 |
| 128KB | (4) 32K x 8 | None | (1) 32K x 8 | (1) 64K x 1 |
| 256KB | (4) 32K x 8 | (4) 32K x 8 | (1) 32K x 8 | (1) 64K x 1 |

| CACHE JUMPER CONFIGURATION | | | | | | |
|---|---|---|---|---|---|---|
| Size | | J36/1 & 2 | J36/3 & 4 | J36/5 & 6 | J36/7 & 8 | J36/9 & 10 |
| » | Disabled | Open | Open | Open | Open | Open |
| | 64KB | Open | Open | Open | Open | Open |
| | 128KB | Closed | Open | Closed | Closed | Open |
| | 256KB | Closed | Closed | Closed | Open | Closed |