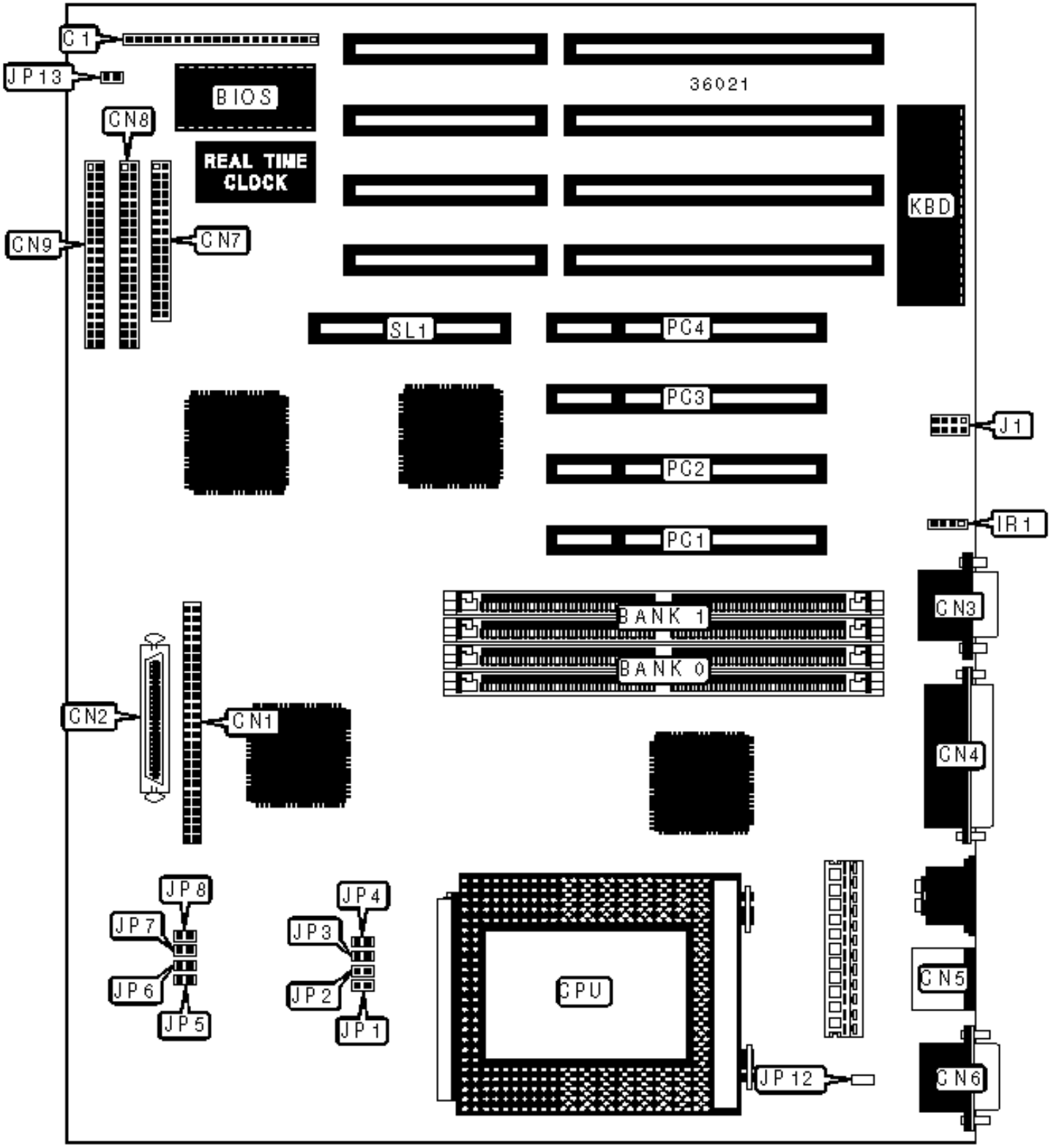
LUCKY STAR TECHNOLOGY CO., LTD.

6IX-K

Configuration



CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	C1/pins 1 – 5	PS/2 mouse port	CN5
Speaker	C1/pins 7 – 10	Serial port 1	CN6
Reset switch	C1/pins 12 & 13	IDE interface 1	CN7
IDE interface LED	C1/pins 15 & 16	IDE interface 2	CN8
Turbo LED	C1/pins 18 & 19	Floppy drive interface	CN9
Green PC connector	C1/pins 21 & 22	IR connector	IR1
SCSI-2 interface	CN1	USB connector	J1
SCSI-2 interface	CN2	32-bit PCI slots	PC1 – PC4
Serial port 2	CN3	RAID slot	SL1
Parallel port	CN4		

USER CONFIGURABLE SETTINGS			
Function		Label	Position
»	Factory configured - do not alter	JP5	Unidentified
»	Factory configured - do not alter	JP6	Unidentified
»	Factory configured - do not alter	JP12	Unidentified
»	CMOS memory normal operation	JP13	Open
	CMOS memory clear	JP13	Closed

SIMM CONFIGURATION		
Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36

24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36
128MB	(2) 16M x 36	None
136MB	(2) 16M x 36	(2) 1M x 36
144MB	(2) 16M x 36	(2) 2M x 36
160MB	(2) 16M x 36	(2) 4M x 36
192MB	(2) 16M x 36	(2) 8M x 36
256MB	(2) 16M x 36	(2) 16M x 36

CACHE CONFIGURATION

Note: 256KB/512KB cache is located on the Pentium Pro CPU.

CPU SPEED SELECTION

CPU speed	Clock speed	Multiplier	JP1	JP2	JP3	JP4	JP7	JP8
150MHz	60MHz	2.5x	Closed	Closed	Closed	Open	Closed	Open
166MHz	66MHz	2.5x	Closed	Closed	Closed	Open	Open	Closed
180MHz	60MHz	3x	Closed	Closed	Open	Closed	Closed	Open

200MHz	66MHz	3x	Closed	Closed	Open	Closed	Open	Closed