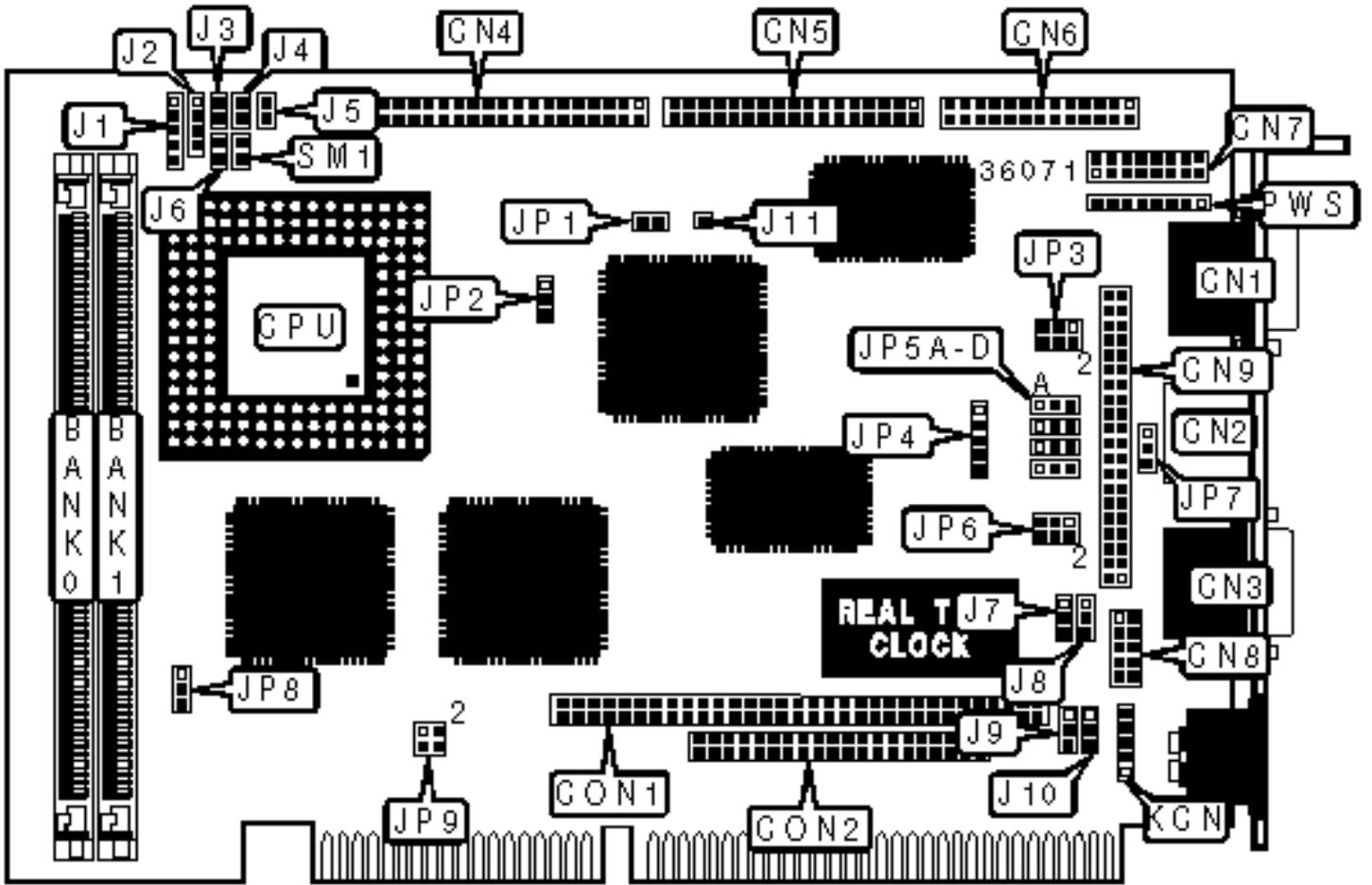


LANNER ELECTRONICS, INC.

AP-486LH

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
VGA port	CN1	Power LED & keylock	J1
PS/2 mouse port	CN2	Speaker	J2
Serial port 1	CN3	Reset switch	J3
IDE interface	CN4	Turbo LED	J4
Floppy drive interface	CN5	IDE interface LED	J5
Parallel port	CN6	Turbo switch	J6
Ethernet 10BaseT connector	CN7	Back light control connector	J11
Serial port 2	CN8	IR connector	JP4
LCD connector	CN9	Auxiliary keyboard connector	KCN
PC/104 connector	CON1	Power connector	PWS
PC/104 connector	CON2	Green PC connector	SMI

USER CONFIGURABLE SETTINGS

Function	Label	Position
» Factory configured - do not alter	JP1	Unidentified
» Factory configured - do not alter	JP2	Pins 1 & 2 closed
» Factory configured - do not alter	JP3	Unidentified
» Factory configured - do not alter	JP7	Unidentified
» Factory configured - do not alter	JP8	Pins 1 & 2 closed

SIMM CONFIGURATION

Size	Bank 0	Bank 1
1MB	(1) 256K x 36	None
2MB	(1) 512K x 36	None

2MB	(1) 256K x 36	(1) 256K x 36
3MB	(1) 512K x 36	(1) 256K x 36
4MB	(1) 1M x 36	None
4MB	(1) 512K x 36	(1) 512K x 36
5MB	(1) 1M x 36	(1) 256K x 36
6MB	(1) 1M x 36	(1) 512K x 36
8MB	(1) 2M x 36	None
8MB	(1) 1M x 36	(1) 1M x 36
9MB	(1) 2M x 36	(1) 256K x 36
10MB	(1) 2M x 36	(1) 512K x 36
12MB	(1) 2M x 36	(1) 1M x 36
16MB	(1) 4M x 36	None
16MB	(1) 2M x 36	(1) 2M x 36
17MB	(1) 4M x 36	(1) 256K x 36
18MB	(1) 4M x 36	(1) 512K x 36
20MB	(1) 4M x 36	(1) 1M x 36
24MB	(1) 4M x 36	(1) 2M x 36
32MB	(1) 8M x 36	None

SIMM CONFIGURATION (CON'T)

Size	Bank 0	Bank 1
32MB	(1) 4M x 36	(1) 4M x 36
33MB	(1) 8M x 36	(1) 256K x 36
34MB	(1) 8M x 36	(1) 512K x 36
36MB	(1) 8M x 36	(1) 1M x 36
40MB	(1) 8M x 36	(1) 2M x 36

48MB	(1) 8M x 36	(1) 4M x 36
64MB	(1) 8M x 36	(1) 8M x 36
64MB	(1) 16M x 36	None
68MB	(1) 16M x 36	(1) 1M x 36
72MB	(1) 16M x 36	(1) 2M x 36
80MB	(1) 16M x 36	(1) 4M x 36
96MB	(1) 16M x 36	(1) 8M x 36
128MB	(1) 16M x 36	(1) 16M x 36
Note: Board accepts EDO memory.		

CACHE CONFIGURATION

Note: The location of the cache is unidentified.

VIDEO MEMORY CONFIGURATION

Note: The location of the video memory is unidentified.

SERIAL PORT 2 SELECTION

Setting	J7	J8	J9	J10	JP6
» RS-232	2 & 3	2 & 3	2 & 3	2 & 3	1 & 2
RS-422	1 & 2	1 & 2	1 & 2	1 & 2	3 & 4
RS-485	1 & 2	1 & 2	1 & 2	1 & 2	5 & 6

Note: Pins designated should be in the closed position.

WATCHDOG TIME OUT SELECTION

Seconds	JP5A	JP5B	JP5C	JP5D
.5	Pins 1 & 2 closed	Open	Open	Open

	1	Open	Pins 1 & 2 closed	Open	Open
	2	Open	Open	Pins 1 & 2 closed	Open
»	4	Open	Open	Open	Pins 1 & 2 closed
	8	Open	Open	Open	Pins 2 & 3 closed
	16	Open	Open	Pins 2 & 3 closed	Open
	32	Open	Pins 2 & 3 closed	Open	Open
	64	Pins 2 & 3 closed	Open	Open	Open

DISK ON CHIP ADDRESS SELECTION

Address	JP9
D0000	Pins 1 & 2 closed
D8000	Pins 3 & 4 closed

PANEL RESOLUTION SELECTION

Type	JP3/pins 1 & 2	JP3/pins 3 & 4	JP3/pins 5 & 6
1024 x 768 DSTN	Closed	Closed	Closed
1280 x 1024 TFT	Open	Closed	Closed
640 x 480 DSTN	Closed	Open	Closed
800 x 600 DSTN	Open	Open	Closed
640 x 480 TFT	Closed	Closed	Open
» 640 x 480 TFT	Open	Closed	Open
1024 x 768 TFT	Closed	Open	Open
800 x 600 TFT	Open	Open	Open