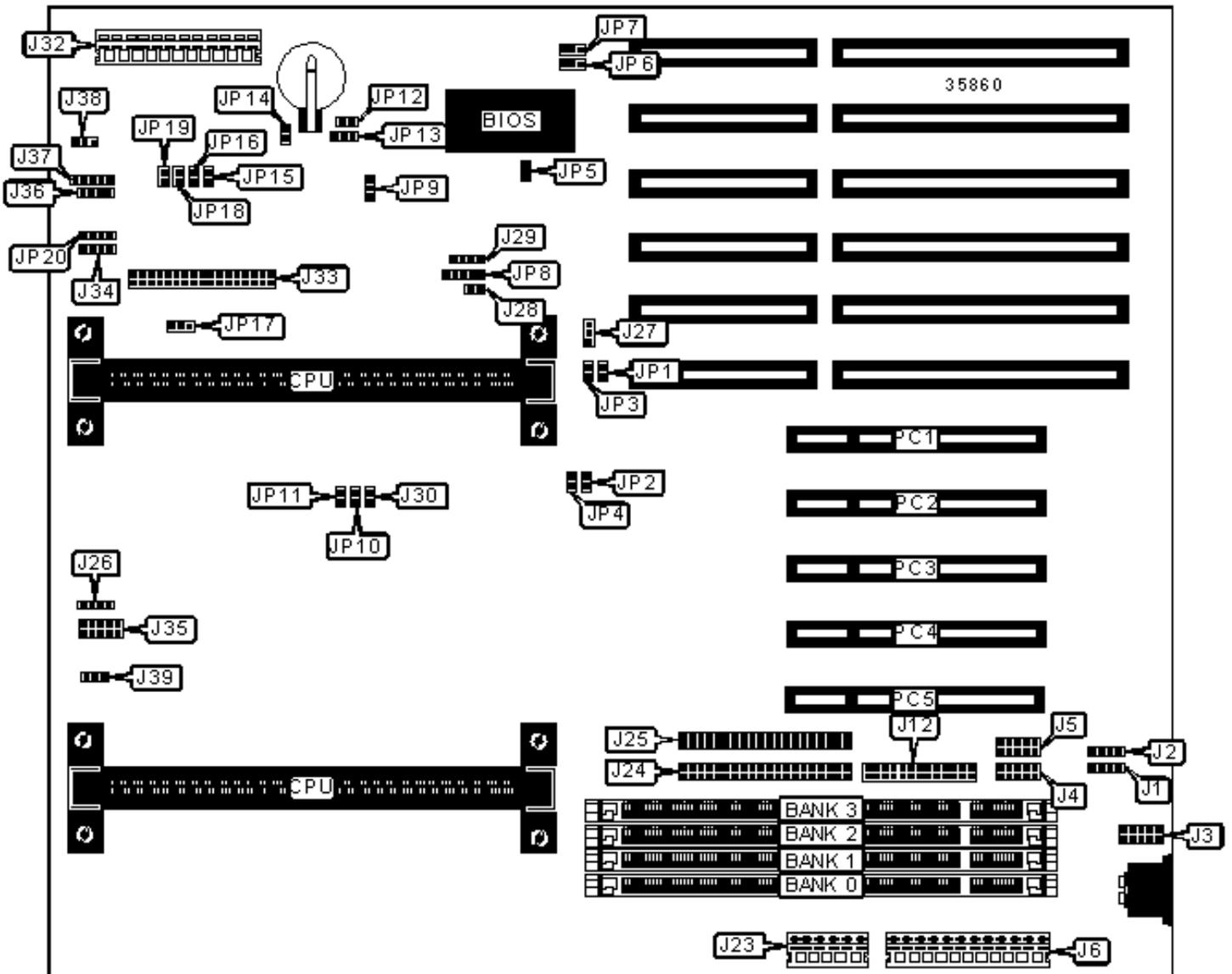


CSS LABORATORIES, INC.

MB-P202

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
USB connector 1	J1	5v auxiliary power	J32
USB connector 2	J2	Floppy drive interface	J33
PS/2 mouse interface	J3	IDE interface LED	J34
Serial port 1	J4	Front panel connector	J35
Serial port 2	J5	Reset switch	J36/pins 1 & 2
5v power	J6	Turbo LED	J36/pins 3 & 4
Parallel port	J12	Power LED & keylock	J37
3.3v power	J23	Chassis fan power	J38
IDE interface 1	J24	Chassis fan power	J39
IDE interface 2	J25	IR connector	JP8
Speaker	J26	IDE interface LED	JP20
Chassis fan power	J27	32-bit PCI slots	PC1 – PC5

USER CONFIGURABLE SETTINGS

Function	Label	Position
» Factory configured - do not alter	J28	Unidentified
» Factory configured - do not alter	J29	Unidentified
» EXTSMI select normal	JP2	Open
EXTSMI select forced sleep	JP2	Closed
» Factory configured - do not alter	JP4	Unidentified
BIOS type select default address	JP5	Closed
BIOS type select test address	JP5	Open
» BIOS SMI select BIOS controlled	JP9	Pins 2 & 3 closed
BIOS SMI select chipset controlled	JP9	Pins 1 & 2 closed

»	Factory configured - do not alter	JP12	Unidentified
»	ISA bus speed select /4	JP13	Pins 2 & 3 closed
	ISA bus speed select /3	JP13	Pins 1 & 2 closed
»	Factory configured - do not alter	JP14	Unidentified
»	Factory configured - do not alter	JP17	Unidentified

DIMM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2	Bank 3
32MB	(1) 4M x 64	None	None	None
32MB	(1) 2M x 64	(1) 2M x 64	None	None
32MB	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64
40MB	(1) 4M x 64	(1) 1M x 64	None	None
48MB	(1) 4M x 64	(1) 2M x 64	None	None
48MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64	None
64MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64
64MB	(1) 8M x 64	None	None	None
64MB	(1) 4M x 64	(1) 4M x 64	None	None
72MB	(1) 8M x 64	(1) 1M x 64	None	None
80MB	(1) 8M x 64	(1) 2M x 64	None	None
96MB	(1) 8M x 64	(1) 4M x 64	None	None
96MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64	None
128MB	(1) 16M x 64	None	None	None
128MB	(1) 8M x 64	(1) 8M x 64	None	None
128MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64
136MB	(1) 16M x 64	(1) 1M x 64	None	None
144MB	(1) 16M x 64	(1) 2M x 64	None	None

176MB	(1) 16M x 64	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64
192MB	(1) 16M x 64	(1) 8M x 64	None	None
192MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64	None
256MB	(1) 32M x 64	None	None	None
256MB	(1) 16M x 64	(1) 16M x 64	None	None
256MB	(1) 8M x 64			
272MB	(1) 16M x 64	(1) 16M x 64	(1) 1M x 64	(1) 1M x 64
280MB	(1) 32M x 64	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64
288MB	(1) 16M x 64	(1) 16M x 64	(1) 2M x 64	(1) 2M x 64
320MB	(1) 16M x 64	(1) 16M x 64	(1) 4M x 64	(1) 4M x 64
384MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64	None
448MB	(1) 32M x 64	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64
512MB	(1) 32M x 64	(1) 32M x 64	None	None
512MB	(1) 16M x 64			
640MB	(1) 32M x 64	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64
768MB	(1) 32M x 64	(1) 32M x 64	(1) 32M x 64	None
1024MB	(1) 32M x 64			

CACHE CONFIGURATION

Note: 256KB/512KB cache is located on the Pentium II CPU.

CPU SPEED SELECTION

CPU speed	Clock speed	Multiplier	JP1	JP3	JP10	JP11
233MHz	66MHz	3.5x	Closed	Open	Closed	Open
266MHz	66MHz	4x	Closed	Open	Closed	Open
300MHz	66MHz	4.5x	Closed	Open	Closed	Open

CPU SPEED SELECTION (CON'T)

CPU speed	Clock speed	Multiplier	JP15	JP16	JP18	JP19
233MHz	66MHz	3.5x	Open	Open	Closed	Closed
266MHz	66MHz	4x	Closed	Closed	Open	Closed
300MHz	66MHz	4.5x	Open	Closed	Open	Closed

CPU VOLTAGE SELECTION

Voltage		JP30
»	2.5v	Closed
	3v	Open

FLASH BIOS SELECTION

Setting		JP6	JP7
	EPROM	Pins 1 & 2 closed	Pins 2 & 3 closed
»	1MB flash	Pins 1 & 2 closed	Pins 1 & 2 closed
	2MB flash	Pins 2 & 3 closed	Pins 2 & 3 closed